

SHARP

PERSONAL COMPUTER

11%-80A

OWNER'S MANUAL



MACHINES SUPPLIED IN THE U.K. AND
REPUBLIC OF IRELAND HAVE 48K BYTE
RAM FITTED AS STANDARD

Personal Computer
MZ-80A

**Owner's
Manual**

080311-250182

NOTICE

This apparatus complies with requirements of EEC directive 76/889/EEC.

This manual is applicable to the SA-5510 BASIC interpreter used with the SHARP MZ-80A Personal Computer. The MZ-80A general-purpose personal computer is supported by system software which is filed in software packs (cassette tapes or diskettes).

All system software is subject to revision without prior notice, therefore, you are requested to pay special attention to file version numbers.

This manual has been carefully prepared and checked for completeness, accuracy and clarity. However, in the event that you should notice any errors or ambiguities, please feel free to contact your local Sharp representative for clarification.

All system software packs provided for the MZ-80A are original products, and all rights are reserved. No portion of any system software pack may be copied without approval of the Sharp Corporation.

Preface

This manual describes the Sharp MZ-80A personal computer. Read this manual thoroughly to become familiar with the operating procedures, BASIC language and precautions. This manual describes the MZ-80A and associated software.

Chapter 1 describes the features of the MZ-80A, general operating procedures—read these sections first, and language specifications and summary of the standard system software BASIC interpreter SA-5510. BASIC (an abbreviation for “Beginner’s All-purpose Symbolic Instruction Code”) was developed as an all purpose language to provide beginners with a means of easily programming computers to solve a diverse range of problems. Its simplicity and versatility make it well suited to personal programming applications. BASIC SA-5510 is an extended BASIC interpreter which enables the MZ-80A computer to be used to its fullest capacity.

Chapter 2 describes command and subroutines of the MONITOR SA-1510.

Chapter 3 describes the hardware. This information will be helpful to you if you intend to expand system.

Precautions

The MZ-80A is one of the finest personal computers in the world; its design incorporates all the technical knowledge accumulated by Sharp in its many years of experience in the electronics field. All units are thoroughly inspected prior to shipment so that each will operate normally when it is unpacked. However, be sure to check visually for any damage caused during transportation. If any damage is found or any parts are missing, contact your dealer immediately.

Observe the following guidelines to keep your set in optimum operating condition:

- Do not place the MZ-80A in locations where the temperature is extremely high or low or where it varies to a great extent. Avoid exposing the unit to direct sunlight, vibration or dust.
- Handle the power cable carefully to prevent it from being damaged. When removing it from the AC outlet, turn the power off first, then pull the plug (do not pull the cable).
- If the power switch is turned off then immediately turned on again, initialization may not be performed correctly. Allow a few moments after turning the power off before turning it on.
- The personal computer MZ-80A contains 32K byte RAM as standard equipment. When you use system software that requires the disk drive access (DISK BASIC, FDOS, etc.), it is necessary to expand the existing RAM area to 48K bytes.

For more detailed information, see Appendix 5.

—IMPORTANT—

For users in the United Kingdom:

The wires in the mains lead of this apparatus are coloured in accordance with the following code:

BLUE : Neutral

BROWN : Live

As the colours of the wires in the mains lead of this apparatus may not correspond with the coloured markings identifying the terminals in your plug, proceed as follows:

- The wire which is coloured BLUE must be connected to the terminal which is marked with the letter N or coloured BLACK.
- The wire which is coloured BROWN must be connected to the terminal which is marked with the letter L or coloured RED.

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Your MZ-80A and BASIC Programming

Chapter

1

1.1 Profile of the MZ-80A

You must know the configuration of a computer to construct programs which can actually run on it. The more you know about the console, memory, processors, peripheral environment, and language processing programs, the more efficient and elaborate your programs will be because you can create your programs taking full advantage of the computer facilities. You can, however, acquire detailed such detailed knowledge only by accumulating experience in designing and running programs on a computer yourself.

This first section presents a profile of the SHARP MZ-80A personal computer to allow you to grasp an outline of its hardware configuration and basic operating procedures. In the next section, we will take our first steps in computer programming.

■ Profile

The MZ-80A is an integrated personal computer which made its debut in the fall of 1981. It is a completely new multi-purpose small computer designed with a wide range of future hardware and software applications in mind. Its greatest features are its high speed and ease of operation. When it was introduced, the MZ-80A was widely acclaimed as a system which would open a new dimension in computer programming.

Figure 1.1 is a simplified illustration of the hardware configuration of the MZ-80A. It consists of a storage unit (which stores programs and data), a central processing unit (which performs operations on data as directed by the programs in the storage unit and transfers the data to and from the storage unit), and several input/output units. The storage unit is divided into main memory, monitor program, and video RAM sections. The MZ-80A has 32 K bytes of RAM (read/write memory) in its main memory section. The main memory section can be expanded to 48 K bytes by incorporating an additional 16 K bytes of RAM. The input units include a keyboard and a cassette tape unit. The output units include CRT display, cassette tape, and audio output units.

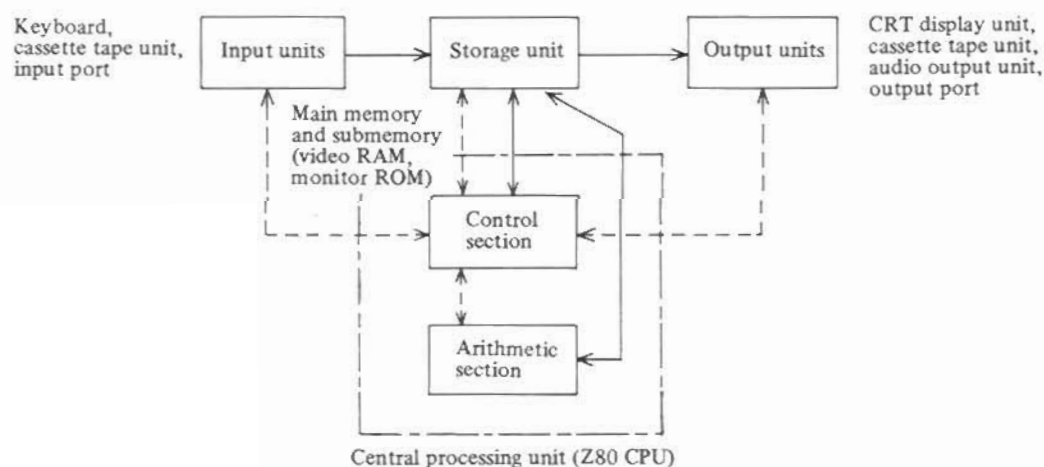


Figure 1.1 MZ-80A configuration

The central processing unit, which consists of control and arithmetic sections, performs active dynamically; it serves as the brain of the computer and controls its overall operation. Its operation, however, is made up of repetitions of the following simple operating sequence:

1. A data item containing an instruction is read from storage.
2. The instruction is executed.

In other words, logically speaking it is a collection of data items in the storage unit give instructions that cause the computer to operate in a dynamic manner. This collection of data items is called a program. It is, therefore, necessary to prepare a program to indicate the steps of a job and store it in the storage unit to cause the computer to perform the job.

Inside the computer, data and control signals are logically represented by binary numbers which are represented by the digits of 0 and 1. The number of digits of a binary number (i.e., a sequence of 0s and 1s) is counted in terms of bits. For example, the 8-bit binary number

0 0 1 1 0 1 0 1

is a data item which has a length of 8 bits (this is equivalent to 53 in decimal representation). Since bits are too small to be convenient for indicating the length of data, a unit called the "byte" is used to indicate a data item of 8 bits. One byte can represent up to 2^8 (= 256) different numbers.

The MZ-80A employs a Z80, a so-called 8-bit microprocessor (which process one byte of data at a time), as its central processing unit. Accordingly, programs which give instructions and data to be processed are all stored and transferred in byte units. Byte locations in the storage unit are designated by a 2-byte pointer in the central processing unit. With this 2-byte pointer, the Z80 can address up to 2^{16} (= 65536) locations. Since 2^{10} (= 1024) represents 1 K bytes, the Z80 is said to have an address space of 64 K bytes. As mentioned above, the MZ-80A main storage unit is made up of 48 K bytes, or 3/4 of the Z80 RAM (Random Access Memory) address space. RAM is a type of memory which can be freely read and written; on the other hand, ROM (Read Only Memory) can only be read.

The majority of special-purpose computers dedicated to automatic control systems and many personal computers have memories in which 1/3 to 1/2 or more of the memory space is composed of ROM for storage of control or system programs (e.g., BASIC interpreter programs). The use of RAM in the memory configuration of the MZ-80A is based on the premise that main memory should be freely available for a variety of uses. The MZ-80A stores all system programs in external files from which they are loaded into main memory by a monitor program.

The SA-5510 BASIC interpreter, one of the MZ-80A system programs, functions to translate BASIC source programs into machine code for execution.



The personal computer MZ-80A

1.2 Operating the MZ-80A

This section describes the constituent units of the MZ-80A and their functions.

■ Top view of the MZ-80A



Figure 1.2

■ Rear view of the MZ-80A

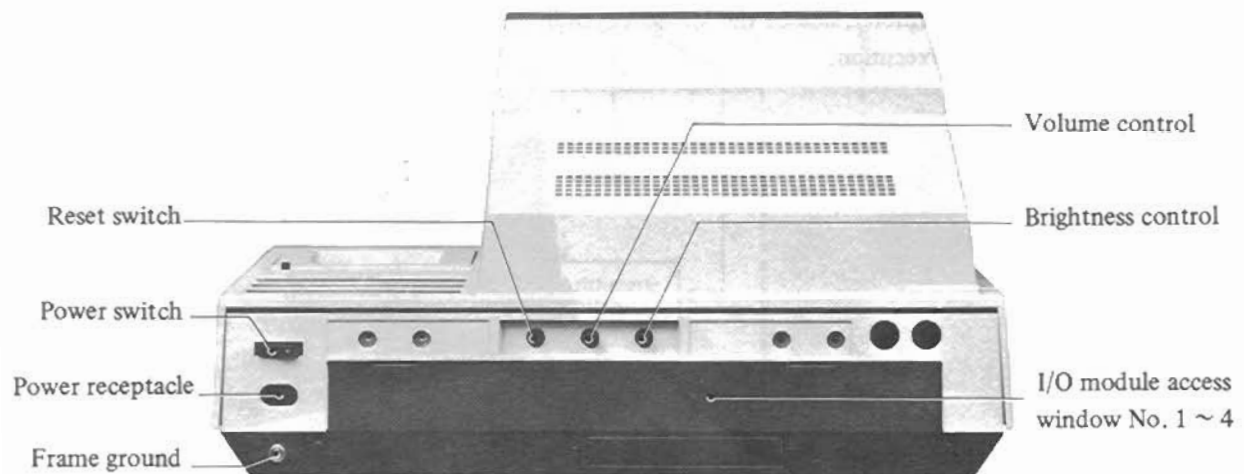


Figure 1.3

1.2.1 Activating system software

The MZ-80A personal computer is supported by system software which is filed in software packs.

BASIC SA-5510 is stored on a cassette tape file, and must undergo initial program loading whenever it is to be used. Loading is easily achieved.

First, turn on the power switch on the back of the MZ-80A. The Monitor program starts and the following message will be displayed on the CRT display.

```
* * MONITOR SA-1510 * *
* [X]
  ↑
  cursor flickers
```

Place the BASIC cassette file in the cassette tape deck and press the **[L]** key, then press the **[CR]** key. (L: Load)

The Monitor's program loader starts, and message "↓PLAY" is displayed. Press the **[PLAY]** button of the cassette tape deck.

The program loader loads the BASIC interpreter (photo at left of Figure 1.4), and upon completion of loading, the MZ-80A displays the message illustrated in the photo at right and the BASIC interpreter begins to operate.

The message "Ready" indicates that system control is at the BASIC command level and that the system is ready to accept any command.

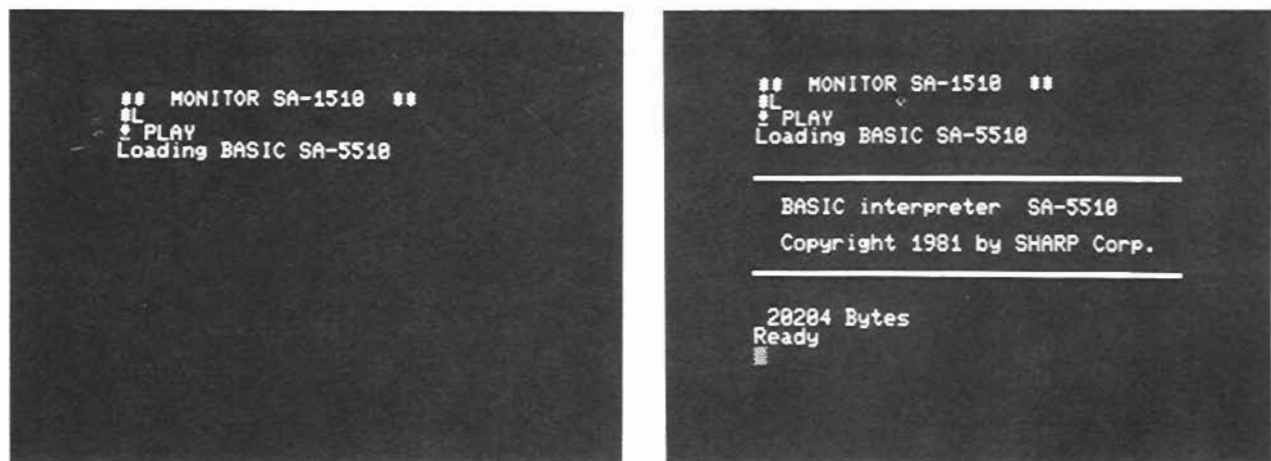


Figure 1.4

Please refer to the chapter 2 on activating system software from the diskette files and Monitor commands.

1.2.2 Keyboard

The keyboard of the MZ-80A is arranged as shown in Figure 1.5, and is divided into 2 areas; main keyboard and numeric pad.



Figure 1.5 The keyboard of the MZ-80A

The main keyboard (typewriter keyboard) conforms to ASCII standard and includes character keys and control keys (such as the carriage return key, the control key and the cursor control keys.)

The numeric pad is for entering numeric data and is similar to that of an ordinary electronic calculator.

The main keyboard has two operating modes;

- [1] Normal mode
- [2] Graphic mode

Keys provided on the main keyboard produce different characters according to operating mode, as shown in Figure 1.6.



Figure 1.6 Different characters of the [A] key

Note that the letter key normally produce capital letters. To enter lower case letters, hold down the [SHIFT] key then press the letter key—just opposite of an ordinary typewriter. The reason for this is that capital letters are generally easier to read on the screen, so most people prefer to write their programs in capital letters.

Figure 1.7 shows the control keys (the stippled keys).





Figure 1.7 Control keys



The functions of the control keys are explained below.





SHIFT : Similar to the shift key of an ordinary typewriter; when this key is depressed, the character keys and some control keys are shifted.

CR : Carriage return key. The **ENT** key has the same function as the **CR** key.

GRPH : If this key is pressed in the normal mode, the graphic mode is entered and the cursor pattern changes from “” to “”, and vice versa.

INST DEL : DEL erases the character at the left of the cursor location, shifting all following characters of the string to the left one space. INST inserts a space where the cursor is located by shifting all following characters of the string to the right one space.

CLR HOME : HOME returns the cursor to the upper left hand corner of the display screen. CLR clears the display screen and also returns the cursor to the screen's upper left hand corner.
In the graphic mode, HOME produces the reverse character “”, and CLR produces the reverse character “”.

CURSOR : Cursor control keys. Each key moves the cursor in the direction indicated by the arrow (normal position and shift position).
In the graphic mode, each key produces the reverse arrow;    .

BREAK CTRL : When this key is pressed with the **SHIFT** key depressed, a break code is generated, and halts execution of BASIC programs.

Figure 1.8 shows the **CTRL** key and some other keys (the stippled keys).



Figure 1.8 **CTRL** and some keys

The functions of these keys depressing the **CTRL** key are explained below.

- CTRL** + **A** : This locks the **SHIFT** key so that it does not need to be held down. Pressing these keys again or pressing the **CR** key releases the shift lock.
- CTRL** + **E** : This rolls down the listing of the CRT display.
- CTRL** + **D** : This rolls up the listing of the CRT display.
- CTRL** + **Z** : This generates the character "→". This character is used as a delimiter (PASCAL, FDOS, etc.)
- CTRL** + **@** : This sets the character display mode to reverse mode. Pressing these keys again sets the character display mode to normal mode.
- CTRL** + **|** : This sets the V-RAM configuration to the MZ-80K mode.
- CTRL** + **|** : This sets the V-RAM configuration to the MZ-80A mode.

1.3 BASIC Operations for Programming

Now let's start our study of BASIC programming. Here, our purpose is to allow the beginner to gain familiarity with the basic elements of programs. In the first section, we will construct very short programs to illustrate fundamental concepts and learn about basic operations which are required during the course of BASIC programming. That is, we will learn:

- 1 How to construct a program.
- 2 How to run a program.
- 3 How to correct a program.
- 4 How to store a program (on cassette tape).
- 5 How to run a program stored in an external file.

1 Constructing a program

To have a computer do a job, it must be given sequence of instructions according to which it is to work. Determining the sequence of instructions, implementing them as a BASIC program, entering the program into the MZ-80A from the keyboard, and correcting the program afterwards are operations which are fundamental to program development. The problem is given below is a simple example of work to be done on a computer.

Example 0: Read two numeric data items from the keyboard, compute their sum, and display the result.

The sequence of instructions is, as indicated in the problem, "read two numeric data items from the keyboard," "compute their sum," and "display the result." These instructions are written in BASIC as follows:

```

10 INPUT A
20 INPUT B      } Read two numeric data items from the keyboard.
30 LET C = A + B . . . . . Compute their sum.
40 PRINT C . . . . . Display the result.
50 END . . . . . End.
```

On the first two lines, variables A and B are assigned two numeric values through the INPUT statement, which has the function of receiving data from the keyboard. On the next line, the sum of A and B is assigned to variable C. The content of C is shown on the display unit through the PRINT statement on the next line, which has the function of displaying data on the CRT display unit. Then the program ends. Although we explain these steps as if they were a matter of course, they are far from self-explanatory. Thus, it is here that we will begin our study.

There are two points to keep in mind in the above problem:

- A BASIC program is written using words such as INPUT, LET, PRINT, END, etc. Lines containing these words are called INPUT statements, PRINT statements, and so forth.
- Each line begins with a number such as 10.

In other words, a BASIC program is made up of statements beginning with a set of words (called reserved words) or their abbreviations, and numbers (called line numbers) which precede the statements. Although the above program has only five lines, it is a complete program. In fact, a single line can constitute a program if it contains a line number and a statement. Large programs have the same program elements as such a single line program.

The next step is to enter their program into the computer from the keyboard. This is not hard to do; you can enter it in the same way you type on a typewriter. You must take note, however, of the following:

- All variable names and words such as INPUT and PRINT must be entered in upper case letters. The MZ-80A keyboard prints upper case letters in the normal mode and lower case letters in the shift mode, so you need not press the **SHIFT** key (as with a typewriter) when keying upper case letters.
- Each line must be terminated by pressing the **CR** key (or **ENT** key on the numeric key pad). A line of data keyed in is not stored in memory as a program line until the CR key is pressed.

Now, key in the first line.

1 0 I N P U T A **CR**

The cursor on the screen will move to the beginning of the next line when the **CR** key is pressed. Enter the second and third lines in succession. The entire program is stored in memory when the END statement on line number 50 is entered, followed by pressing the **CR** key.

Now key in:

L I S T **CR**

The listing of program input will appear on the screen. LIST is a command which displays the list of program lines stored in memory on the screen. It is called a command to distinguish it from statements (such as INPUT) which are used within the program.

2 Executing a program

To execute a program, give the RUN command to the computer. Key in:

R U N **CR**

A “?” mark will then appear on the next line and the cursor will flash. This means that the program execution has started and that the first INPUT statement is being executed. Key in, for example, the number 19 as the value of variable A. Entry of data during execution of the INPUT statement must also be terminated by pressing **CR**.

1 9 **CR**

It is convenient to use the **ENT** key, instead of the **CR** key, when entering numeric values from the numeric key pad.

The second INPUT statement is then executed and a “?” mark again appears on the screen. Key in “81” as the value of variable B.

8 1 **CR**

The computer, on receiving the variable B value, performs computation and assignment operations as directed on line number 30, then displays the result

1 0 0

on the screen as directed by the PRINT statement on line number 40. Thus, we obtain the result of adding 19 + 81. The computer ends program execution when it encounters the END statement on line number 50, displays

Ready

on the screen and causes the cursor to start flashing again. The “Ready” message indicates that the computer is in a mode, called the command mode, in which no program is executed and commands are awaited. In the command mode, you can enter commands such as LIST and RUN or modify the program.

3 Correcting a program

The procedure for correcting or modifying a program is basically the same as the procedure for creating one. For example, to modify the above program so that the result of $A - B$ is assigned to variable C and the content of C is displayed on the screen, it is necessary to key in

3 0 L E T C = A - B **CR**

When a line with the same line number as one of the old lines is entered, the old line is replaced by the new line.

A more convenient method, called screen editing, may be used when only portions of a line are to be changed, as in this example, where the plus sign is to be changed to a minus sign. With screen editing, all that is required is to move the cursor to the display position where a change is to be made using the cursor control keys and to overwrite the character(s). To terminate the editing session, press the **CR** key (The **CR** key may be pressed with the cursor in any position, as long as it is within a line). To insert or delete character(s), use the **INSERT/DEL** key. Run the program after modifying it.

4 Storing a program

The programs stored in the computer main memory are lost when power to the computer is turned off. You must learn how to store programs in external files in order to execute or complete them later, or exchange them with friends who use the MZ-80A.

The cassette tape unit in the MZ-80A is an input/output device which is used not only for starting the BASIC interpreter, but for recording and reading programs and data. To record a program onto cassette tape, use the following procedure:

- Load a cassette tape in the unit. When recording at the beginning of the tape, rewind it by pressing the **REW** button before proceeding to the next step.
- Enter a **SAVE** command together with an appropriate program name.

The **SAVE** command causes the program in the computer to be saved on the cassette tape.

Now, let's record the above program (changed to a subtraction program through screen editing) on cassette tape. Name the program "Subtraction." After mounting a cassette tape on the MZ-80A, key in:

S A V E " S u b t r a c t i o n " **CR**

Then,

↓RECORD . PLAY

will appear on the screen. Press the **RECORD** and **PLAY** keys simultaneously, and

Writing "Subtraction"

will appear on the screen, indicating that the save operation is in progress.

The prompt "Ready" will again appear on the screen when the program has been saved.

5 Reading a program from cassette tape

The **LOAD** command is used to read programs from cassette tape. To read the program "Subtraction," key in:

L O A D " S u b t r a c t i o n " **CR**

Figure 1.9 shows that, after the BASIC interpreter has been started, the program Subtraction has been read into the computer from the cassette tape by a **LOAD** command.

It also shows the messages Found and Loading, which are displayed in the course of program reading to indicate that the requested file has been found and that it is being read.

```

** MONITOR SA-1510 **
*
* PLAY
* Loading BASIC-SA-5510

-----
BASIC interpreter SA-5510
Copyright 1981 by SHARP Corp.

-----
32492 Bytes
Ready
LOAD "Subtraction"
* PLAY
Found "Subtraction"
Loading "Subtraction"
Ready

```

Figure 1.9 Loading a program

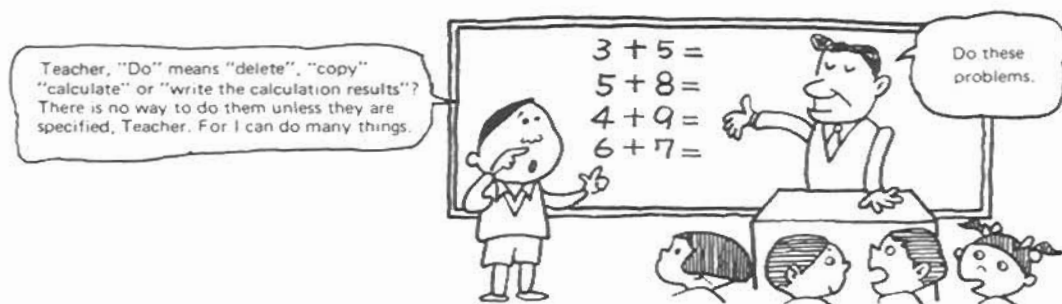
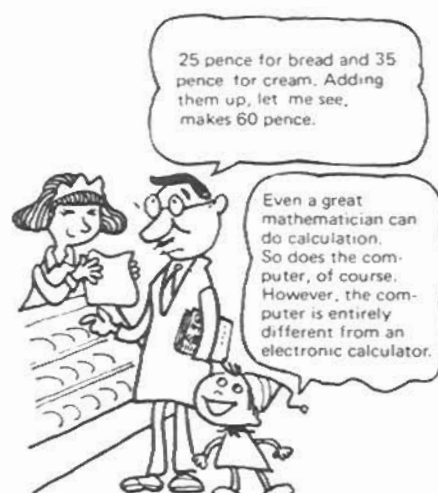
■ What is the Direct Mode?

Using the computer like an electronic calculator is possible if required. This kind of operation is called "Direct Mode".

Like the electronic calculator, key-in $5 + 8 =$.

To key-in the **+**, press the key while holding down the **SHIFT** key.

In fact, however, the computer displays the characters on the CRT screen only as keyed-in, and of course, no calculation is executed even with the **CR** key depressed. Here lies the difference between your computer and the electronic calculator. Your computer requires an instruction of what should be done about $5 + 8 =$.



PRINT

To use the computer in the same manner as the electronic calculator, the computation of $5 + 8$ is required to be displayed on the CRT screen. For this, there is the PRINT command available as an instruction. Using this command, let's press the keys in the following order to transfer the instructions.

PRINTS + 8 CR

As the keys are depressed, the characters below will be displayed on the CRT screen.

```
Ready ..... Meaning "Go ahead with your work".
PRINT 5 + 8 ..... Display the computation of 5 + 8, and
                    with the CR key pressed indicating the end of a command.
13 ..... This is the executed result of the command.
Ready ..... What is to be done next?
..... ..... Cursor
```

■ The Four Arithmetic Operations

If you want to go on to multiplication and division, note that the computer uses signs slightly different from those of ordinary mathematics.

Multiplication sign	*
Division sign	/

Calculation with Parenthesis

The computer is capable of handling more complex calculations than an ordinary calculator. This is a calculation with parenthesis.

In case of ordinary mathematical operation, different signs of groupings are used to write as follows:

$$3 \times 6 [6 + 3 \{ 9 - 2 (4 - 2) + 1 \}]$$

Whereas the parenthesis () alone is used at all times with the computer.

$$3 * 6 * (6 + 3 * (9 - 2 * (4 - 2) + 1))$$

Even with the above, the computer never forgets the rule that computation in the inner signs of groupings be done first, and never makes any mistake.



Exercise

PRINT (6 + 4) / (6 - 4)

5

PRINT 3 * (5 + 9 * (9 - 2) - 6 / (4 - 2)) + 5

200

PRINT (3 + 4) * (5 + 6)

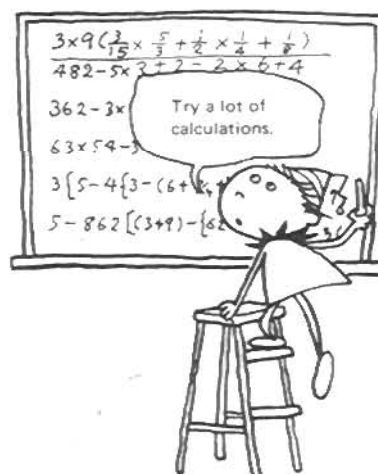
77

PRINT (10 + 20) / 6 * (2 + 3)

25

PRINT (10 + 20) / (6 * (2 + 3))

1



String? Expression?

```
PRINT 3 + 5
```

With the above, pressing the **CR** key makes 8, doesn't it? Now, put the expression in quotation marks".

```
PRINT "3 + 5" and CR
3 + 5
```

Oh, the result is different. Try another one

```
PRINT "HELLO MY FRIEND" CR
HELLO MY FRIEND
```

As is clear from the above, the characters or symbols put between quotation marks "" are displayed as they are on the CRT screen.

The block of characters and/or symbols between the quotation marks is called a **string**.

```
PRINT "3 + 5"
```

This is a string
put between quotation marks.

```
PRINT 3 + 5
```

This is an expression,
not a string.

It is necessary for you to know more about the strings. The free use of strings will double the pleasure in operating the computer.



PRINT is the command which you will have to get along with quite often. If you think it troublesome to key-in **PRINT** at every operation, **press the ? in place of PRINT**.

The computer automatically converts the ? to **PRINT**.

```
? 3 * 5
15
? (3 + 4) * 10
70
```



What are the PRINT's 1st and 2nd Approaches?

It is possible to add a plurality of items, such as strings and expressions, to the PRINT command. In this case, individual items should be separated using **semicolons** and **commas**.

```
PRINT "3 + 5 =", 3 + 5 CR
3 + 5 =      8
```

The expression between the quotation marks is a string. The actual calculation is done according to the expression following the semicolon.

Try it.

What will happen when using a comma (,) in place of a semicolon (;)?

```
PRINT "3 + 5 =", 3 + 5 CR
3 + 5 =          8
```

Why! The result of 3 + 5 is displayed far away from the expression. This means the following difference lies between the semicolon and comma.

- , Use of this separator results in display of output lists on successive lines.
- , This separator causes output lists to be displayed in a tabulated format.

When a separation is made with a comma, the 6 character space is not away from the end position of a string, but 10 character space from the starting position of the string. This fact requires your special attention.

```
PRINT "12345", 3 + 5 CR
12345      8
10 character space
```

```
PRINT "123456789", 3 + 5 CR
123456789  8
10 character space
```

If the string is longer than 10 character space, the result 8 is automatically made a further 10 character space away.

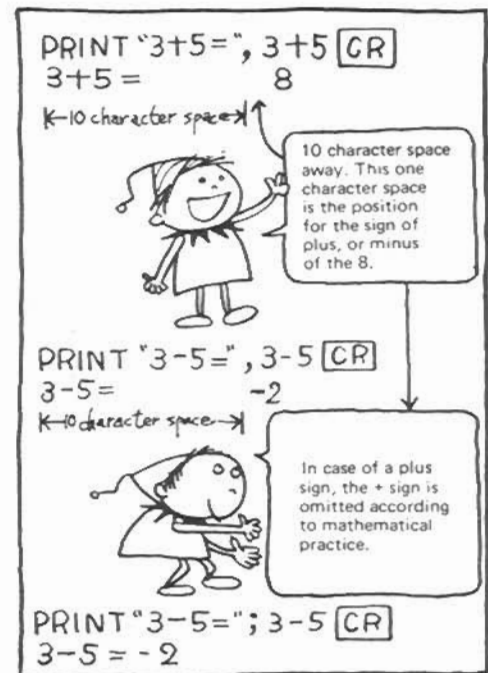
```
PRINT "123456789012", 3 + 5 CR
123456789012      8
20 character space
```



2nd approach



1st approach



Let the Computer Run!

Here is a program with statements covering several line.

```
10 A = 3
20 B = 5
30 C = A + B
40 PRINT A, B, C
50 END
```



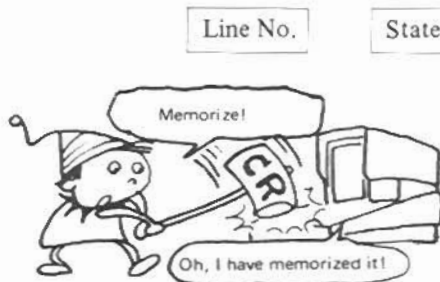
This program is the one to instruct assignment of $A = 3$ and $B = 5$, and the display of A , B and C on the CRT screen in expression as follows:

$C = A + B$

The numeral at the head of each statement is called a line number. The computer is sure to execute the line numbers from small in value to large in the correct sequence. Therefore, this makes it possible to insert a new statement in the program afterwards. For example,

35 $D = B - C$

The computer executes a program in the sequence of the line numbers, and therefore, the line numbers are made in steps of 10, as illustrated in the above example, so that new statements can be inserted later whenever required. The line numbers can be selected at liberty from 1 to 65,535.



Line No.

Statement

CR

Displaying the character on the CRT screen alone is not sufficient for the keying-in of the statement. After the display of each statement, the **CR** key must be pressed each time, to commit that statement to the memory.

For example, presuming the following,

35 **CR**

the statement in line number 35 is deleted from the computer.



Now, let's execute the program.

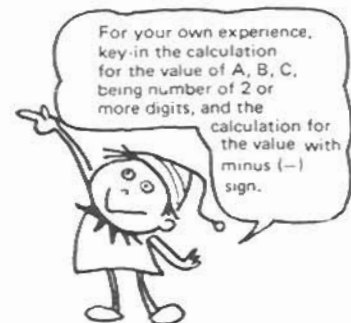
Press the keys as follows **RUN CR**.

RUN

3 5 8

←10 character space→ ←10 character space→ ↑

One character space for plus or minus sign. For minus value, minus (-) is inserted.



■ LIST for Quick Understanding

While continuing conversation with the computer by repeated trials and errors, the first keyed-in program may sometimes be gone from the CRT screen. Even so, don't worry. The computer never forgets any program once keyed-in. When you want to see the previous keyed-in program, key-in the following:

LIST

This is followed by the display all the stored programs on the CRT screen. If the program extends over tens and hundreds of lines beyond display at a time, part of the stored programs can be displayed.

LIST - 30

Displays a program up to line number 30.

LIST 30 -

Displays a program after line number 30.

LIST 30 - 50

Displays a program between line numbers 30 and 50.

LIST 30

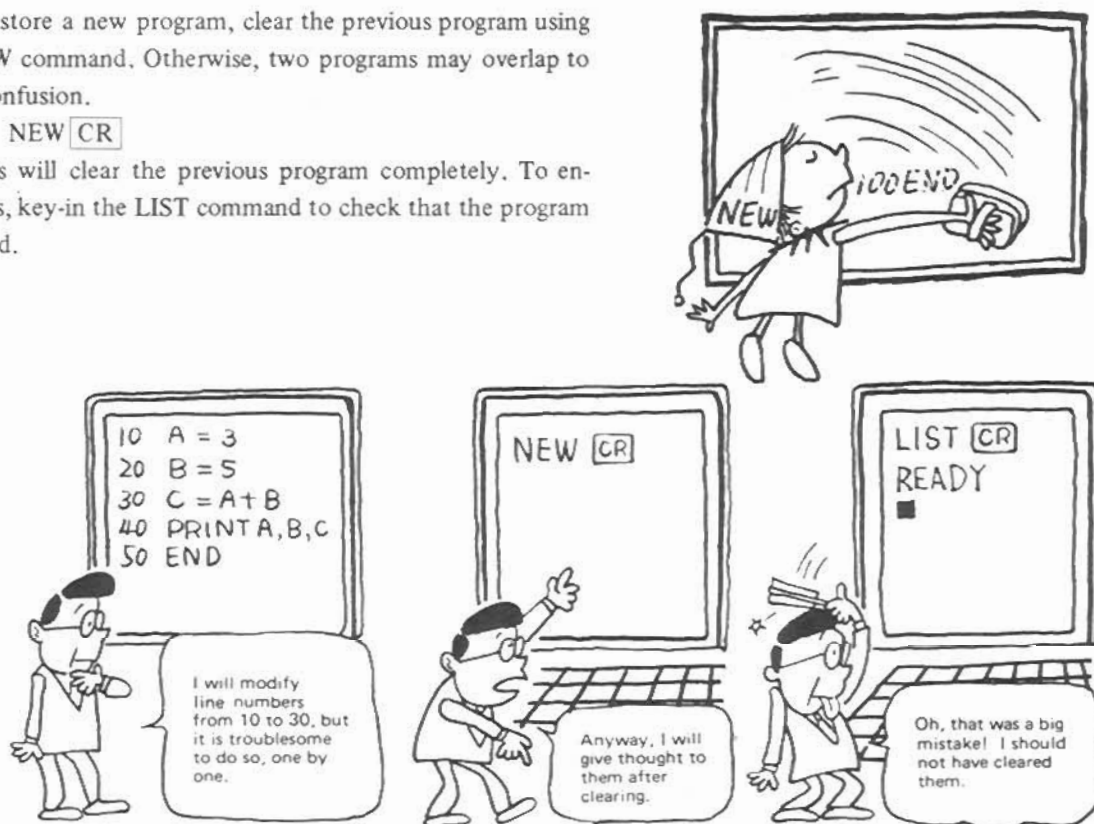
Displays a program of line number 30.

The result of NEW

To store a new program, clear the previous program using the NEW command. Otherwise, two programs may overlap to cause confusion.

NEW

This will clear the previous program completely. To ensure this, key-in the LIST command to check that the program is cleared.



■ Error Puts the Computer in Confusion

```

10  A = 3
20  B = 5
30  C = A + B
40  PRINT A, B, C
50  END

```

This is the same program as used before. Did it run well? If there is an error in any statement, the computer tells you about it. For example,

```
50  EMD
```

If you make a mistake of M for N, the computer executes the program up to line number 40 as instructed, but it does not know what EMD is all about. The computer tells you about a syntax error, as follows:

```
* Error 1 in 50
```

Then, key-in correctly as 50 END. For two statements identical in statement number, if any, the computer takes up the one that was keyed-in later. With this, is your program complete?

If so, try to make a mistake in line number 20, for example.

```
20  5 = B
```

With this, the statement in line number 20 must be revised. Sure? Use the LIST command to check the revision.

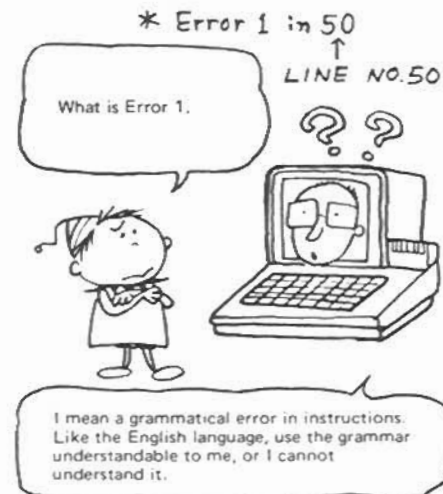
```

10  A = 3
20  B = 5
30  C = A + B
40  PRINT A, B, C
50  END
205 = B

```

Oh, something funny occurs. Line number 20 is not revised. On top of that, a strange statement with line number 205 lists out. This results because the computer ignored a space (blank part) between 20 and 5 and arranged them as a line number. A space to the computer is entirely insignificant and ignored.

Note: The interpreter notifies the operator of occurrence of an error during program execution or operation in the direct mode with the corresponding error number. Refer to the Error Message Table; page 120.



■ Collect the Statement ???

If you want to do the following about your program which has been stored in the computer;

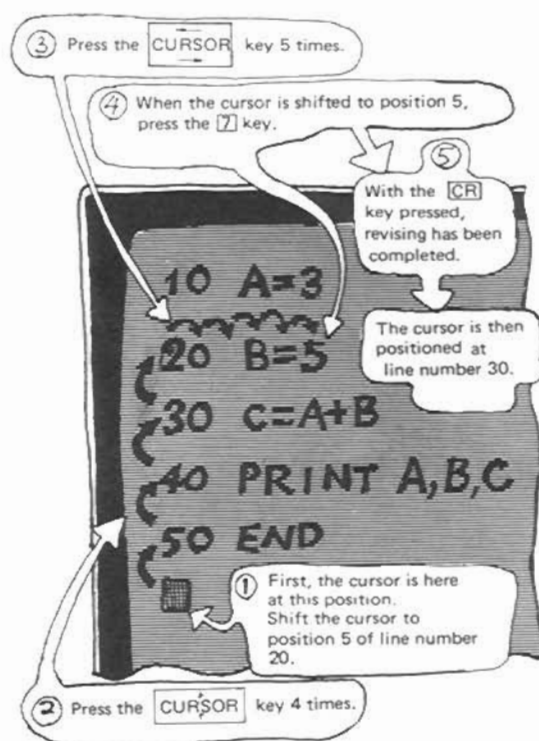
- To correct errors,
- To modify for a better statement,
- To modify for a separate statement,
- To modify part of a complete statement and to generate a new statement,
-

Let's study about statement modification, insertion and deletion when the above are required.

Cursor Shift

To revise the characters in a statement, the cursor must be shifted to the respective character positions. Now, let's revise 5 of the statement $B = 5$ in line number 20 to 7. Refer to the diagram at right for the shift procedure.

With this, the program displayed on the CRT screen has been modified. In fact, however, this has not yet modified the program stored in the computer. To modify the stored contents, the CR key must be pressed. What? Did you key-in 6 instead of 7? To modify the character to the left of the cursor, there are two methods available.



Method 1 Pressing the **INST·DEL** key.

With the **INST·DEL** key held down, the cursor shifts to the left by one character space, deleting the character next to it on the left. Press the **7** key again. Needless to say, the **CR** key must be pressed finally.

Method 2 Shifting to the left using the **CURSOR** key.

While pressing **SHIFT** key, depress the **CURSOR** key. The cursor shifts to the left by the number of times the key is pressed.

Then, press the **7** key again. The **CR** key must be pressed finally.

Correct the Statement!

Character Insertion

To modify the program on page 18 for the statement in line number 30, as follows,

30 D = 100 + A + B  Do not press the **CR** key yet.

shift the cursor to character A. Then press the keys as shown below.

With the **SHIFT** key depressed, press the **INST·DEL** key 4 times.

There must be a space for just 4 characters to add 100+. Key-in 100+ to this space. No more description is required for the revision of C to D. Since the statement has been modified so far, why not modify the line number from 30 to 35, and press the **CR** key. Modify line number 40 as shown below.

40 PRINT A, B, C, D

Then type RUN **CR**

```
RUN
3      5      8      108
```

Character Deletion

35 D = 100 + A + B

Let's modify this statement. To modify it to the following,

35 D = A + B + C

Shift the cursor to character A and press the **INST·DEL** key 4 times. This shifts the cursor until A + B portion comes right next to mark =

```
RUN
3      5      8      16
```

```
10 A=3
20 B=5
35 D=100+A+B
40 PRINT A,B,C,D
50 END
```

Portions in the squares have been modified.

This has cleared the statement in line number 30 on the CRT screen.

Just to make sure, type in LIST. Oh, line number 30 still remains there.

```
10 A=3
20 B=5
30 C=A+B
35 D=100+A+B
40 PRINT A,B,C,D
50 END
```

This is because no command was given to delete line number 30.

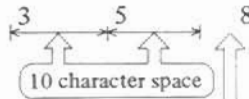
If you wish to delete it, key-in 30 **CR**.

Don't forget the **CR** key for modification.

Further Study of Comma and Semicolon

For review, the following example is taken again.

```
10 A = 3
20 B = 5
30 C = A + B
40 PRINT A, B, C
50 END
```



This space before the number is for the plus or minus sign.

You remember this, don't you? In other words, when using commas between A, B and C, a numeral is displayed 10 character space away. Generate a program with new statements inserted, and run it. Statements to be inserted are the following:

```
32 D = B ↑ A
34 E = B * A
36 F = B / A
45 PRINT D, E, F
```

```
RUN
3      5      8
125    15    1.6666667
```

With the comma (,) revised to semicolon (;) for line numbers, 40 and 45, run the program once more. To modify the program, type in LIST and use the cursor in as smart a manner as possible.

```
RUN
3 5 8
125 15 1.6666667
```

Space for plus/minus signs

Semicolon (;) has a function that combines the characters or symbols on display together. Add semicolon (;) to the end of line number 40 then RUN, in order to make sure of this fact.

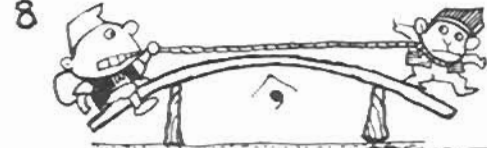
```
40 PTINT A; B; C;
RUN
3 5 8 125 15 1.6666667
```

The following is replaced for line number 40...

```
40 PRINT A
41 PRINT B
42 PRINT C
RUN
```

3
5
8

Pay attention to the vertical column.



There is the difference between , and ;.

Add a comma (,) or semicolon (;) to the ends of line numbers 40 and 41.



$B \uparrow A$ means B to the Ath power.

$$B^A = B \uparrow A$$



■ Colon and it's use

Use of Colon

```

10 A = 3
20 B = 5
30 C = A + B
32 D = B ↑ A
34 E = B * A
36 F = B / A
40 PRINT A; B; C
45 PRINT D, E, F
50 END

```

This program consists of short statements. A program in this length can be processed under one line number, if required.

```

100 A = 3 : B = 5 : C = A + B : D = B ↑ A : E = B * A : F = B / A : PRINT
A; B; C : PRINT D, E, F : END
RUN 100

```



Colon (:) is a symbol to be used when more than 2 statements are inserted in one line number. This kind of statement is called a "multi-statement". A statement with 2 lines can be described in one line number. 1 line consists of 40 characters, making it possible to use 76 characters including a line number.



How Much Left ? SIZE

It is natural for you to desire to know how much storage capacity is left at your disposal as programs are stored in the computer one after another.

For this, the following is done:

```
PRINT SIZE
```

In response to this, the computer tells you about the remaining storage capacity in bytes.



■ Does "A=B" Equal "B=A"?

Now let's give attention to the = sign we have often used so far. Try the following execution.

```

10 A = 1
20 PRINT A,
30 A = A + 2
40 PRINT A
50 END
RUN
1      3

```

1 and 3 are on display. $A = A + 2$ is for line number 30. If this is an equation, A is subtracted from both expressions making $0 = 2$, resulting in a contradiction. It is not an equation.

Sign = means that the result of the right expression is substituted by symbol A prepared on the left expression.



In line number 10, value 1 is substituted by symbol A, and at the right expression of line number 30, the value in symbol A and 2 are added and substituted by symbol A using symbol =.

At this time, value 1 previously put in A does not exist any more.

The following 2 programs produce different results which proves that "A = B" does not equal "B = A".

```

10 A = 5
20 B = 7
30 PRINT A, B
40 A = B
50 PRINT A, B
60 END
RUN
5      7
7      7 ←

```



```

10 A = 5
20 B = 7
30 PRINT A, B
40 B = A
50 PRINT A, B
60 END
RUN
5      7
5      5 ←

```



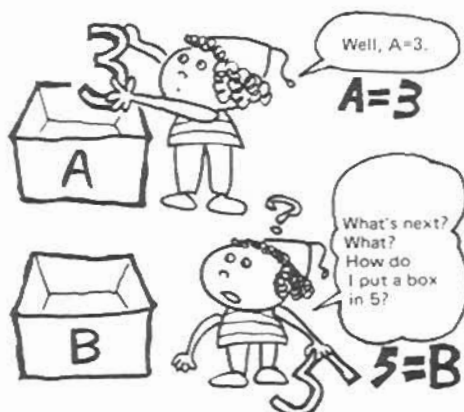
Variables the Computer is Very Fond of

The variables used in the computer statements are different in usage from the mathematical variables. The statement-used variables are the names given to the boxes designed to accommodate values.

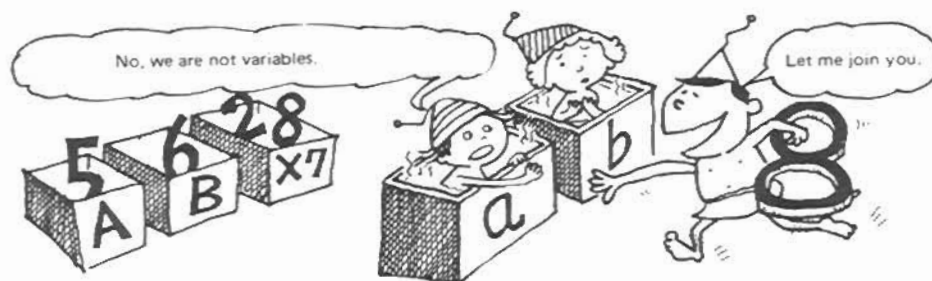
$B = 5$

This means that value 5 should be substituted by box B. Therefore, the use described under the "Error Puts Computer in Confusion" results in a difficult statement for the computer, though it cannot be mistaken as a line number. Because it says to put box B into 5.

```
10 A = 3
20 B = 5
30 A = A + B
40 PRINT A
50 END
RUN
8
```



From the mathematical definition, this program has a contradiction, however, the computer will understand.



Characters subject to Variables

1. A variable should be a combination of two or less characters. Any variable over 2 characters can be stored, but the characters after the second are neglected in computer processing. For example, ABC and ABD can be displayed. In processing, however, they are regarded as the same variables as AB.
2. The following are the characters for use as variables:
 - (1) A to Z. Alphabetical 26 ways.
Example: A, M, Z
 - (2) 260 characters with numeral of 1 figure (0 to 9) added to the alphabet.
Example: A0, K5, Z9
 - (3) Characters with two alphabetical characters combined.
Example: AA, BK, XZ

However, some variables, such as IF, ON, TO, etc in BASIC reserved words, should not be used.

■ Computing the Earth

The prince of a star takes accurate observation of the earth. "The earth is a blue planet over there in the Solar System. Though slightly distorted, the earth is approximately 13,000 kilometers in diameter. From orbit calculation, its mass is about 6×10^{18} thousand tons."

The prince went to his computer to generate the following program for calculations of volume VE, surface area SE and mean density ZE of the earth.



```

10 DE = 13000 ..... Substitute the earth's diameter for variable DE.
20 WE = 6E + 18 ..... Substitute the earth's mass for variable WE.
30 SE = 4 * π * (DE/2) ↑ 2 ..... This substitutes the surface area for variable SE.
40 VE = 4 * π * (DE/2) ↑ 3/3 ..... This substitutes the earth's volume for variable VE.
50 ZE = WE/VE * (1E - 2) ..... This substitutes the mean density for variable ZE.
60 PRINT "EARTH DIAMETER" ; DE ; "KILOMETERS"
70 PRINT "EARTH SURFACE AREA" ; SE ; "SQUARE KILOMETER"
80 PRINT "EARTH VOLUME" ; VE ; "CUBIC KILOMETER"
90 PRINT "EARTH MASS" ; WE ; "THOUSAND TONS"
100 PRINT "EARTH MEAN DENSITY" ; ZE ; "KILOGRAM/CUBIC METER"
110 END

```

The prince of a star understands slightly the size of the earth. Pay much attention to the units used in the calculations. Further attention is focused on the sequence of calculations when the arithmetic expression contains, *, + or ↑. The operation priority is shown below:

- 1 ↑ (Power)
- 2 - (Minus sign)
- 3 *, / (Multiplication and Division)
- 4 +, - (Addition and Subtraction)

The expressions below are complex in combination. Do you see any difference between the expressions?

☐ $2 + 3 \uparrow 2 = 11$
☐ $(2 + 3) \uparrow 2 = 25$

☐ $12/3 * 2 = 8$
☐ $12/(3 * 2) = 2$

☐ $2 * 2 \uparrow 3 = 16$
☐ $(2 * 2) \uparrow 3 = 64.000001$

☐ $12/3 \uparrow 2 = 1.3333333$
☐ $(12/3) \uparrow 2 = 16$

■ Archimedes and the Mysterious Soldier

The sum of the interior angles of a triangle is 180° . With a flash of inspiration, Archimedes sat on the road and drew a triangle. There came a mysterious soldier with his spear pointing at Archimedes.

Soldier: Archimedes, your life is finished. Be prepared to die!

Archimedes: Wait a minute, I will finish this calculation.

Soldier: What? Angle A is 30° and angle B is a right angle.

It's easy to determine angle C. 60° . If side CA length is known, side AB and BC lengths or even the area of the triangle can be easily determined.

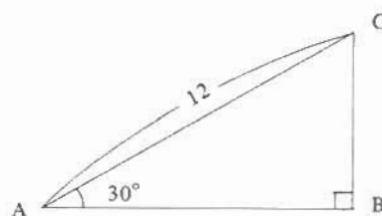
Archimedes: Don't be silly.

Soldier: All that needed is to generate a BASIC program. Let me see, Oh, Yes, it's good with CA = 12.

```

10 A = 30 : B = 90 : CA = 12
20 AB = CA * COS (A *  $\pi$ /180)
30 BC = CA * SIN (A *  $\pi$ /180)
40 S = AB * BC/2
50 C = 180 - A - B
60 PRINT "AB=" ; AB, "BC=" ; BC, "CA=" ; CA
70 PRINT "AREA S=" ; S
80 PRINT "A=" ; A, "B=" ; B, "C=" ; C
90 END

```



Using the inverse tangent ATN, let's determine the size of angle C from the side AB and BC lengths known. This requires the following to be keyed-in.

```
50 C = ATN (AB/BC) * 180/ $\pi$ 
```

The result is in the unit of degree. The same result is obtained, isn't it?

■ The Function Family Members

Introduced here are more functions, such as SIN (X). Such functions are used with parentheses, in which constants, variables or arithmetic expressions can be placed.

Function	BASIC Symbol	Calculated Value	Example
Integer	INT (X)	Maximum integer within X	INT (3.14) = 3 INT (0.55) = 0 INT (-7.9) = -8
Absolute value	ABS (X)	Absolute value of X	ABS (2.9) = 2.9 ABS (-5.5) = 5.5
Sign	SGN (X)	1 if X is greater than 0. 0 if X is equal to 0. -1 if X is less than 0.	SGN (500) = 1 SGN (0) = 0 SGN (-3.3) = -1
Exponent function	EXP (X)	e^x ($e = 2.7182818$)	EXP (1) = 2.7182818 EXP (0) = 1
Common logarithm	LOG (X)	$\log_{10} X$ Provided X is greater than 0.	LOG (3) = 0.47712125
Natural logarithm	LN (X)	$\log_e X$ Provided X is greater than 0.	LN (3) = 1.0986123
Square root	SQR (X)	\sqrt{X} Provided X is greater than or equal to 0.	SQR (9) = 3 SQR (0) = 0

Is PRINT 2 * 2 Identical to PRINT 2 ↑ 2?

Well $934 \uparrow 2$ results in fractions of 872355.99, but $934 * 934$ results 872356. This is correct as an arithmetic expression, but calculations are done in a limited number of figures, involving unexpected errors. For example, $2 \uparrow 2$ is done using the formula called a progression expansion.

$$2 \uparrow 2 = 1 + \frac{2 \ln 2}{1!} + \frac{(2 \ln 2)^2}{2!} + \dots + \frac{(2 \ln 2)^n}{n!} + \dots$$

This part is cut off, causing an error.

This calculation may cause the computer to scream. The computer will produce certain types of errors. These errors are, however of little concern.

■ Free Definition of Function.....DEF FN

Various functions have been described, and here is an explanation of DEF FN defined as a new function combining such various functions. Some definition examples are listed below:

DEF FNA (X) = $2 * X \uparrow 2 + 3 * X + 1 \dots\dots\dots 2X^2 + 3X + 1$ is defined as FNA (X).

DEF FNB (X) = $\text{SIN} (X) \uparrow 2 + \text{COS} (X) \uparrow 2 \dots\dots \sin^2 X + \cos^2 X$ is defined as FNB (X) this is always 1.

DEF FNE (V) = $1/2 * M * V \uparrow 2 \dots\dots\dots 1/2MV^2$ is defined as FNE (V).

DEF represents "define". New functions are named with FN suffixed. X or V in the parenthesis is called the argument. For example, the third function (seems to be motion energy) is used.

```
10 DEF FNE (V) = 1/2 * M * V ↑ 2
```

```
20 M = 5.5 : V = 3.5
```

```
30 PRINT FNE (V), FNE (V * 2), FNE (V * 3)
```

```
40 END
```

Motion energy at initial velocity V and motion energy with velocity doubled or tripled are displayed. DEF FN command is very convenient particularly when the same functions are often used in a long program.

Fall from an altitude of 10,000 meters!

How do you think the velocity and altitude of a fall from an altitude of 10,000 meters changes per second?

Fuction FNV (T) in the program is the fall velocity after a lapse of time T, and FNH (T) is the altitude at the same time.

Acceleration of gravity G, atomospheric resistance factor K and altitude H when a fall occurs are assigned by line number 20.



```
10 ? " ☐ " : T = 0
```

```
20 G = 9.8 : K = 0.15 : H = 10000
```

```
30 DEF FNV (T) = G/K * (1 - EXP (- K * T))
```

```
40 DEF FNH (T) = H - FNV (T) * T
```

```
50 ? " ☐ "
```

```
60 PRINT "TIME      "; T : MUSIC "+ A0" ..... Instruction with beep to be explained on page 76.
```

```
70 PRINT "VELOCITY" : FNV (T)
```

```
80 PRINT "ALTITUDE" : FNH (T)
```

```
90 T = T + 1 : GOTO 50 ..... This is a shift instruction for the program to be shifted to line No. 50.
```

☐ and ☐ are entered with the CLR
HOME key pressing in the graphic mode.

■ This is INPUT, Answer Please

To inform the computer of variables' values, we have so far taken the method where the value is first determined, as follows:

```
10 A = 3
20 B = 5
```

There are several methods available for informing the computer of the values of variables. One of them uses a command called INPUT.

```
10 INPUT A, B, C
20 D = A + B + C
30 PRINT A, B, C, D
40 END
RUN
? ❏
```

This is new display, isn't it? ? ❏ is making an inquiry to you about the value of first variable A following the INPUT command. In response to this inquiry, key-in the value and press the **CR** key to inform the computer that everything is O.K.

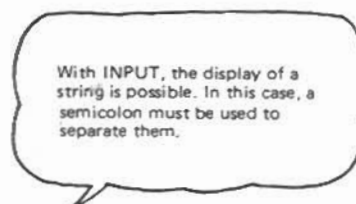
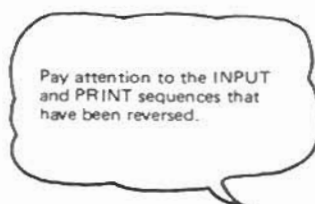
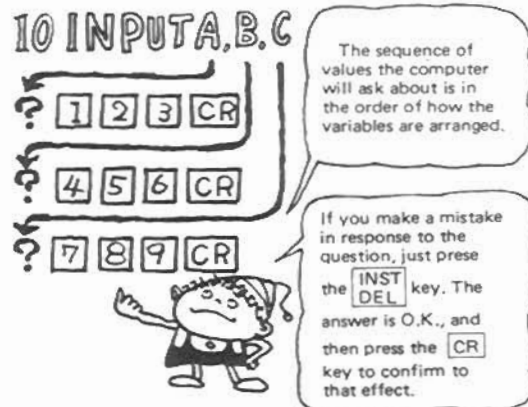
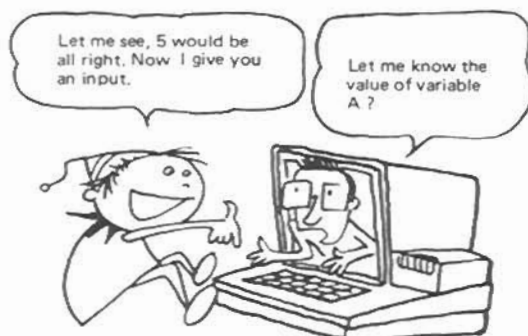
Look! The same display is there. This is the inquiry about the value of the second variable B. If there are 3 variables, the computer asks question 3 times. If you reply using any key other than 0 to 9 by mistake, and press the **CR** key, the following is displayed.

* Error 4 in 10 Data type mismatch

The computer will then make inquiries about the values all over again.

```
10 INPUT A, B, C, D
20 INPUT E, F, G, H
30 PRINT H, G, F, E
40 PRINT D, C, B, A
50 END
```

```
10 INPUT "A = ?" ; A
20 INPUT "B = ?" ; B
30 INPUT "C = ?" ; C
40 S = A + B + C
50 M = S/3
60 PRINT "TOTAL" ; S, "MEAN" ; M
70 END
```



■ Yes or No in Reply to a Proposal?

On a sunny Sunday, a gentleman and a lady sit face to face in a nice coffee shop. He is 43 years old, and she is 22 years old.

Gentleman: I love you at first sight. Can you marry me?

Lady: Yes, if you love me so much. I don't care about the age difference. But not now. You have to wait until my age is half of yours.

Presume his age is A, hers is B and the number of years she asked him to wait is X. After X years, he is A + X years while she is B + X. Since her age is then half of his, the condition of $A + X = 2(B + X)$ is required. To solve the equation for X, the following is obtained.

$$X = A - 2B.$$

```

10 PRINT "WHAT IS HIS AGE ?"
20 INPUT A
30 PRINT "WHAT IS HER AGE ?"
40 INPUT B
50 X = A - 2 * B
60 PRINT "WAIT" ; X ; "YEARS!"
70 END
RUN
WHAT IS HIS AGE?
? 43 CR
WHAT IS HER AGE ?
? 22 CR
WAIT - 1 YEARS!
```



It is impossible to wait for - 1 year. In other words, they could have been married a year ago. Asked suddenly about a question, the computer may be confused at what variable you are talking about. In this program, a string indicating inquiry contents is inserted in line numbers 10 and 30. The string for an answer is also used in line number 60.

The INPUT method in this program can be simplified. Modify line numbers 10 and 30 as described below, deleting line numbers 20 and 40 from the program.

```

10 INPUT "WHAT IS HIS AGE ?" ; A
30 INPUT "WHAT IS HER AGE ?" ; B
```

Those that follow line number 50 are identical to the above program.

```

RUN
WHAT IS HIS AGE? 43 CR
WHAT IS HER AGE? 22 CR
WAIT - 1 YEARS!
```



DATA and READ go hand in hand

Another method to inform the computer of variables.

```
10 READ A, B, C, D
20 X = A + B + C + D
30 PRINT X
40 DATA 3, 5, 7, 9
RUN
24
```

This program picks up values which are then used for calculation.

Two types of commands, READ and DATA, are used in this method.

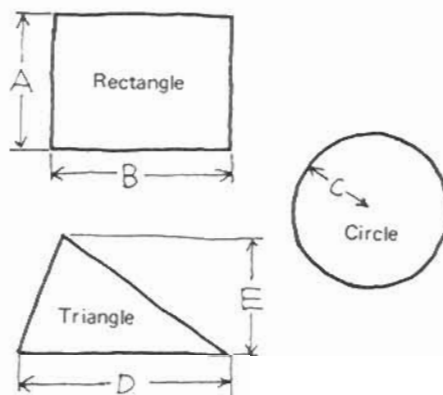


READ A, B, C, D Some variables are arranged.
DATA 10, 11, 12, 13 Number of values identical to that of variables that follow READ.

Similar to the INPUT command, the arrangements of variables and values must be matched.

It is unexpectedly easy to generate programs to determine rectangular, circle and triangle areas using the READ and DATA commands.

```
10 READ A, B
20 S1 = A * B
30 PRINT "RECTANGLE =" ; S1
40 READ C
50 S2 = π * C ↑ 2
60 PRINT "CIRCLE =" ; S2
70 READ D, E
80 S3 = D * E / 2
90 PRINT "TRIANGLE =" ; S3
100 DATA 2, 4, 6, 8, 10
110 END
```

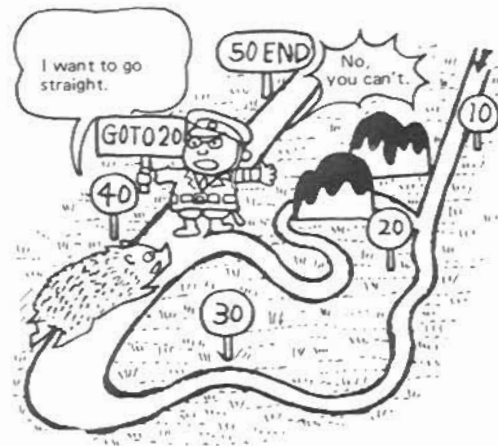


There seems to be room for improvements in the program.
Try various ways yourself.

■ Don't Oppose GOTO

For programs described so far, the computer executes them in the correct sequence from small to large line numbers. In fact, however, execution requires the sequence to be changed on some occasions. On such occasions, GOTO statements are very effective. GOTO means that an unconditional branch is made to the line number specified.

```
10 N = 1
20 PRINT N
30 N = N + 1
40 GOTO 20
50 END
RUN
1
2
3
```



Not stopped? Press the **SHIFT** key, then **BREAK** key to stop.

Once upon a time, the great Knight Sir Lancelot of the Lake did a great deed for King Arthur of Camelot. King Arthur was so grateful to Sir Lancelot he said, "I would like to give you any prize you care to ask for".

Sir Lancelot replied "Thank you my Lord, I would like to have 1 Ginea today, 2 Gineas tomorrow, 4 Gineas on the 3rd day, 8 Gineas on the 4th day and so on until the 30th day". King Arthur was so surprised by such a small request that he agreed immediatly.

Let us make the program below to find out how much King Arthur must pay.

```
10 D = 1 : F = 1 : S = 1
20 PRINT "DAYS", "GINEAS", "TOTAL"
30 PRINT D, F, S
40 D = D + 1 ..... This is for adding oneday to each day.
50 F = 2 * F ..... This is for multiplying oneday's total by two.
60 S = S + F ..... This shows the total by adding to previous day total.
70 IF D = 31 THEN 90
80 GOTO 30
90 END
RUN
```

DAYS	GINEAS	TOTAL
1	1	1
2	2	3
:	:	:
10	512	1023
:	:	:
20	524288	1048575
:	:	:
30	.53687091E + 09	.10737418E + 10

On the 10th day he was given 1023 Gineas, on the 20th day he was given 1048575 on the 30th day he asked for about 1000000000 Gineas.

■ IF.....THEN

IF ~ THEN

```
10 IF  $\triangle\triangle\triangle$  THEN  $\square\square\square$ 
20 ■■■
```

If $\triangle\triangle\triangle$ conditions are satisfied, then $\square\square\square$ jobs can be executed. If not, omit $\square\square\square$ and go to ■■■ of the next line number. This is the IF ~ THEN statement. If $\square\square\square$ is a numeral, a jump is made to the line number of the numeral.

```
10 READ A
20 IF  $A \geq 0$  THEN PRINT "A =" ; A
30 GOTO 10
40 DATA -10, 20, 5, -9, 8, -6, 5
50 END
RUN
A = 20
A = 5
A = 8
A = 5
```

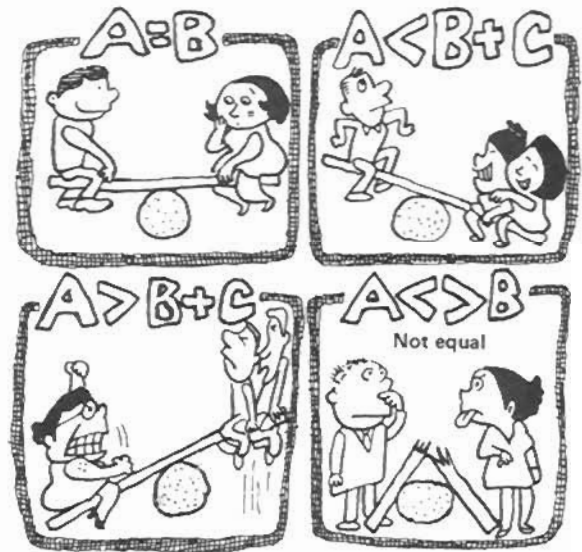
Positive numbers alone are displayed.

The general form of IF THEN statements is as follows:

IF conditions THEN statement or line number

The conditions herein referred to are "greater than" or "less than" expressions using equal sign and unequal sign.

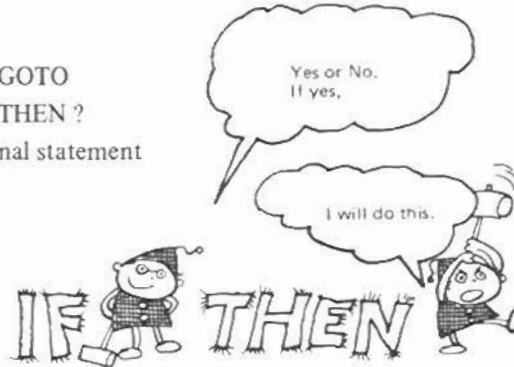
Sign Conditions	How to Use
=	$A = B$
<	$A < B + C$
>	$A > B + C$
<=	$A + B \leq C$
or = <	
>=	$A \geq B$
or = >	
< >	$A < > B$
or > <	



■ IF.....THEN and its Associates

If ... conditions are true (Yes), statement after THEN is executed. If they are false (No), execution is advanced to the next line number. Here is an introduction to its associates.

```
IF ..... THEN GOTO or IF ..... GOTO
IF ..... THEN PRINT or IF ..... THEN ?
IF ..... THEN A = 5 * 7 substitutional statement
IF ..... THEN INPUT
IF ..... THEN READ
IF ..... THEN GOSUB
IF ..... THEN RETURN
IF ..... THEN STOP
IF ..... THEN END
```



```
10 PRINT " [C] "
20 PRINT "INSERT OPTIONAL FIGURE FROM 1 TO 9."
25 PRINT "INSERT 0 WHEN YOU STOP."
30 L = 0 : M = 0 : N = 0
40 INPUT A
50 IF A = 0 THEN 90
60 IF A <= 3 THEN L = L + 1 : GOTO 40
70 IF A <= 6 THEN M = M + 1 : GOTO 40
80 N = N + 1 : GOTO 40
90 PRINT "YOU INSERTED FIGURES FROM 1 TO 3" ; L ; "TIMES" ;
100 PRINT "FROM 4 TO 6" ; M ; "TIMES" ;
110 PRINT "AND FROM 7 TO 9" ; N ; "TIMES"
120 END
```

A new symbol [C] is used in line number of 10. Display of [C] is possible when [CLR HOME] key is pressed, with the [SHIFT] key depressed in the graphic mode. This command will clear all the character on the CRT screen and shift the cursor to the top left corner of the CRT screen.

In addition, when the [CLR HOME] key alone is pressed in the graphic mode, symbol [H] appears.

This symbol functions only to shift the cursor to the top left corner.

If these are not clear, check with PRINT " [C] " or PRINT " [H] ".



■ Leave Any Decision to IF

IF can select Even numbers

Let's consider a program for selecting even numbers only, out of many numerals, using IF . . . GOTO statement. IF has great ability to select numbers.

```

10 READ X : IF X = -9999 THEN STOP
20 IF X/2 <> INT (X/2) GOTO 10
30 PRINT X ; : GOTO 10
40 DATA 2, 13, 56, 55, 4, 78, 31
50 DATA 6, 22, 15, 19, 80, 11, -9999
RUN
2 56 4 78 6 22 80

```

INT (X/2) in line number 20 is the statement for picking integers alone. Therefore, if X is even, $X/2 \neq \text{INT}(X/2)$ is impossible, with execution advancing to line number 30. If it is possible, it's regarded as odd, reading the next value.

To test your progress, let's try an exercise. How can you decide the multiple of 3 or 4? You've got it, haven't you? The answer is this.

Modification for the multiple of three

```
20 IF X/3 <> INT (X/3) GOTO 10
```

Modification for the multiple of four

```
20 IF X/4 <> INT (X/4) GOTO 10
```

IF can select Maximum and Minimum

```

10 S = 999 : L = -999
20 READ X : IF X = -9999 THEN 80
30 IF X > L THEN L = X
40 IF X > S THEN S = X
50 GOTO 20
60 DATA 2, -5, 91, 256, -43
70 DATA 87, 321, -76, -9999
80 PRINT "MAXIMUM VALUE =" ; L
90 PRINT "MINIMUM VALUE =" ; S
100 END
RUN
MAXIMUM VALUE = 321
MINIMUM VALUE = -76

```



Line number 10 is very important. Put as large a number as possible in variable S for substitution of the minimum value, and as small a number as possible in variable L for substitution of the maximum value. What about the execution results? Variable L and S come out as true maximum and minimum values. This is a good example of the use of IF . . . THEN.

■ Password Found for Numbers

The greatest common divisor (GCD) is a password for two integers. For example, presuming that two numbers are 10 and 20, divisible numbers for 10 and 20 are four numbers that are 1, 2, 5 and 10. Of these numbers, the maximum value, namely, 10 is the greatest common divisor for numbers 10 and 20.

Now, let's generate a program.

```

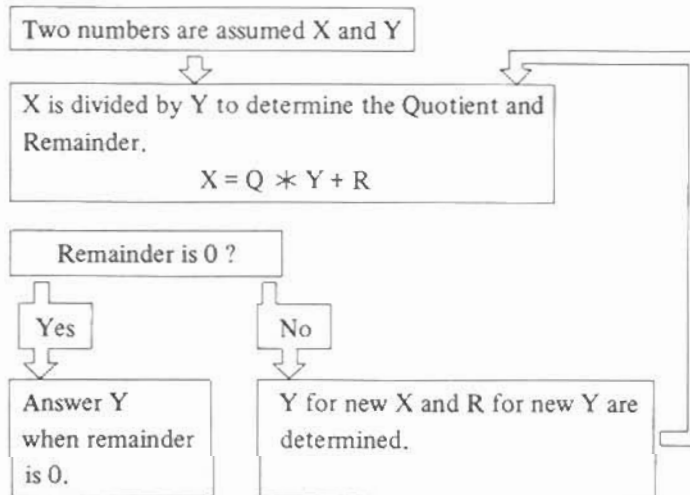
10 PRINT "X", "Y", "PASSWORD"
20 READ X, Y
30 PRINT X, Y
40 Q = INT (X/Y)
50 R = X - Q * Y
60 X = Y : Y = R
70 IF R > 0 THEN 40
80 PRINT X : GOTO 20
90 DATA 63, 99, 1221, 121, 64, 658
100 DATA 12345678, 987654321
110 END
RUN

```

Comma (,) following Y is very convenient for continuous display on the CRT screen.

Exposure of a Trick for this Program !

Long ago, a Greek mathematician, Euclid, developed this method of solution.



Using IF . . . , try as many as possible.

```

10 IF SGN (X) = -1 THEN ? "MINUS"
20 IF SGN (X) = 0 THEN ? "ZERO"
30 IF SGN (X) = 1 THEN ? "PLUS"

```

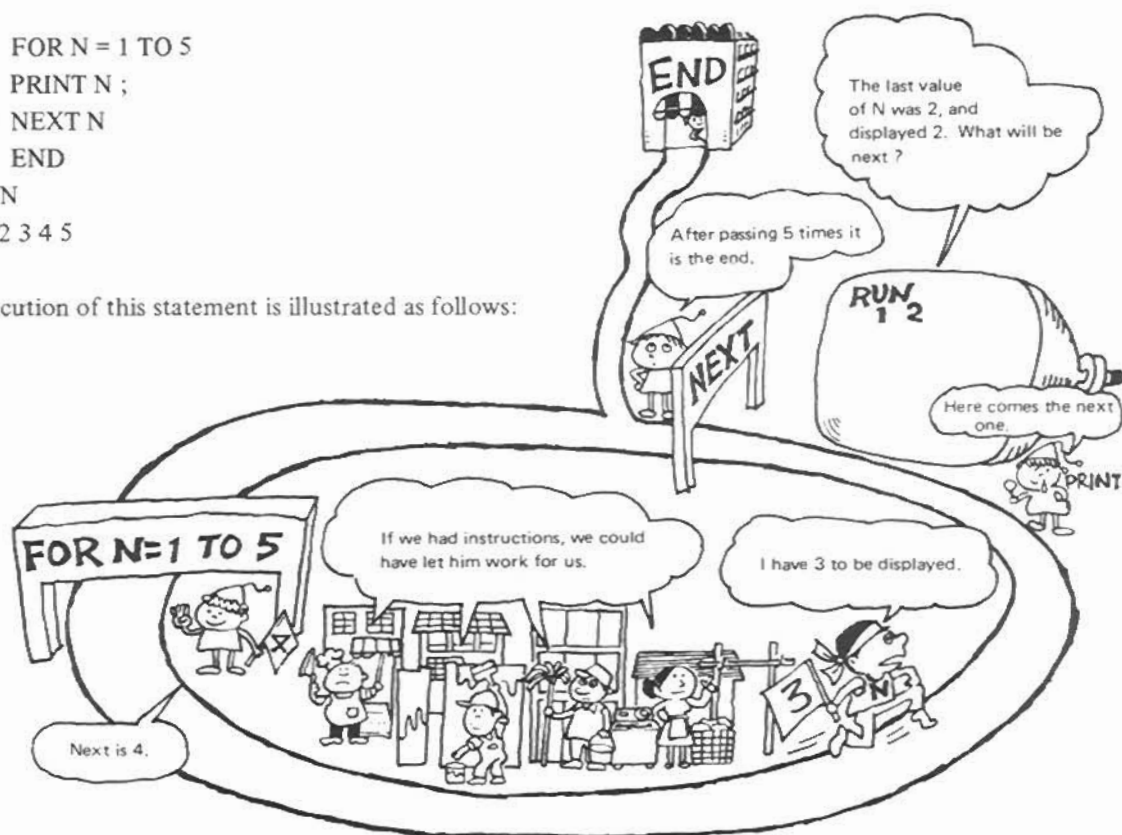


■ FOR.....NEXT is an Expert of Repetition

The FOR NEXT statement is an instruction used for repetition of a sequence of program statements. Let's have a look at a simple program, first.

```
10 FOR N = 1 TO 5
20 PRINT N ;
30 NEXT N
40 END
RUN
1 2 3 4 5
```

The execution of this statement is illustrated as follows:



The variation of N is not only increased by 1, but can be increased, for example, by 0.5 or decreased by 2. The variation at this time is assigned by the word of STEP.

To increase in 0.5 increments:

```
10 FOR N = 1 TO 5 STEP 0.5
```

To decrease in 2 decrements:

```
10 FOR N = 5 TO 1 STEP -2
```

The general form of FOR NEXT statement is as follows:

```
FOR variable = Initial Value TO Last Value STEP Variation
Repeated Program
NEXT Variable
```

The initial value, final value and variation may be either a variable, constant or expression.

Loop in a loop

Alice is doing her homework. She is preparing a multiplication table using the computer, and a program which contains double FOR . . . NEXT statements.

```

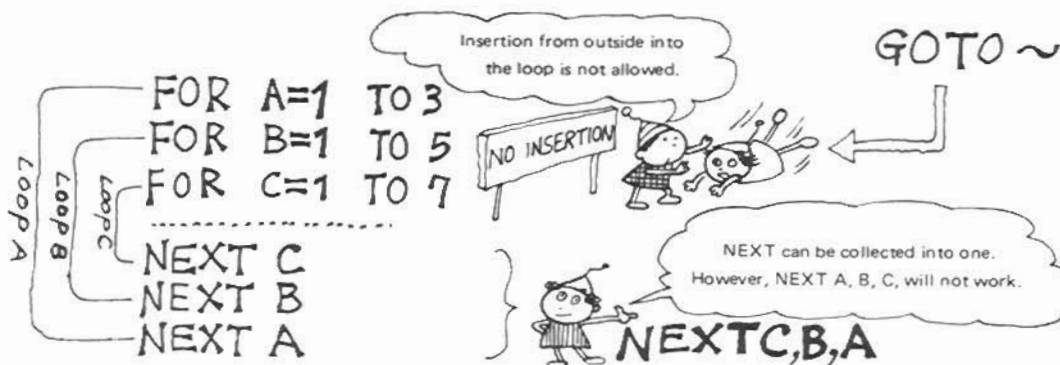
10 FOR X = 1 TO 9
20 FOR Y = 1 TO 9
30 PRINT X * Y;
40 NEXT Y
50 PRINT
60 NEXT X
  
```

Loop Y
Loop X



In the FOR . . . NEXT loop for variable X, the FOR . . . NEXT loop for variable Y is included. Variables X and Y vary from 1 to 9, respectively, and 1 is substituted for variable X to execute variable Y loop. In other words, with variable X remaining at 1, variable Y varies 1, 2, 3, . . . to 9, and each time, the multiplication product with variable X is displayed at line number 30. When variable Y reaches 9, a line feed is executed at line number 50, and at line number 60, variable X is then 2.

The FOR . . . NEXT loop can be used double, triple, etc., up to 15. What must be observed, however, is that loops are never crossed and no jump into the loop by means of GOTO is allowed.



Thus, loop C is completely included in loop B, while loop B is completely included in loop A. As shown on the right, one word NEXT can be used for all three loops.

Line up in Numerical Order

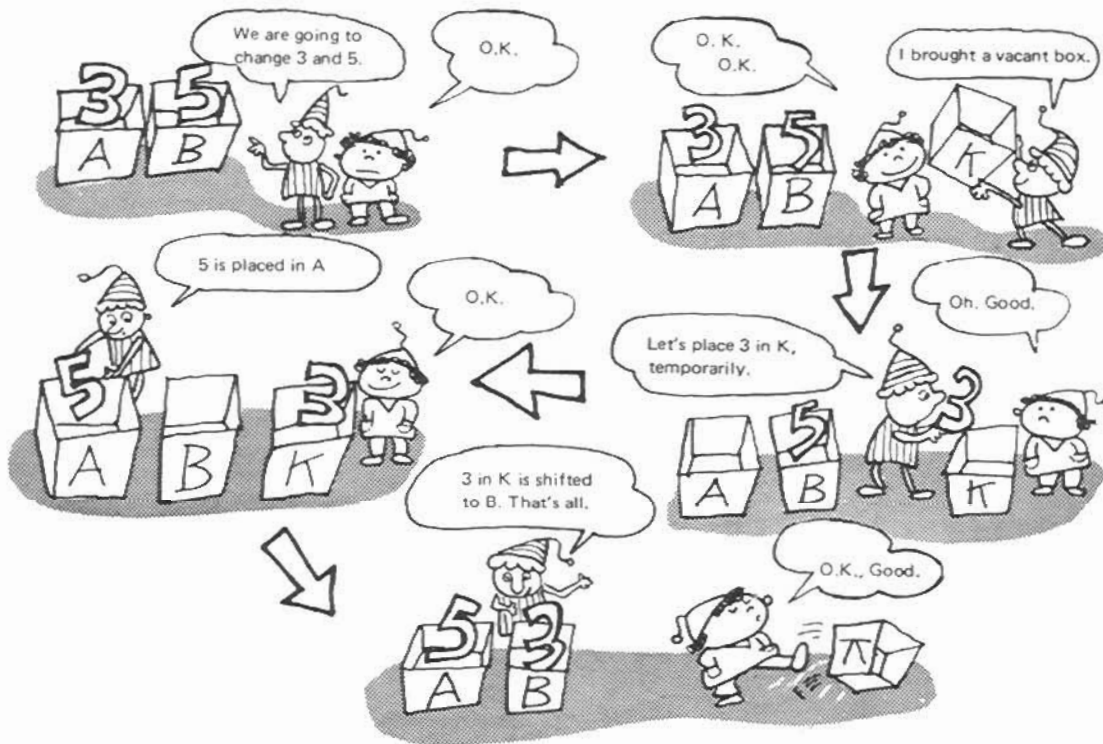
With 4 numerals selected at random and keyed-in, the computer can arrange them in numerical order. This is a program for such a function. Use the INPUT command.

```

10 PRINT " "
20 PRINT "TELL ME VALUES OF 4 NUMERALS" : PRINT
30 INPUT A, B, C, D
40 IF A <= B THEN K = A : A = B : B = K
50 IF B <= C THEN K = B : B = C : C = K
60 IF C <= D THEN K = C : C = D : D = K
70 IF A < B GOTO 40
80 IF B < C GOTO 40
90 IF A < C GOTO 40
100 PRINT A, B, C, D
110 PRINT : PRINT "ONCE MORE PLEASE" : PRINT
120 GOTO 30

```

Give attention to line number 40. Using another variable K, after the THEN statement, the job is being done by changing the values of A and B. If A = 3 and B = 5 in the initial state;



By the above job, A = 5 and B = 3 are obtained. Similar processing is executed at line numbers 50 and 60. Line numbers 70 through 90 are prepared for the repetition of the changing job.

■ How Many Right Triangles are Possible?

Now, let's generate a program that picks up positive integers from 1 to 20 to meet the Pythagorean theorem $A^2 = B^2 + C^2$.

```

10 PRINT "  C  "
20 PRINT "    \ "
30 PRINT "    / \ "
40 PRINT "  B |   \ A"
50 PRINT "    |   \ "
60 PRINT "    |___\ "
70 PRINT "              "
80 PRINT "          C"
90 PRINT : PRINT "POSITIVE INTEGERS TO MEET PYTHAGOREAN THEOREM"
110 PRINT : PRINT "  A", "  B", "  C"
120 FOR A = 1 TO 20
130 FOR B = 1 TO 20
140 FOR C = 1 TO 20
150 IF A * A - B * B - C * C = 0 THEN PRINT A, B, C
160 NEXT C, B, A
180 END

```

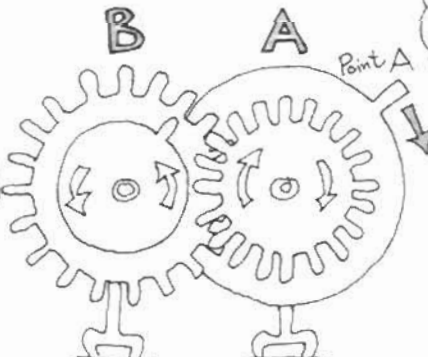


Using the graphic patterns try to draw a fine triangle on the CRT screen.
Don't forget quotation marks after drawing.

You already know the meaning of line number 10. Try to draw carefully so that a fine triangle is formed between line numbers 20 through 80. At line numbers 120 through 160, the FOR . . . NEXT loop is triple. The equation shown at line number 150 is repeated 8000 times ($20 \times 20 \times 20$) with C from 1 to 20 at A = 1 and B = 1, and with C from 1 to 20 at A = 1 and B = 2, and so on.

This operation requires a considerable period of time for its completion.

Triple FOR~NEXT



How many times does he have to turn the pedals in order to rotate point A through one complete revolution.



TAB() is Versatile

It is possible to assign where to start writing the characters or symbols of a string on the CRT screen. The TAB() is used to do so.

Using PRINT TAB (8) ; "ABC", string ABC is displayed at the number in the parenthesis counted from the left hand side, namely, starting at the 9th position.

The numbers to be assigned for the parenthesis are from 0 to 78, and variables may be used if defined as numerals.



PRINT TAB (8) ; "ABC" starts at 8 + 1.



Let's operate an example of a simple program combined with the FOR . . . NEXT statements.

```
10 FOR X = 1 TO 20
20 PRINT TAB (X) ; " * "
30 NEXT X
```

```
10 FOR Y = 1 TO 20
20 PRINT TAB (20 - Y) ; " * "
30 NEXT Y
```

Now, let's try a little more complex program.

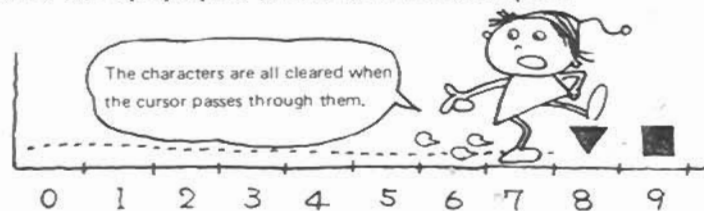
```
10 PRINT " ■ " : PRINT SPACES (8) ;
20 FOR X = 1 TO 22 : PRINT " * " ; : NEXT X : PRINT
30 FOR Y = 1 TO 20
40 PRINT TAB (8) ; " * " ; TAB (29 - Y) ; " ■ " ; TAB (29) ; " * " : NEXT Y : PRINT SPACES (8) ;
50 FOR Z = 1 TO 22 : PRINT " * " ; : NEXT Z
```

A new statement is there at line numbers 10 and 40. When this SPACES () is substituted for TAB, exactly same result is obtained. However, there is the difference shown below between the SPACES and TAB.

TAB (8) shifts the cursor by 8 character space from the left hand on CRT screen.



SPACES (8) displays space (blank) for 8 character space.



■ Grand Prix using RESTORE

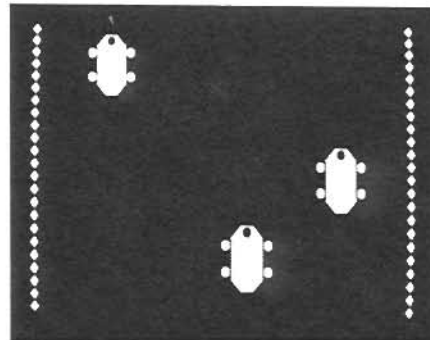
Challenge of a Car Race

How about the simplest car race program ?

```

10 X = 33 * RND (1)
20 FOR A = 1 TO 5
30 READ MS
40 PRINT TAB (0) ; " ♦ " ; TAB (X) ; MS ;
50 PRINT TAB (37) ; " ♦ "
60 NEXT A
70 Y = 10 * RND (1)
80 FOR A = 1 TO Y
90 PRINT TAB (0) ; " ♦ " ;
100 PRINT TAB (37) ; " ♦ " : NEXT
110 RESTORE : GOTO 10
120 DATA " ▴ ▽ ▴ ▽ " , " ● ● ● ● ● "
130 DATA " ● ● ● ● ● " , " ● ● ● ● ● "
140 DATA " ▴ ▽ ▴ "

```



TAB (X) at line number 40 determines where to display automobiles on the road, particularly from the left side. What distance between the automobiles ? For this, random numbers from 1 to 9 are generated at line number 70, and at line numbers 80 through 100, automobiles are controlled so that they do not collide. By the way, RESTORE at line number 110 is not a familiar command, is it ?

RESTORE Returns to the Start of Data

No matter where it may be, or no matter how it may be scattered, DATA statement is read by READ statement.

O.K.

```

10 DATA 27
20 READ A, B, C
30 DATA 10
40 .....
50 DATA 9, 13
60 READ D
70 END

```

NO

```

10 READ A, B
20 READ C
30 DATA 27, 10, 9
40 READ D
50 END

```

Why No ? Because variable D has no value of DATA to be read.

Then what about this ?

```

10 READ A, B
20 READ C
30 DATA 27, 10, 9
35 RESTORE ..... RESTORE statement enables the reading of the first data in the
40 READ D ..... first DATA statement of the program.
50 END

```

Talkative Strings

The computer should generate a language understandable to human beings and should talk to us To make such a desire come true, string variables are absolutely necessary.

```

10 AS = "MIKE" : BS = "PAUL"
20 CS = "TONY" : DS = "PETE"
30 ES = "DENIS" : FS = "MARTIN"
40 GS = "PHILIP"
50 IS = "JACK" : JS = "HARRY"
60 KS = "BILL" : LS = "DAVID"

```

Ordinary variable symbols with \$ (dollar sign) suffixed are called string variables. Processing, very similar to that of ordinary variables is possible. Let's look at some examples to see their characteristics.

```

70 PRINT BS
80 PRINT AS
RUN
PAUL
MIKE

```

Using " ; ", connects string variables.

```

100 PRINT BS ; CS ; AS ; ES ; LS ; DS ; KS
RUN 100
PAULTONYMIKEDENISDAVIDPETEBILL

```

What will happen if " , " is used in place of " ; " ?

```

120 PRINT BS, AS, IS
RUN 120
PAUL      MIKE      JACK

```

10 character space

To combine string variables to generate a new string, add string variables together using "+".

```

140 XS = BS + CS + DS + JS + GS
150 YS = AS + CS + BS + FS + IS + GS
160 PRINT XS
170 PRINT YS

```

With this, a new string variable is possible.



■ Another type of INPUT

Combine string variables and INPUT statement in a program to create a poem.

```

10 INPUT AS, BS, CS
20 PRINT AS ; " "; BS ; " "; CS
30 GOTO 10
RUN
? A FROG JUMPS
? INTO A POND
? WITH A SPLASH OF WATER.
A FROG JUMPS INTO A POND WITH A SPLASH OF WATER.

```

Space for syllable
separations.



Using INPUT statement, the input of a string can be keyed-in, requiring no quotation marks “ ”.

Another example of this is shown below.

```

10 PRINT "TYPE IN ANYTHING AT ALL"
20 INPUT AA$
30 PRINT "YOU HAVE JUST TYPED" ; AA$
40 GOTO 10

```

String variables, when combined with READ and DATA statements, can be generated into a program.

```

10 READ X1$, X2$
20 PRINT X1$ ; "LIKE CREAM" ; X2$
30 DATA DO YOU, CAKES ?
RUN
DO YOU LIKE CREAM CAKES ?

```

Note that quotation marks are not required when READ statement is used.



■ LEFT\$, MID\$, RIGHT\$

LEFT\$ (), MID\$ () and RIGHT\$ () are statements to generate new strings by taking out part of strings.

```
10 A$ = "AQUARIUS PISCES ARIES LEO"
```

```
20 B$ = LEFT$ (A$, 15)
```

```
30 PRINT B$
```

```
RUN
```

```
AQUARIUS PISCES
```

Character up to the 15th from the left hand side.

LEFT\$ (A\$, 15) selects the characters up to the 15th out of the string A\$ in order to generate a new string. The string variable B\$ has been defined for the new string.

To select some characters when counted from the right hand side of a string, RIGHT\$ () is used.

```
40 C$ = RIGHT$ (A$, 9)
```

```
50 PRINT C$
```

```
RUN
```

```
ARIES LEO
```

Selects the last 9 characters from A\$

To select characters in the center of a string, MID\$ () is used.

```
60 D$ = MID$ (A$, 10, 12)
```

```
70 PRINT D$
```

```
RUN
```

```
PISCES ARIES
```

Selects 12 characters starting at position 10 of A\$



AQUARIUS



PISCES



ARIES



LEO



A\$



AQUARIUS



PISCES



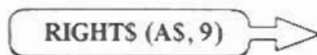
LEFT\$ (A\$, 15)



ARIES



LEO



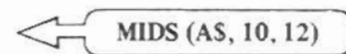
RIGHT\$ (A\$, 9)



PISCES



ARIES



MID\$ (A\$, 10, 12)

■ LEN is a Measurement for Strings

LEN () is used to discover the character count of a string.

A simple example of this statement is as follows:

```
10 AS = "ABCDEFG"
20 PRINT LEN (AS)
RUN
7
```

The character count of a string variable AS, namely "7" is displayed.

Here is a program using LEN () statement for drawing a square.

```
10 PRINT "☐" : PRINT "TYPE HORIZONTAL SIDE USING * KEY"
20 INPUT AS
30 FOR J = 1 TO LEN (AS) - 2
40 PRINT TAB (2) ; " * " ; SPACES (LEN (AS) - 2) ; " * "
50 NEXT J
60 PRINT TAB (2) ; AS : GOTO 20
```

Vary the values of * input. The computer performs square drawing by using LEN (). Then, drawing is made possible by characters or symbols other than "*". Using LEFT \$ (), line numbers 20 and 40 of the previous program are modified.

```
20 INPUT AS : AAS = LEFT $ (AS, 1)
40 PRINT TAB (2) ; AAS ; SPACE $ (LEN (AS) - 2) ; AAS
```

The use of LEN makes a string parade possible.

```
10 SS = "SHARP BASIC"
20 FOR M = 1 TO LEN (SS)
30 PRINT LEFT$ (SS, M)
40 NEXT M
RUN
S
SH
SHA
SHAR
SHARP
SHARP
SHARP B
SHARP BA
SHARP BAS
SHARP BASI
SHARP BASIC
```

```
10 SS = "SHARP BASIC"
20 FOR M = 1 TO LEN (SS)
30 PRINT RIGHT$ (SS, M)
40 NEXT M
RUN
C
IC
SIC
ASIC
BASIC
BASIC
P BASIC
RP BASIC
ARP BASIC
HARP BASIC
SHARP BASIC
```



■ ASC and CHR\$ are Relatives

ASC

```
10 PRINT ASC ("A");
20 PRINT ASC ("ABC");
30 TS = "Z" : PRINT ASC (TS)
40 END
RUN
65 65 90
Ready
```

With strings in the parenthesis () of ASC, when PRINT is keyed-in the result always shows numerals. Actually, this shows the ASCII code. All characters used with the computer are based on the ASCII code. For its table, refer to page 210. ASC () picks up the ASCII code for the first character of string in the parenthesis (). This gives a clear clue to the reason why the same result is obtained although the strings in the parentheses differ between line numbers 10 and 20. The ASCII code is for characters up to 255.

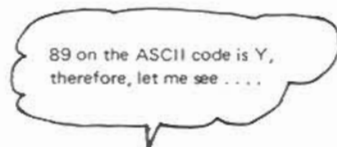
CHR\$

If characters can be converted to the ASCII code, it is natural that there is a statement to reverse the conversion. That's right. CHR\$ statement does that job.

```
PRINT CHR$ (65), CHR$ (ASC ("K"))
A      K
Ready
```

A cipher is generated using the numerals. Let CHR\$ read it.

```
10 FOR J = 1 TO 24 : READ A
20 BS = CHR$ (A)
30 PRINT BS ; : NEXT : END
40 DATA 73, 32, 83, 84, 85, 68
50 DATA 89, 32, 66, 65, 83, 73
60 DATA 67, 32, 79, 70, 32, 77
70 DATA 90, 45, 56, 48, 65, 46
RUN
I STUDY BASIC OF MZ - 80A.
Ready
```



■ STR\$ and VAL are Numeral Converters

STR \$

```

10 A = 12 : B = 3 : C = A + B
20 C$ = STR$ (A) + STR$ (B)
30 PRINT C, C$
40 END
RUN
    15      123
Ready

```

The value of variable A is converted to a string of characters by STR\$ (A) and string-processed. The reason why C\$ contents are 123 is clear to you. In the following program, use STR\$ to match the “.” of data.

```

10 FOR X = 1 TO 5
20 READ A
30 L = 5 - LEN (STR$ (INT (A)))
40 PRINT TAB (L) ; A
50 NEXT : END
60 DATA 1. 2 3 4 5 6, 1 2. 3 4 5 6
70 DATA 1 2 3. 4 5 6, 1 2 3 4. 5 6
80 DATA 1 2 3 4 5. 6

```



The results of the program on the left are as follows.

```

      1. 2 3 4 5 6
      1 2. 3 4 5 6
    1 2 3. 4 5 6
  1 2 3 4. 5 6
1 2 3 4 5. 6
READY

```

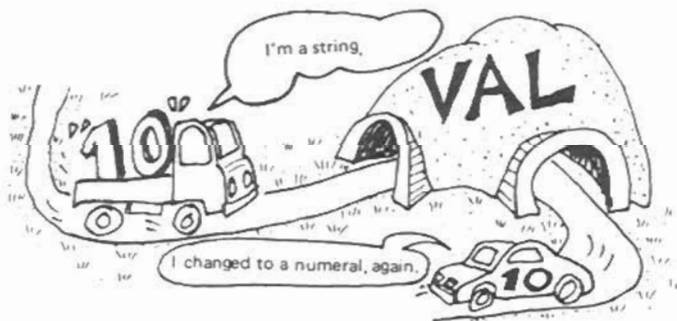
VAL

VAL statement has a function opposite to STR\$ statement. In other words, it converts a string of characters to a numeral.

```

10 AS = "1 2 3 4 5 6"
20 B = VAL (AS)
30 C = 6 5 4 3 2 1 + B
40 PRINT AS
50 PRINT B
60 PRINT C
80 END
RUN
1 2 3 4 5 6 ..... Not a numeral but a string.
1 2 3 4 5 6 ..... Numeral, so there is a space for ± (plus/minus sign) to be placed
7 7 7 7 7 ..... before the most significant digit of the numeral. For a netagive
READY                                numeral, a minus sign is placed in the space.

```



■ Print out as £123,456,789.....

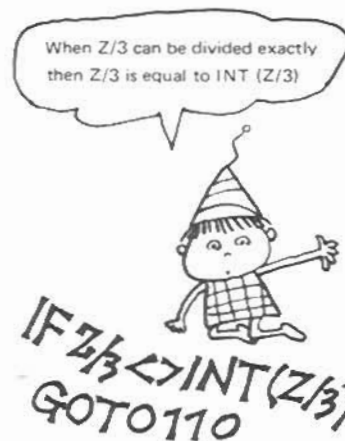
This program reads an integer of an optional figure under the INPUT statement, and writes it adding commas (,) to every 3 figures from the right. Given 0 as an integer, the program terminates.

```

10 PRINT "INPUT INTEGER" ;
20 INPUT X$
30 IF X$ = "0" THEN END
40 PRINT "£" ;
50 FOR Y = 1 TO LEN (X$)
60 PRINT MID$ (X$, Y, 1) ;
70 Z = LEN (X$) - Y
80 IF Z/3 <> INT (Z/3) GOTO 110
90 IF Z = 0 GOTO 110
100 PRINT " , " ;
110 NEXT Y
120 PRINT : PRINT : GOTO 10
RUN
INPUT INTEGER ? 1 2 3 4 5 6 7 8 9
£ 123,456,789

INPUT INTEGER ? 1 2 3 4
£ 1,234

```



Line number 80 checks to see if Z (Character position counted from the right) is a multiple of 3. If so, a comma " , " is placed at line number 100. For example, presuming that the input integer is a number of 9 figures, the following is obtained.

	Character count of INPUT integer								
	← LEN (X\$) →								
INPUT integer :	□	□	□	■	□	□	□	□	□
Y variable :	1	2	3	4	5	6	7	8	9
Z variable :	8	7	6	5	4	3	2	1	0
	← Z = LEN (X\$) - Y →								

Take a number consisting of figures 1 to 4, and another number of the same figures but with a reverse arrangement to the former, then add up these two numbers. You will thus find that the sum is the same whether it is counted from the right or from the left.

```

10 PRINT " ■ ENTER SOME NUMBER COMPOSED OF FIGURES 1 TO 4 (WITHIN 8 DIGITS)"
20 Z$ = "" : INPUT X$
30 FOR K = LEN (X$) TO 1 STEP - 1
40 Y$ = MID$ (X$, K, 1)
50 Z$ = Z$ + Y$ : NEXT K : X = VAL (X$) + VAL (Z$)
60 PRINT X : PRINT : GOTO 20

```

■ Difference between the Simple and Compound Interests

£ 10,000 is deposited in a bank, and one year later, £ 10,600 is drawn. Interest rate, in this case, is found to be $600/1000 = 0.06$ or 6%. Then, what is the interest when deposited for 2 years. There are two methods available for interest calculation. One is simple interest calculation based on the fact that the interest of £ 600 for the first year doubles for the second year, amounting to £ 1200. The other is compound interest calculation based on the idea that a deposit at the beginning of the second year is £ 10,600 with interest of £ 636 ($£ 10,600 \times 0.06$) added to make £ 1236 for two years. Compound interest calculation is slightly better in interest rate. For a larger sum of money deposited for longer terms, the difference in interest rate between the two methods must be noticeable. The following is the equation for determining interest included for the n year in each calculation method.

Interest included by simple calculation (n year with rate R)

$$B = X (\text{principal}) + n \cdot X \cdot R$$

Interest included by compound calculation (n year with rate R)

$$C = X \cdot (1 + R)^n$$

Based on the above equations, the following program is generated to calculate interest included both in simple and compound interests.

```

10 PRINT "PRINCIPAL"
20 INPUT X
30 PRINT "INTEREST RATE %"
40 INPUT R
50 PRINT "NUMBER OF YEARS"
60 INPUT Y : PRINT : PRINT
70 PRINT "PRINCIPAL =" ; X
80 PRINT "INTEREST RATE =" ; R ; "%"
90 PRINT "YEARS" ; TAB (6) ; "SIMPLE" ;
100 PRINT TAB (17) ; "COMPOUND" ;
110 PRINT TAB (30) ; "DIFFERENCE"
120 FOR A = 1 TO Y
130 B = X + A * X * (R/100)
140 C = INT (10 * X * (1 + R/100) ^ A) / 10
150 D = C - B
160 PRINT A ; TAB (6) ; B ;
170 PRINT TAB (15) ; C ; TAB (30) ; D
180 NEXT A
190 PRINT : PRINT : GOTO 10

```

The following is an example of program execution:

```

PRINCIPAL = 10000
INTEREST RATE = 6%

```

YEARS	SIMPLE	COMPOUND	DIFFERENCE
1	10600	10600	0
2	11200	11236	36
3	11800	11910.1	110.1
4	12400	12624.7	224.7
5	13000	13382.2	382.2
6	13600	14185.1	585.1
7	14200	15036.3	836.29999

■ Annuity if Deposited for 5 years

In the previous example, we looked at the difference in interest between the simple and compound interest calculations for money deposited. Actually, however, monthly deposit, like fixed deposit, is more familiar to us. If a fixed amount of money X is deposited monthly, the interest included increases with $X(1+R)$ for the first year, $X(1+R)^2$ for the second and so on. In addition, when sum X is deposited yearly, the money to be deposited the year after, 2 years from now, will be $X(1+R)$. Such an increase of deposits is shown below in equations:

Interest included a year after (Principal X and interest R)

$$M_1 = X(1+R)$$

Interest included 2 years after

$$M_2 = X(1+R)^2 + X(1+R)$$

Interest included 3 years after

$$M_3 = X(1+R)^3 + X(1+R)^2 + X(1+R)$$

Based on the above, the interest included is calculated in the following equation for n years.

$$M_n = X(1+R)^n + X(1+R)^{n-1} + \dots + X(1+R)$$

This is simplified as follows:

$$M_n = X((1+R)^{n+1} - (1+R))/R$$

Here's the program generated to indicate what is the interest included for any desired year with the same amount of money deposited each year. Even though the same amount is deposited, this program is designed to allow inputs of minimum and maximum amounts.

```

10 PRINT "INTEREST RATE %";
20 INPUT R
30 PRINT "ENTER AMOUNTS"
40 PRINT "MINIMUM" ; INPUT L
50 PRINT "MAXIMUM" ; INPUT H
60 PRINT "NUMBER OF YEARS" ;
70 INPUT Y : PRINT : PRINT
80 PRINT "RATE" ; R ; "%"
90 PRINT "EACH YEAR" ; TAB(12) ; Y ; "YEARS"
100 R = R/100 : PRINT
110 FOR A = L TO H STEP 10000
120 B = INT (A * ((1 + R) ^ (Y + 1) - (1 + R)) / R)
130 PRINT A ; TAB(12) ; B
140 NEXT A
150 PRINT : PRINT : GOTO 10

```

The Result of Program Execution

INTEREST RATE %? 10	
ENTER AMOUNTS	
MINIMUM? 50000	
MAXIMUM? 100000	
NUMBER OF YEARS? 7	
RATE 10%	7YEARS
EACH YEAR	
50000	521794
60000	626153
70000	730512
80000	834871
90000	939229
100000	1043588
INTEREST RATE %? ■	

■ Stop, Check and Continue

The computer does not always work as desired when operated with a program generated. This requires a STOP statement to be inserted to check the contents of variables at the stop position. For example, in the following program, the STOP statement is inserted.

```
10 READ A, B
20 X = A * B
30 STOP
40 Y = A/B
50 END
60 PRINT X, Y
70 DATA 15, 5
80 END
RUN
Stop in 30 ←
```

At the time, the display of variables is made in direct mode as follows:

```
PRINT A,B,X CR
```

This enables you to check the program. To re-start the program, give a command to the computer as follows:

```
CONT CR
```

The computer restarts execution from the stop position. With the END statement at line number 50, the computer displays the "Ready" and stops again. Then, print in the direct mode, as follows:

```
X = 3 : Y = 5 CR
```

The computer will then continue program execution when the CONT command is given, displaying 3 and 5 for variables X and Y.

The following program continues to display a triangle of * marks, indefinitely.

```
10 A = 0 : B = 38 : C = 1
20 FOR X = A TO B STEP C
30 FOR Y = 0 TO X
40 PRINT " * ";
50 NEXT Y : PRINT : NEXT X
60 K = A : A = B : B = K
70 C = -C : GOTO 20
```

With the BREAK key pressed while holding down the SHIFT key, program execution stops. Then, insert the following in the program and give the CONT command.

```
100 END
```

This is followed by the display below.

```
* Error 17 ..... CONT command cannot be executed.
```

The CONT command cannot be used when a program is edited using a line number after program execution has been stopped with the STOP statement, END statement or BREAK key operation. This requires special attention.

The CONT command is used when ;

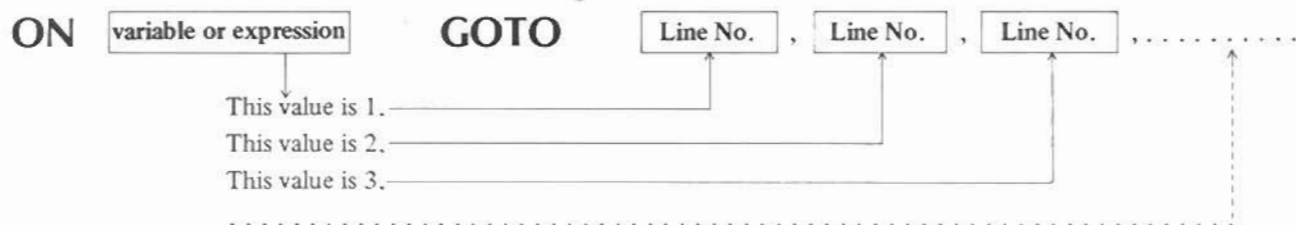
- Program execution is stopped with the SHIFT – BREAK keys.
- Program execution is stopped by the STOP or END statement.
- Inputs are stopped at the INPUT statement using the SHIFT – BREAK keys.

The CONT command cannot be used ;

- Before program has been executed using the RUN command.
- When program is edited after execution has been stopped.
- If an error occurs during execution. Program returns to the "Ready".
- To stop cassette tape operation, cassette tape operation is stopped with the SHIFT + BREAK keys.
- When the MUSIC command for music sound is stopped.

■ Jump in masse Using the ON GOTO Statement

You have learnt much about the GOTO statement. Description here is given of the On ... GOTO statement, an extended function of the GOTO statement.



For example, when the value of a variable or expression after ON is 3, a jump is effected to the third line number that follows GOTO. In other words, it is possible to assign the branch of a program in accordance with the values of variables.

```
10 INPUT "NUMBER (1 - 3) ?" ; A
20 ON A GOTO 50, 60, 70
50 PRINT "X X X" : GOTO 10
60 PRINT "Y Y Y" : GOTO 10
70 PRINT "Z Z Z" : GOTO 10
RUN
NUMBER (1 - 3) ? 1
X X X
NUMBER (1 - 3) ? 2
Y Y Y
NUMBER (1 - 3) ? 3
Z Z Z
```

Given 1.2, for example, integer 1 is processed, cutting off any place of decimals.

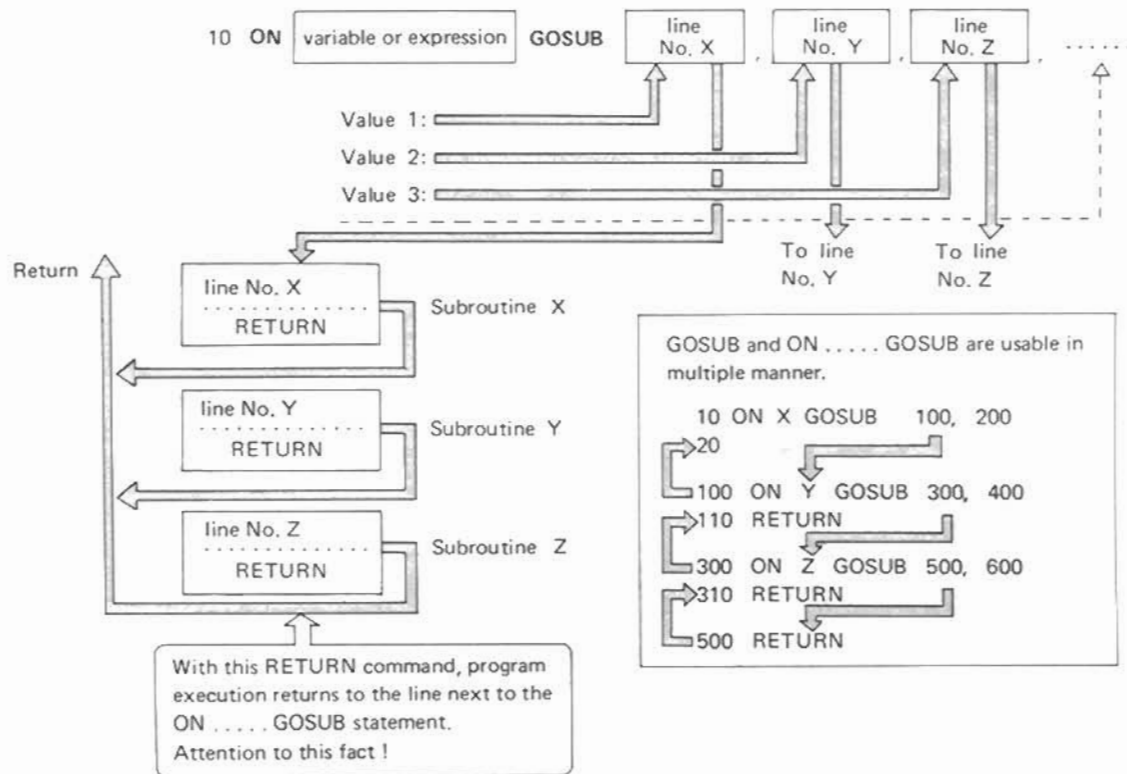
Let's play a joker-pick game using the ON ... GOTO statement. A joker is included in 5 cards. The place of the joker is unknown, of course. Guess where the joker is to compete with the computer for a win. When asked "Do you pass?" in the program, key-in 1 for pass, 2 for not pass and 3 if the game is not to be played. Three passes are allowed.

```
10 R = INT (5 * RND (1)) + 1
20 N = N + 1 : IF N = 6 THEN 120
30 INPUT "DO YOU PASS?" ; X
40 ON X GOTO 60, 90, 50
50 PRINT "GAME NOT PLAYED !!!" : GOTO 120
60 NP = NP + 1
70 IF NP >= 4 THEN NP = NP - 1 : N = N - 1 : PRINT "NO PASS ALLOWED"
80 GOTO 20
90 NR = NR + 1
100 IF R = NR + NP THEN PRINT "UNLUCKY ! YOU HAVE SELECTED THE JOKER" : GOTO 120
110 PRINT "LUCKY ! YOU HAVE NOT SELECTED THE JOKER" : GOTO 20
120 END
```



■ ON....GOSUB is the Use of a Subroutine Group

The ON GOSUB statement is very similar in function to the ON GOTO statement.



Now, let's consider the program for a time table to check your progress. Most important in the following program is that subroutines are called at line number 180, despite the jump made at line number 90 to line number 170 through 190 of subroutines.

Thus, the GOSUB and ON GOSUB statement can be used in a convenient, multiple manner.

```

10 AS = "FRENCH " : BS = "MATHEMATICS" : CS = "ENGLISH"
20 DS = "SCIENCE " : ES = "MUSIC      " : FS = "ATHLETICS"
30 GS = "SOCIAL STUDIES" : HS = "ART          " : IS = "TECHNOLOGY"
40 JS = "RELIGION  " : KS = "ECONOMICS"
50 PRINT "WHAT DAY OF THE WEEK ? "
55 PRINT "(1 - MON, 2 - TUE, 3 - WED, 4 - THU, 5 - FRI, 0 - ALL)"
60 INPUT XS : X = ASC (XS) - 47
70 FOR Y = 0 TO 3 : PRINT TAB (3 + 8 * Y) ; Y + 1 ;
80 NEXT Y : PRINT
90 ON X GOSUB 170, 110, 120, 130, 140, 150
100 PRINT : GOTO 50
110 PRINT "MON : " ; AS ; CS ; DS ; BS : RETURN
120 PRINT "TUE : " ; HS ; HS ; ES ; BS : RETURN
130 PRINT "WED : " ; AS ; CS ; JS ; KS : RETURN
140 PRINT "THU : " ; DS ; AS ; ES ; FS : RETURN
150 PRINT "FRI : " ; AS ; DS ; IS ; GS : RETURN
170 FOR Y = 1 TO 5
180 ON Y GOSUB 110, 120, 130, 140, 150
190 PRINT : NEXT Y
200 RETURN
  
```

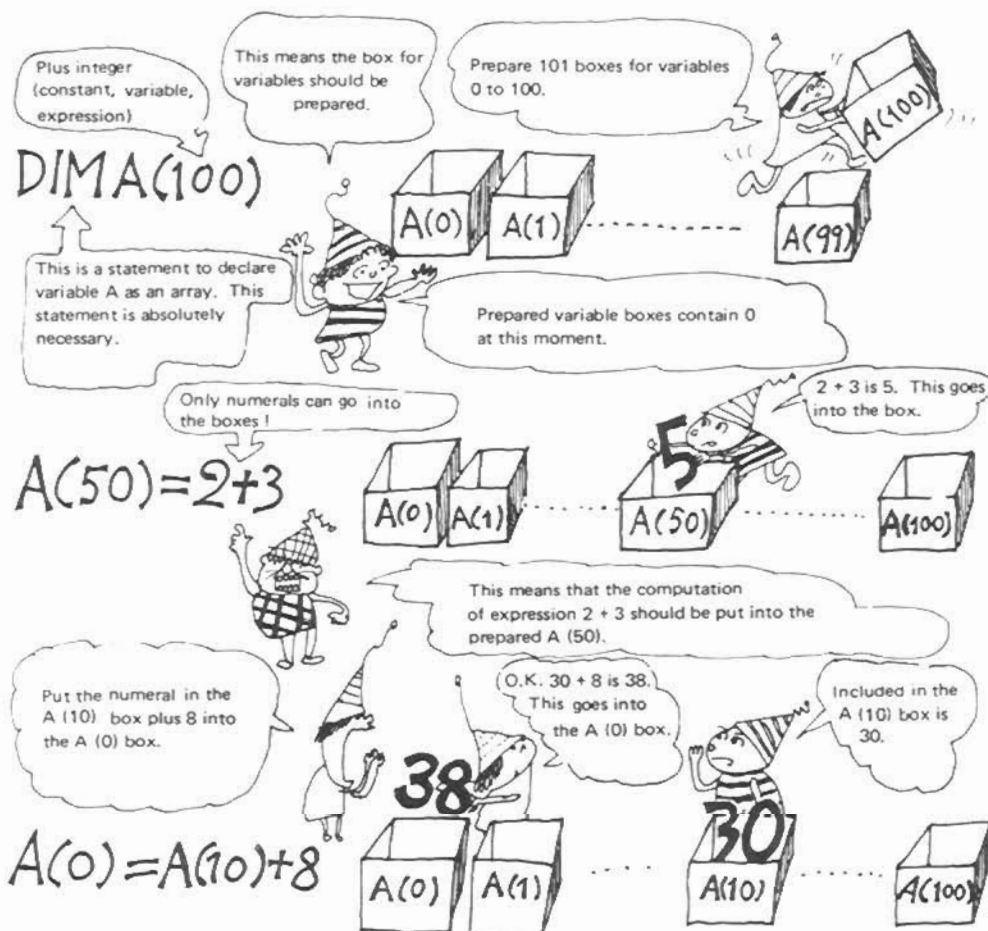


■ Primary Array has the Strength of 100 Men

Now, consider the substitution of variables for 100 items of data. The use of variables A1 and A2 makes the following possible.

```
10 A1 = 5
20 A2 = 30
30 A3 = 12
.....
```

Just a minute. This is terribly hard work for writing 100 statements! For this, the primary array is available as a new type of variable, which makes program generation very convenient. Now, let's look at what the primary array is all about.



Now, you have understood what the primary array is, haven't you?

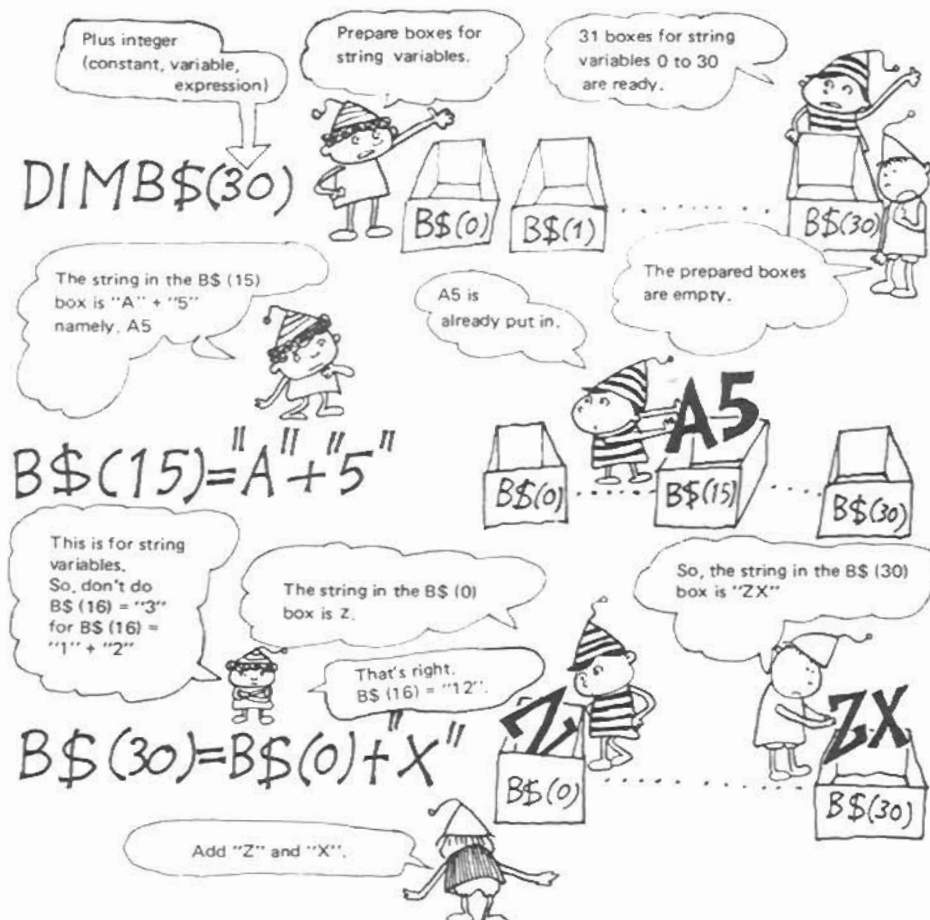
Using the primary array, the program has been generated as follows:

```
10 DIM A (100)
20 FOR J = 1 TO 100
30 READ A (J)
40 NEXT J
50 DATA 5, 30, 12, .....
```

See, the program is very short. As is clear from this example, variables in the form of an array can assign the parenthesis of subscribed variables, such as A (J), with variable J. This is the main feature of the primary array.

■ Array is also Available for String Variables

Since an array is available for numeral variables, there must be an array available even for string variables. Here's an introduction to what the primary array for string variables is all about.



Let's generate a simple program. Just a look at this. Keeping variable strings in the form of arrays eliminates the labour of writing whenever they are used. The program itself is neat and simple.

```

10 DIM A$(2), B$(2), C$(2)
20 FOR J = 1 TO 2 : READ A$(J), B$(J)
30 C$(J) = A$(J) + " " + B$(J)
40 PRINT A$(J), B$(J), C$(J)
50 NEXT J
60 END
70 DATA YOUNG, GIRL, WHITE, ROSE

```

```

RUN
YOUNG      GIRL      YOUNG GIRL
WHITE      ROSE      WHITE ROSE
Ready

```

■ Array is the Master of File Generation

Some teachers say that testing is all right but putting test results in order is really hard. If so, some students insist testing should be stopped. A good method is available for teachers who are subject to giving tests to students.

The use of an array helps them solve the problem! The following shows student identification and marks for mathematics.

Student No.	20	15	12	40	23	16	31	45	26	11
Marks	75	51	28	56	100	81	60	43	66	48

Generate a file program arranged in the order of merit.

```

10 DIM A (10), B (10)
20 FOR J = 1 TO 10
30 READ A (J), B (J) : NEXT
40 FOR K = 1 TO 9 : M = 0
50 FOR J = K TO 10
60 IF B (J) <= M THEN 80
70 M = B (J) : L = J
80 NEXT J
90 B (L) = B (K) : B (K) = M
100 A1 = A (L) : A (L) = A (K) : A (K) = A1
120 NEXT K
130 PRINT "☐"
140 PRINT "ORDER OF MERIT (MATHEMATICS)"
150 PRINT
160 PRINT "STUDENT NO." ; TAB (14) ;
170 PRINT "MARKS"
180 FOR J = 1 TO 10
190 PRINT A (J) ; TAB (14) ; B (J) : NEXT J
200 END
210 DATA 20, 75, 15, 51, 12, 28, 40, 56, 23, 100
220 DATA 16, 82, 31, 60, 45, 43, 26, 66, 11, 48
RUN
ORDER OF MERIT (MATHEMATICS)

```

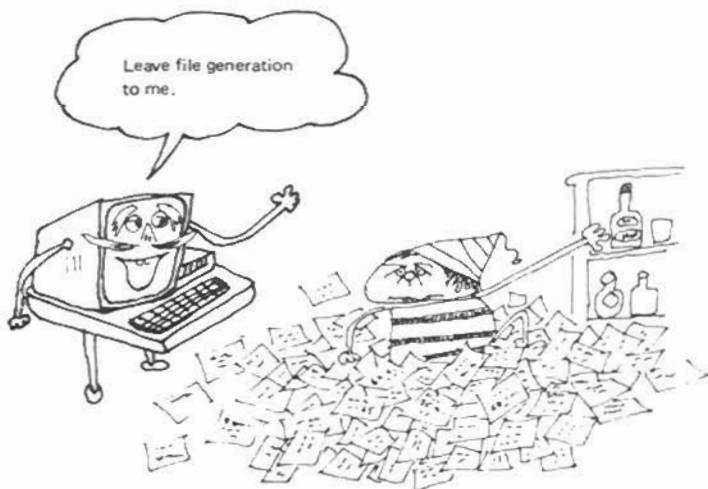
STUDENT No.	MARKS
23	100
16	81
20	75
26	66
31	60
40	56
15	51
11	48
45	43
12	28

Ready

A file is a summary of data sorted out for items.



Leave file generation to me.



■ Challenge of French Study

We used to study french words using word-notebooks. Smart and more simplified word-notebooks are available using the computer. French words and their meanings are contained in separate files. The computer gives two types of questions; one asking about the meanings of French words retrieved from the file and the other asking English to be translated to French. In the program, the primary string array is used as the files containing French words and their meanings. Executing the following program, try to test your French vocabulary, answering a variety of questions the computer will ask you.

```

10 DIM AS (10), BS (10), CS (10)
20 FOR J = 1 TO 10
30 READ AS (J), BS (J)
40 CS (J) = AS (J) + BS (J)
50 NEXT J
60 K = INT (10 * RND (1)) + 1
70 PRINT " ☒ WHAT IS MEANING OF THE WORD ? "
80 PRINT AS (K),
90 INPUT XS
100 AX$ = AS (K) + XS
110 IF CS (K) = AX$ THEN PRINT "O.K.!!" : FOR M = 1 TO 3000 : NEXT M : GOTO 150
120 PRINT "WRONG" : FOR M = 1 TO 1000 : NEXT M
130 PRINT " ☐ " ; SPACES (10) : PRINT " ☐ ☐ " ; TAB (12) ; SPACES (25)
140 PRINT " ☐ " : GOTO 80
150 K = INT (10 * RND (1)) + 1
160 PRINT " ☒ TRANSLATE TO FRENCH"
170 PRINT BS (K),
180 INPUT Y$
190 YB$ = Y$ + BS (K)
200 IF CS (K) = YB$ THEN PRINT "O.K.!!" : FOR M = 1 TO 3000 : NEXT M : GOTO 60
210 PRINT "WRONG" : FOR M = 1 TO 1000 : NEXT M
220 PRINT " ☐ " ; SPACES (10) : PRINT " ☐ ☐ " ; TAB (12) ; SPACES (25)
230 PRINT " ☐ " : GOTO 170
240 END
250 DATA CHAT, CAT, PORTE, DOOR, MAISON, HOUSE, CHIEN
260 DATA DOG, CANARD, DUCK, POISSON, FISH, MAIN, HAND
270 DATA FENETRE, WINDOW, FILLETTE, GIRL, FEMME
280 DATA WIFE
RUN
WHAT IS MEANING OF THE WORD ?
POISSON      ?

```



In this case, the question about the meaning of poisson is answered by keying-in that English. Display of O.K.!! is on the CRT screen to indicate you are correct. For any other answer, error display is made. Conversely, furthermore, there is the case when you answer "POISSON" when asked about translation.

■ Secondary Array is More Powerful

Let's look at this table (bottom right) which is an improvement on the test result table (bottom left) of mathematics, English and French for 3 students.

Name	John	Peter	Paul
Subject	John	Peter	Paul
Mathematics.	92	75	72
English	70	94	78
French	65	60	95

Student	John	Peter	Paul
M	1	2	3
N	1	2	3
Mathematics.	A (1, 1)	A (1, 2)	A (1, 3)
English	A (2, 1)	A (2, 2)	A (2, 3)
French	A (3, 1)	A (3, 2)	A (3, 3)

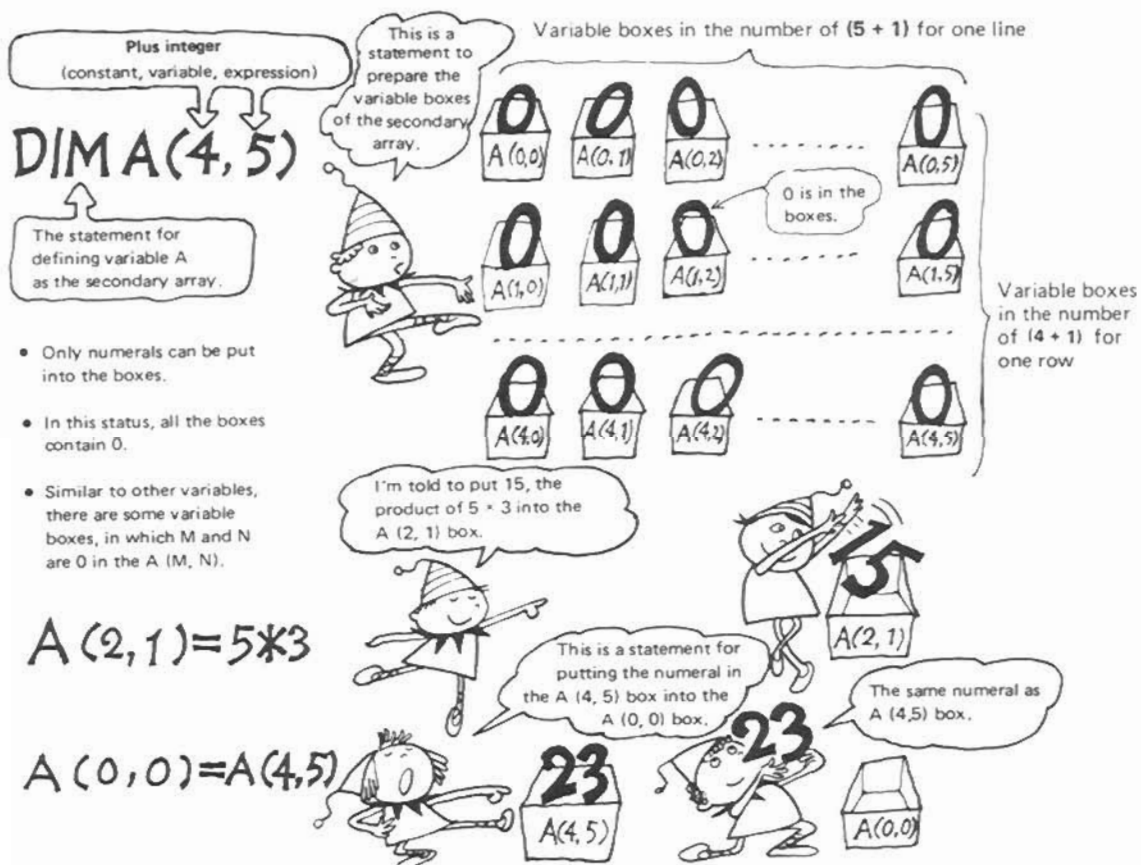
M = 1 ... Mathematics
M = 2 ... English
M = 3 ... French
N = 1 ... John
N = 2 ... Peter
N = 3 ... Paul

In the table at right, the subject, student and marks are expressed as $M (1 - 3)$, $N (1 - 3)$ and $A (M, N)$, respectively. This is very convenient, for example, as is evident in the following:

$A (2, 3) \dots M = 2$ means English, $N = 3$ means Paul \dots English marks of Paul

Simple! Writing $A (2, 3)$ alone gives a clear description of the English mark of Paul. M and N in the $A (M, N)$ represent separate items. Writing $A (M, N)$ using two items is called the secondary array. Two items used mean secondary array. The primary array previously described has one item.

Now, look at how this secondary array can be used in the program for the computer.



Two Exercises

Determine the value and curve of SIN when an angle varies from 0 degree to 180 degrees with 10 degree increments.

```

10 PRINT "  " ; TAB (5) ; "SIN"
20 PRINT
30 FOR K = 0 TO 180 STEP 10
40 X = K *  $\pi$  / 180 : Value in degree unit is
50 S = SIN (X)           changed to radian.
60 A = INT (10 * S)
70 PRINT K ; TAB (4) ;
80 PRINT S ; TAB (18 + A) ;
90 FOR J = 0 TO A
100 PRINT " * " ;
110 NEXT J
120 PRINT
130 NEXT K
140 END

```



Execute the above program. This graph is rather rough. As for drawing graphs, a lot of examples will be described later so that you can learn more about them.

Now, let's generate a program to determine the prime numbers. A prime number is the one that cannot be divided by any integer smaller than itself, except for 1. Since the first prime number is 2, the multiples of 2, namely, even number larger than 2 are not prime numbers. To use variables with subscripts effectively, even numbers are excluded from the start.

```

10 DIM P (255)
20 FOR J = 0 TO 255
30 P (J) = J * 2 + 3 : NEXT J
40 FOR K = 0 TO SQR (255)
50 IF P (K) = 0 THEN 90
60 KK = K + P (K)
70 FOR L = KK TO 255 STEP P (K)
80 P (L) = 0 : NEXT L
90 NEXT K
100 PRINT 2 ;
110 FOR M = 0 TO 255
120 IF P (M) = 0 THEN 140
130 PRINT P (M) ;
140 NEXT M
150 END

```

Substitute 256 odd numbers from 3 to 513 for the parenthesis of variable P with a subscript.

Find prime numbers from the small in value and substitute 0 for the values of the multiples in the parenthesis of P.

The only even number "2" is first displayed, and then the values of P () which are not 0, namely, prime numbers are on display.

This program is a bit complex, isn't it? Note that the multiples of the prime number are excluded from the start. Details on structured programming of prime numbers will be described later.

■ Here's Advice on how Lists can be made

Names are sorted out when making a list of members. The use of a convenient program, if any, facilitates listing of any kind.

Here you learn how to sort strings for address books, telephone numbers or housekeeping account books.

```

10 PRINT "HOW MANY PERSONS ARE SORTED?"
20 INPUT X
30 DIM N$ (X)
40 PRINT "KEY - IN NAMES ONE BY ONE"
50 PRINT "BUT IF 0, JOB DISCONTINUED!"
60 FOR A = 1 TO X : AS = STR$ (A)
70 PRINT "NAME PLEASE " ; " (" ; AS ; " ) "
80 INPUT N$ (A)
90 IF N$ (A) = "0" THEN 110
100 NEXT A
110 A = A - 1
120 FOR B = 1 TO A - 1
130 FOR C = 1 TO A - B
140 D = LEN (N$ (C)) : E = LEN (N$ (C + 1)) : F = 1 : IF D < E THEN E = D
142 X = ASC (MID$ (N$ (C), F, 1))
143 Y = ASC (MID$ (N$ (C + 1), F, 1)) : IF X > Y THEN 150
144 IF X < Y THEN 180
145 IF (E = F) * (D = E) THEN 180
146 IF (E = F) * (D > E) THEN 150
148 F = F + 1 : GOTO 142
150 K$ = N$ (C)
160 N$ (C) = N$ (C + 1)
170 N$ (C + 1) = K$
180 NEXT C, B
190 PRINT
200 FOR B = 1 TO A
210 PRINT N$ (B)
220 NEXT B
230 PRINT : END

```

Name is keyed-in.

The order is substituted.

Result is displayed.

Original List (Keyed-in)

TOM BROWN
HAROLD GREEN
JIM JONES
ANNE MILLER
TOM CARTER
ELICE THOMAS



Sorted List

ANNE MILLER
ELICE THOMAS
HAROLD GREEN
JIM JONES
TOM BROWN
TOM CARTER



■ Cards if Dealt by a Poker Player

The computer deals cards for you. It shuffles them correctly using random numbers, causing no trickery to occur.

```

10 DIM X (4, 13)
20 C = 0
30 PRINT : FOR A = 1 TO 5
40 GOSUB 90 : PRINT : NEXT A : PRINT
50 PRINT "IS YOUR HAND ALRIGHT WITH THESE CARDS?"
60 INPUT "ALL RIGHT (1), GIVE ME NEXT (2) ?" : A
70 ON A GOTO 400, 30
80 GOTO 50
90 C = C + 1 : IF C = 51 THEN 500
100 M = INT (4 * RND (1)) + 1
110 N = INT (13 * RND (1)) + 1
120 IF X (M, N) = -1 THEN 100
130 X (M, N) = -1
140 IF N = 1 THEN PRINT "ACE : " : GOTO 180
150 IF N = 10 THEN PRINT N ; TAB (5) ; " : " : GOTO 180
160 IF N < 10 THEN PRINT N ; TAB (5) ; " : " : GOTO 180
170 ON N - 10 GOTO 200, 210, 220
180 ON M GOTO 300, 310, 320, 330
200 PRINT "JACK : " : GOTO 180
210 PRINT "QUEEN : " : GOTO 180
220 PRINT "KING : " : GOTO 180
300 A$ = "♠" : GOTO 340
310 A$ = "♥" : GOTO 340
320 A$ = "♦" : GOTO 340
330 A$ = "♣" : GOTO 340
340 FOR B = 1 TO N
350 PRINT A$ ;
360 NEXT B
370 RETURN
400 PRINT
410 PRINT "THEN I RESHUFFLE."
420 FOR M = 1 TO 4 : FOR N = 1 TO 13
430 X (M, N) = 0
440 NEXT N, M : GOTO 20
500 PRINT
510 PRINT "TWO CARDS REMAIN ... DO YOU CONTINUE ?"
520 INPUT "YES (1), NO (2) ?" : B
530 ON B GOTO 400, 550
540 GOTO 510
550 END

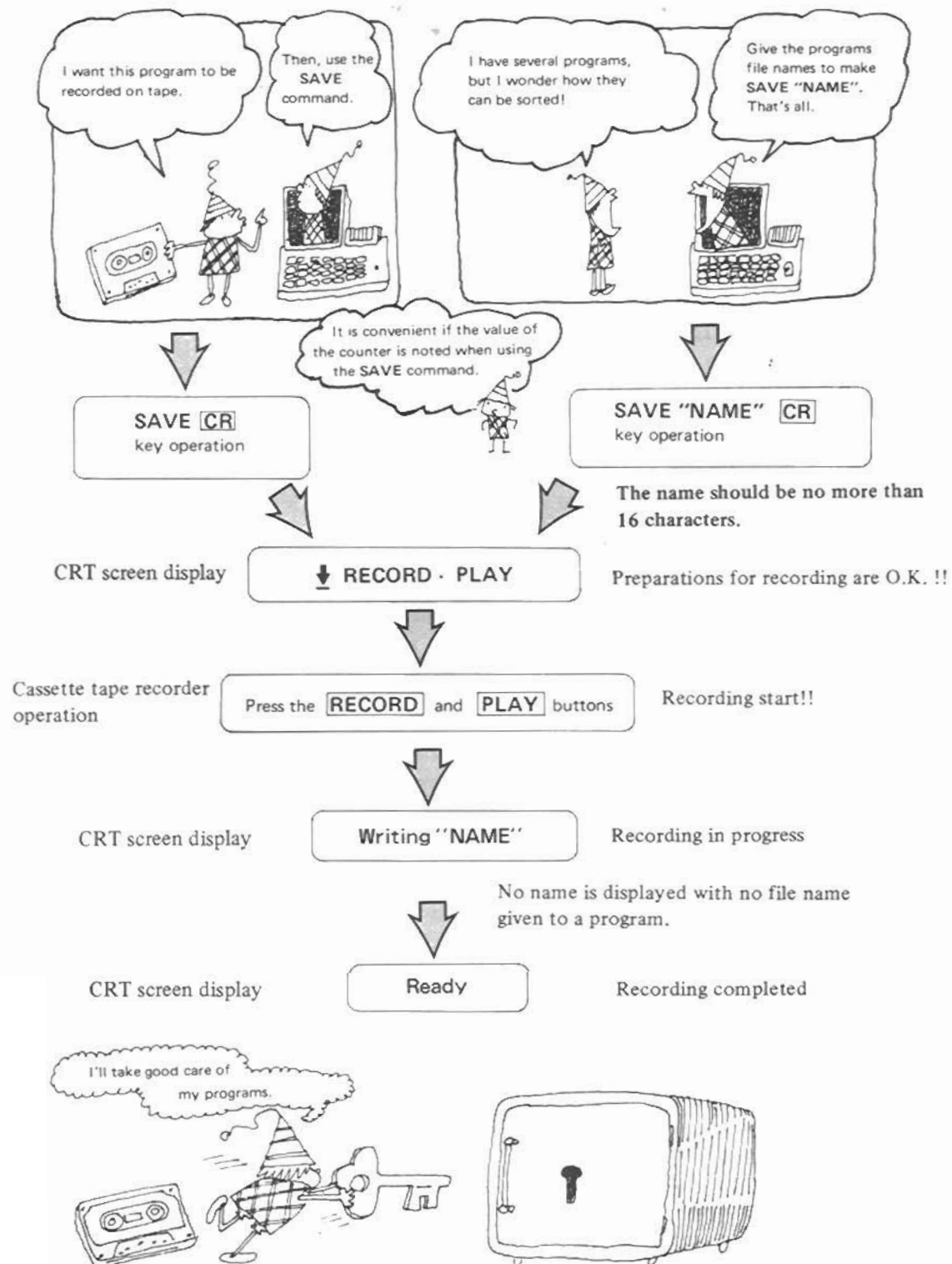
```



Points of the program:

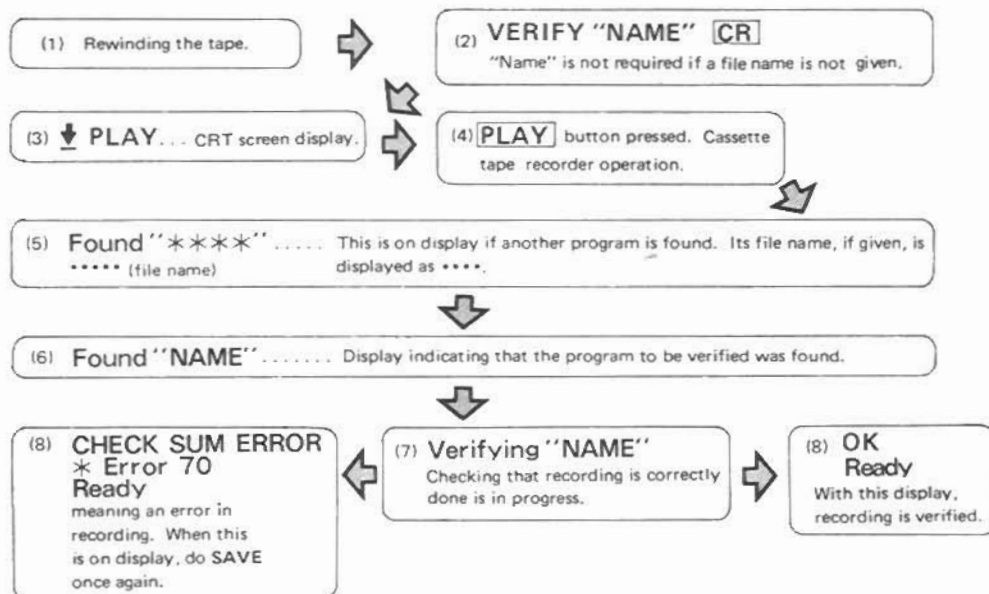
Line number 100 and 110	Turning up a new card.
Line number 120	If a newly turned up card has been previously turned up, another card is to be turned up.
Line number 130	Mark dealt cards with "-1".
Line numbers from 420 to 440	All cards are collected and their marks are returned to "0".

■ Program Recording (SAVE)

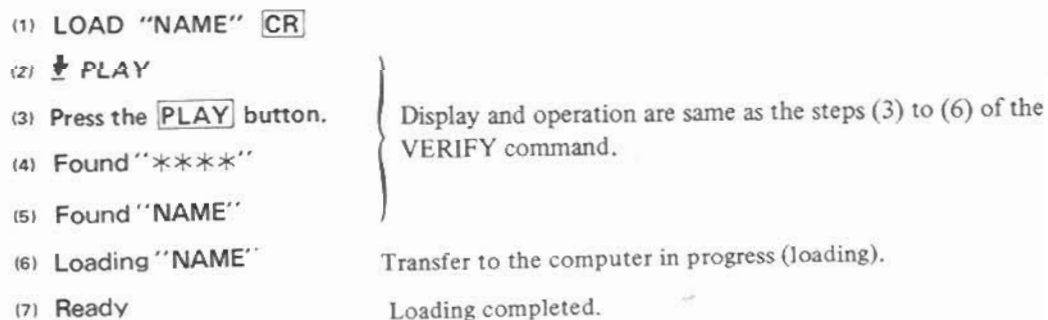
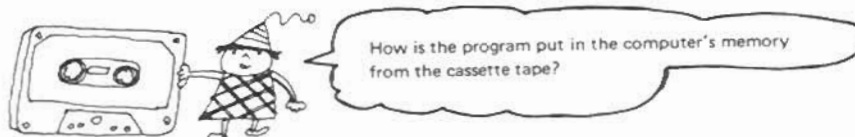


■ Use of VERIFY and LOAD Commands

Verify



Load



■ Data can also be Stored on Cassette Tape

Data storage is also required if programs can be stored

To do so, 5 more statements must be learned. Then, a cassette tape can be used as a storage of data.

- WOPEN/T** This prepares for data writings. It also serves to name a group of data.
PRINT/T Identical in use to the PRINT statement, this writes data on a cassette tape.
ROPEN/T This statement prepares for data readouts. It serves to find a data group with the name given.
INPUT/T Identical in use to the INPUT statement, this reads data out of the cassette tape.
CLOSE/T This statement must be executed before ROPEN if WOPEN is executed or before WOPEN if ROPEN is executed.

To store data, numerals from 1 to 99 are first written on a cassette tape. The "DATA" at line number 10 is the name given to a group of data to be written. A maximum of 16 characters can be used to name a group of data. Of course, it is unnecessary to have a name if so desired.

```
10 WOPEN/T "DATA"
20 FOR X = 1 TO 99
30 PRINT/T X
40 NEXT X
50 CLOSE/T
60 END
```

Now, it is time to read the data which has just been written. First, rewind the cassette tape, then execute the following:

```
10 WOPEN/T "DATA"
20 FOR X = 1 TO 99
30 INPUT/T A
40 PRINT A
50 NEXT X
60 CLOSE/T
70 END
```



Why not execute the above program again with 100 substituted for 99 at line number 20? An error will occur this time. Because the 100th data was not originally written. It is impossible to memorize the written data counts. For this, a numeral, for example, -99999999 unrelated to that use for data is written as a mark at the end of written data.

```
10 WOPEN/T "DATA"
20 FOR X = 1 TO 99
30 PRINT/T X
40 NEXT X
50 PRINT/T -99999999
60 CLOSE/T
70 END
```

```
10 ROPEN/T "DATA"
20 FOR X = 1 TO 200
30 INPUT/T A
40 IF A = -99999999 THEN 70
50 PRINT A
60 NEXT X
70 CLOSE/T
80 END
```

■ Technique to Memorize a Music History

Statements for data storage and readouts can also be used for strings.

The five composer's names are written on the cassette tape and read out of it.

```

10 DIM N$ (5)
20 N$ (1) = "BACH"
30 N$ (2) = "MOZART"
40 N$ (3) = "BEETHOVEN"
50 N$ (4) = "CHOPIN"
60 N$ (5) = "BRAHMS"
70 WOPEN/T "GREAT MUSICIANS"
80 FOR J = 1 TO 5
90 PRINT/T N$ (J)
100 NEXT J
110 CLOSE/T
120 END

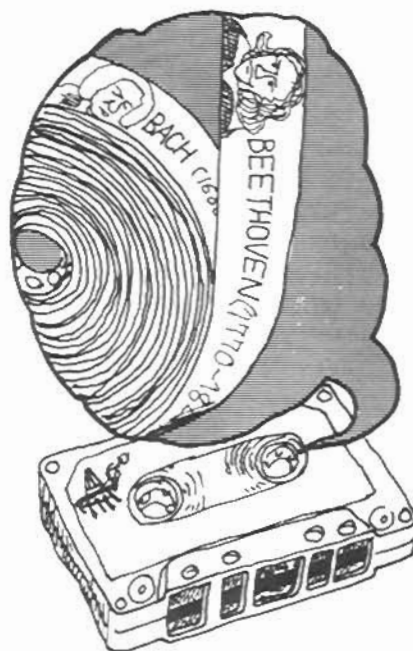
```

This is identical to numeric data writing. Then, readouts are done as follows:

```

200 DIM M$ (5)
210 ROPEN/T "GREAT MUSICIANS"
220 FOR K = 1 TO 5
230 INPUT/T M$ (K)
240 PRINT M$ (K)
250 NEXT K
260 CLOSE/T

```



With this, writing and readout are completed. As you may have noticed, the name of string variable N\$ used for writing is different from that of string variable M\$ used for readouts. Since the value itself is written in the cassette tape as data, it has nothing to do with the name of the substituted variable. This makes it possible to change the variable name in the program as long as the string data is read by the string variable and the numeral data by the numeral variable.

Now, from what you have learnt so far, let's generate a data file with mixed numeric and string data. To also write the years when the previous composers died, for example, the following statements should be modified from the previous program.

```

15 DIM D (5)
65 D (1) = 1750 : D (2) = 1791 : D (3) = 1827
67 D (4) = 1849 : D (5) = 1897
90 PRINT/T N$ (J), D (J)

```

It is clear from the above that the generated file stores string and numeric data in alternate sequence. Accordingly, the readouts of the file must match the alternate sequence, for which line numbers 200, 230 and 240 should be modified as follows:

```

200 DIM M$ (5), T (5)
230 INPUT/T M$ (K), T (K)
240 PRINT M$ (K), T (K)

```

With those statements remaining unmodified, the numeric data is transferred to the string variable M\$ (), causing an error to occur.

List of School Work Results

This is a program for recording the results of French, English and science for a certain class.

```

10 INPUT "HOW MANY STUDENTS IN THE CLASS? "; N
20 DIM N$(N), K(N), E(N)
30 DIM R(N)
40 A$ = " (MARKS) "
50 FOR X = 1 TO N
60 PRINT : PRINT X
70 INPUT "NAME PLEASE ? "; N$(X)
80 PRINT "FRENCH "; A$ ; : INPUT K(X)
90 PRINT "ENGLISH "; A$ ; : INPUT E(X)
100 PRINT "SCIENCE "; A$ ; : INPUT R(X)
120 NEXT X
130 WOPEN/T " RESULT "
140 PRINT/T N
150 FOR X = 1 TO N
160 PRINT/T N$(X), K(X), E(X), R(X)
170 NEXT X
180 CLOSE/T

```



← For writing a data group named "RESULT".
 ← Writing the number of students in the class.
 ← Writing the marks for students.
 ← Writing completed.

Now, let's read the written data of results, and calculate the mean of individual students' points and the mean of each subject.

```

10 ROPEN/T " RESULT "
20 INPUT/T N
30 DIM N$(N), K(N), E(N)
40 DIM R(N)
50 FOR X = 1 TO N
60 INPUT/T N$(X), K(X)
70 INPUT/T E(X), R(X)
80 NEXT X
90 CLOSE/T
100 PRINT TAB(12); " FRENCH ";
110 PRINT TAB(19); " ENGLISH ";
120 PRINT TAB(27); " SCIENCE ";
130 PRINT TAB(34); " MEAN "
140 FOR X = 1 TO N
150 PRINT N$(X); TAB(11); K(X);
160 PRINT TAB(18); E(X);
170 PRINT TAB(26); R(X);
190 PRINT TAB(33); INT(10/3 * (K(X) + E(X) + R(X))) / 10
200 K(0) = K(0) + K(X) : E(0) = E(0) + E(X)
210 R(0) = R(0) + R(X)
220 NEXT X : PRINT " MEAN " ;
230 PRINT TAB(11); INT(10 * (K(0) / N)) / 10 ;
240 PRINT TAB(18); INT(10 * (E(0) / N)) / 10 ;
250 PRINT TAB(26); INT(10 * (R(0) / N)) / 10
260 END

```

← For finding the data group named " RESULT ".
 ← Readouts of the number of students in the class.
 ← Readouts of the name and marks for French.
 ← Readouts of marks for English and science.
 ← Readouts completed.

■ Music Library Kept on Tapes

This data file is indispensable to generate a "Music Library" as discussed in the paragraph "MUSIC Statement".

Data for tunes is string data consisting of various symbol groups. If a data group is named per tune, any tune can be picked out of those recorded on the tape when its name is designated.

For example, a tune can be picked up from this music library for use in the music box of your timer, with some modifications. The tunes in the music library can also be used for programs of games and graphics, providing a number of applications.

Molto Vivace



F. Chopin "Puppy Waltz"

To write the etude of F. Kroepsch used on page 79 into a data file, the following changes must be made:

```
300 WOPEN/T "ETUDE"
310 PRINT/T J1$, J2$, J3$, N1$, N2$, N3$
320 CLOSE/T
```

Attention is required to the fact that the character count for data writing should be within 255 characters. If written as follow;

```
305 MAS = J1$ + J2$ + J3$ : MBS = N1$ + N2$ + N3$
310 PRINT/T MAS, MBS
```

the contents of string variables MAS and MBS exceed 255 characters, which make data writing incomplete.

The length of string data may vary with each tune, therefore it is necessary to write data indicating the end of each tune so that a data error does not occur in data readouts. End mark of tune " ■■ ", for example makes each tune consist of string data within 100 characters for execution give below:

```
500 ROPEN/T "PUPPY WALTZ"
510 FOR A = 1 TO 100
520 INPUT/T M$ (A)
530 IF M$ (A) = " ■■ " THEN 550
540 NEXT A
550 CLOSE/T
```

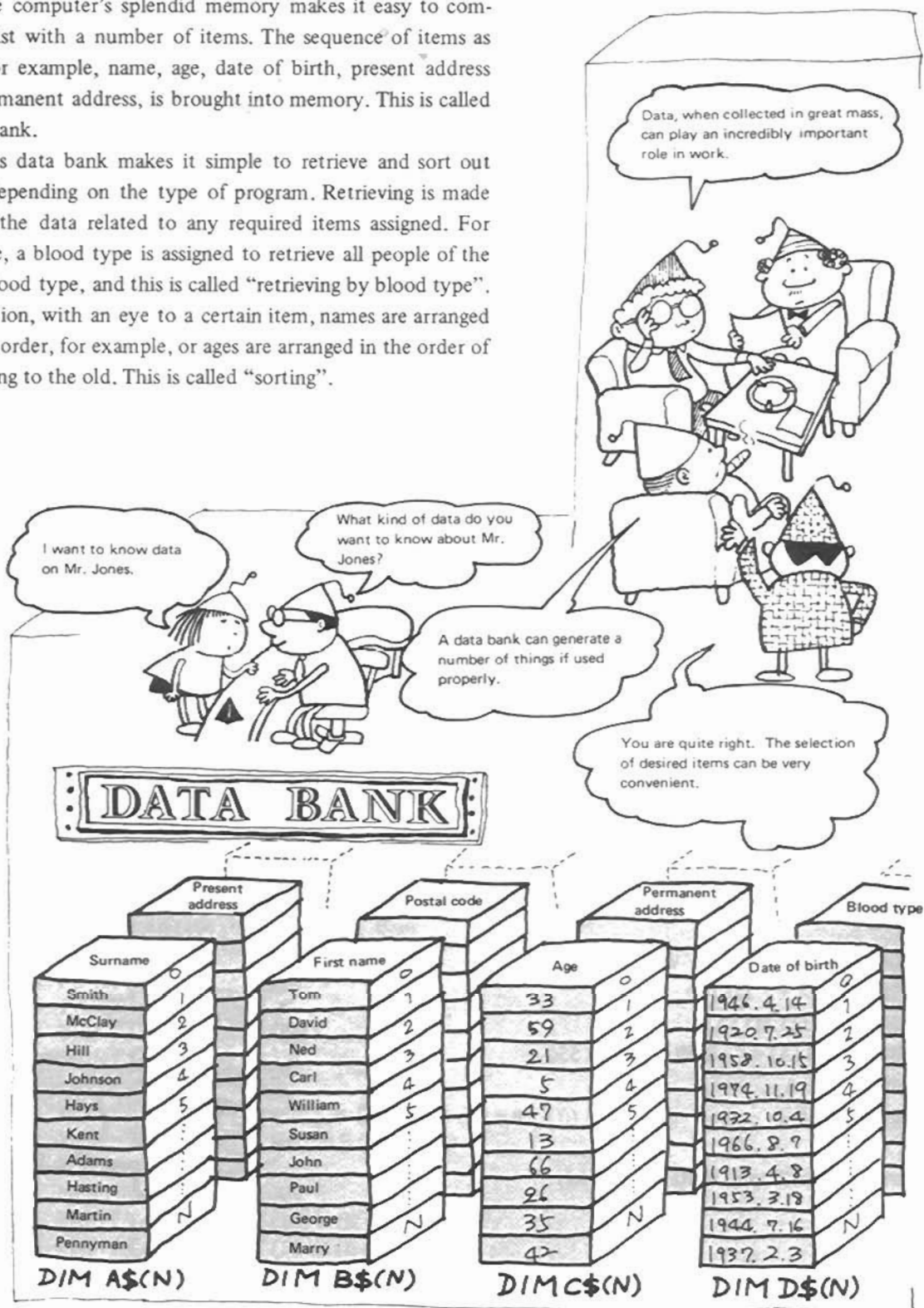
This can read the "Puppy Waltz" completely.



■ Data Dank is a Computer's Speciality

The computer's splendid memory makes it easy to compile a list with a number of items. The sequence of items as data, for example, name, age, date of birth, present address and permanent address, is brought into memory. This is called a data bank.

This data bank makes it simple to retrieve and sort out items depending on the type of program. Retrieving is made for all the data related to any required items assigned. For example, a blood type is assigned to retrieve all people of the same blood type, and this is called "retrieving by blood type". In addition, with an eye to a certain item, names are arranged in ABC order, for example, or ages are arranged in the order of the young to the old. This is called "sorting".



■ Telephone Number List is also a Data Bank

With the above understood, a summary is made of the program in which string data is put into the memory of the DATA statement. Based on this program, modifications are possible so that the address and postal code are also available.

```

10 N = 12
20 DIM MS (N) ..... Surname
30 DIM NS (N) ..... First name
40 DIM AS (N) ..... Home dialling code
50 DIM BS (N) ..... Home telephone number
60 DIM CS (N) ..... Work dialling code
70 DIM DS (N) ..... Work telephone number
80 DIM F (N)
90 FOR K = 1 TO N
100 READ MS (K), NS (K)
110 READ AS (K), BS (K)
120 READ CS (K), DS (K)
130 NEXT K
140 PRINT : PRINT : X = 0
150 PRINT "WHAT IS THE SURNAME" ;
160 INPUT XS ..... Key-in the name to be retrieved.
170 FOR K = 1 TO N
180 IF MS (K) = XS THEN X = X + 1 : F (X) = K .. Retrieving by use of the surname.
190 NEXT K
200 IF X <> 0 THEN 240
210 PRINT "NO RELEVANT PERSON FOUND !"
220 PRINT "PLEASE RE - ENTER"
230 GOTO 140
240 PRINT : PRINT
250 FOR K = 1 TO X
260 L = F (K) ..... For display of persons with the same surname.
270 PRINT "NAME" ; TAB (11) ; " : " ; NS (L) ; " " ; MS (L)
280 PRINT "HOME NUMBER : " ;
290 PRINT "(" ; AS (L) ; ")" ; BS (L)
300 PRINT "WORK NUMBER : " ;
310 PRINT "(" ; CS (L) ; ")" ; DS (L) : PRINT
320 NEXT K
330 GOTO 140
340 DATA JONES, JOHN, 01, 364, 9617, 01, 969, 3678
350 DATA DAVIS, PETER, 021, 396, 2137, 01, 323, 6146
360 DATA SMITH, PAUL, 0449, 73246, 0449, 71277
370 DATA JONES, DAVID, 061, 631, 1235, 061, 312, 1975
380 DATA RICHARDS, ROBIN, 0273, 61976, 0903, 47216
390 DATA SMITH, HARRY, 01, 638, 2174, 29, 147636
400 DATA LAKE, COLIN, 4967, 13642, 4967, 32132
410 DATA WATSON, JOHN, 01, 961, 2431, 0427, 21369
420 DATA CARTER, DAVID, 6317, 21974, 01, 316, 2638
430 DATA HOMLES, FRANK, 2238, 76194, 2238, 78352
440 DATA JONES, FRED, 9743, 61665, 01, 424, 6913
450 DATA WILSON, JAMES, 01, 692, 5687, 0374, 68421
460 END

```



■ SOS in Morse Code

The Morse code was invented by Samuel F. Breese Morse, an American artist, in 1838, and is one of the most important communications media even today.

The principle is simple. It sets up the ratio of times when a specified wave of frequency is produced and not produced.

Prolonged sound — Transmission of sound 3 times as long as short sound.

Short sound —

Pause No sound for the same period of time as short sound.

The Morse code is based on the combination of these three sounds to represent the necessary symbols. Shown below is part of the Morse code, according to which try to strike SOS. Very difficult? The Morse code requires practice until your fingers move naturally and quickly without thinking of where to press.

To make this easy, the program for the Morse code is generated in the following section. Line numbers 20 to 270 are strings to generate signals from A to Z, and line numbers 290 to 380 for those from 0 to 9. Brief description is given of the program.

+A5 ———> Sound A (1a) in the high frequency range with its tonal length of 5 (equivalent to the prolonged signal of the Morse code).

+A2 ———> Sound A (1a) in the high frequency range with its tonal length of 2 (equivalent to the short signal of the Morse code).

R2 ———> Pause with no sound with its length of 2.

A	—	G	— — —	N	— ·	T	—	Z	— — — ·	6	— — — —
B	— · — ·	H	— · — ·	O	— — —	U	— —	1	— — — —	7	— — — ·
C	— · — —	J	— — —	P	— · — ·	V	— · —	2	— — — —	8	— — — ·
D	— · —	K	— —	Q	— — · —	W	— —	3	— — — —	9	— — — ·
E	·	L	— · —	R	— · —	X	— · —	4	— — — —	0	— — — —
F	— · —	M	— —	S	— · —	Y	— — —	5	— — — —	1	—



■ Signals in Dots and Dashes

```

10 DIM A1 (100), M$ (127)
20 M$ (65) = "+A2R2+A5"
30 M$ (66) = "+A5R2+A2R2+A2R2+A2"
40 M$ (67) = "+A5R2+A2R2+A5R2+A2"
50 M$ (68) = "+A5R2+A2R2+A2"
60 M$ (69) = "+A2"
70 M$ (70) = "+A2R2+A2R2+A5R2+A2"
80 M$ (71) = "+A5R2+A5R2+A2"
90 M$ (72) = "+A2R2+A2R2+A2R2+A2"
100 M$ (73) = "+A2R2+A2"
110 M$ (74) = "+A2R2+A5R2+A5R2+A5"
120 M$ (75) = "+A5R2+A2R2+A5"
130 M$ (76) = "+A2R2+A5R2+A2R2+A2"
140 M$ (77) = "+A5R2+A5"
150 M$ (78) = "+A5R2+A2"
160 M$ (79) = "+A5R2+A5R2+A5"
170 M$ (80) = "+A2R2+A5R2+A5R2+A2"
180 M$ (81) = "+A5R2+A5R2+A2R2+A5"
190 M$ (82) = "+A2R2+A5R2+A2"
200 M$ (83) = "+A2R2+A2R2+A2"
210 M$ (84) = "+A5"
220 M$ (85) = "+A2R2+A2R2+A5"
230 M$ (86) = "+A2R2+A2R2+A2R2+A5"
240 M$ (87) = "+A2R2+A5R2+A5"
250 M$ (88) = "+A5R2+A2R2+A2R2+A5"
260 M$ (89) = "+A5R2+A2R2+A5R2+A5"
270 M$ (90) = "+A5R2+A5R2+A2R2+A2"
280 REM NO.
290 M4 (48) = "+A5R2+A5R2+A5R2+A5R2+A5"
300 M$ (49) = "+A2R2+A5R2+A5R2+A5R2+A5"
310 M$ (50) = "+A2R2+A2R2+A5R2+A5R2+A5"
320 M$ (51) = "+A2R2+A2R2+A2R2+A5R2+A5"
330 M$ (52) = "+A2R2+A2R2+A2R2+A2R2+A5"
340 M$ (53) = "+A2R2+A2R2+A2R2+A2R2+A2"
350 M$ (54) = "+A5R2+A2R2+A2R2+A2R2+A2"
360 M$ (55) = "+A5R2+A5R2+A2R2+A2R2+A2"
370 M$ (56) = "+A5R2+A5R2+A5R2+A2R2+A2"
380 M$ (57) = "+A5R2+A5R2+A5R2+A5R2+A2"
390 REM "SPACE"
400 M$ (32) = "R5"
1000 INPUT "TYPE IN A MESSAGE "; A$
1010 FOR J = 1 TO LEN (A$)
1020 A1 (J) = ASC (MID$ (A$, J, 1))
1030 NEXT J
1040 FOR J = 1 TO LEN (A$)
1050 MUSIC M$ (A1 (J)), "R5"
1060 NEXT J
1070 GOTO 1000

```



Key in alphabet from A to Z and munerals from 0 to 9. For example, when you key-in "I LOVE YOU", the Morse code will be generated accordingly. Using the Morse code, you can declare your love to your sweetheart!

■ Unending "Time".....

At the end of this introduction to the BASIC Language, the program for the "Perpetual Calendar" is introduced. It requires no detailed explanation. Our "time" continues eternally.

```

5  DIM M$(12), W$(7)
10 FOR K = 1 TO 12 : READ M$(K) : NEXT K
20 FOR K = 1 TO 7 : READ W$(K) : NEXT K
30 INPUT "YEAR PLEASE ? " ; Y : INPUT "MONTH PLEASE ? " ; MT
40 H = MT : GOSUB 400 : K2 = YB + 1
50 H = MT + 1 : GOSUB 400 : K1 = YB + 1
60 N = K1 - K2 : IF N >= 0 THEN L = 28 + N : GOTO 70
65 L = 35 + N
70 IF MT = 12 THEN L = 31
75 PRINT "  " : GOSUB 190
80 PRINT TAB(8) ; Y ; "  " ; M$(MT) : PRINT : T = 4
90 FOR N = 1 TO 7 : PRINT TAB(T) ; W$(N) ; : T = T + 4 : NEXT N : PRINT
100 T = 0 : IF K2 = 0 THEN 120
110 FOR N = 1 TO K2 : PRINT TAB(T) ; : T = T + 4 : NEXT N : T = T - 4
120 FOR N = 1 TO L : N$ = STR$(N) : J = LEN(N$)
130 PRINT TAB(T + 5 - J) ; N$ ; : T = T + 4
140 IF T = 28 THEN T = 0 : PRINT
150 NEXT N
160 IF T <> 0 THEN PRINT
170 GOSUB 190
180 PRINT "  " : GOTO 30
190 FOR Z = 1 TO 31 : PRINT " * " ; : NEXT Z : PRINT : RETURN
200 DATA JAN, FEB, MAR, APR, MAY, JUN
210 DATA JUL, AUG, SEP, OCT, NOV, DEC
220 DATA SUN, MON, TUE, WED, THU, FRI, SAT
230 END
400 X = Y
410 N = H - 3 : J = 12 : GOSUB 600 : MM = Z
420 IF MM > 9 THEN X = X - 1
430 N = X : J = 400 : GOSUB 600 : X = Z
440 X4 = INT(X/4) : X1 = INT(X/100)
450 KY = X + X4 - X1
460 N = MM : J = 5 : GOSUB 600 : MZ = Z
470 M5 = INT(MM/5) : M2 = INT(MZ/2)
480 N = MZ : J = 2 : GOSUB 600 : P = Z
490 KM = 13 * M5 + 5 * M2 + 3 * P
500 N = KY + KM + 3 : J = 7 : GOSUB 600 : YB = Z
510 RETURN
600 REM Z = N, J
610 K = INT(N/J)
620 Z = N - K * J
630 IF Z < 0 THEN Z = Z + J
640 RETURN

```



```

*****
1980  JAN
SUN MON TUE WED THU FRI SAT
  6   7   8   9  10  11  12
 13  14  15  16  17  18  19
 20  21  22  23  24  25  26
 27  28  29  30  31
*****

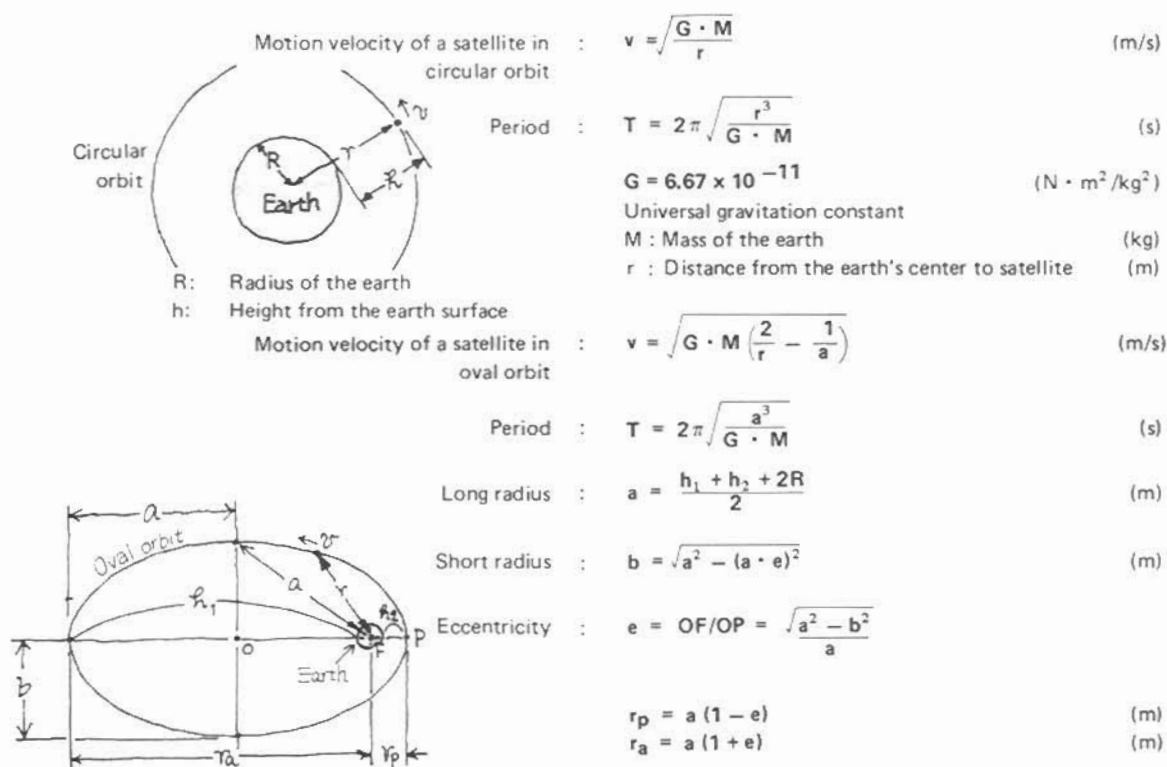
```

YEAR PLEASE ■

■ Miniature Space Dictionary

If you are interested in space, including astronomy and man-made satellites, you might like to try calculations and graphic drawings by using the computer. Shown below are equations and values required for such attempts.

Unlike the earth, the movement of objects in space should mathematical calculations without any complexity caused by atmospheric resistance. For more accurate values, however, consideration must be given to the effects by the planets, the perturbation caused by strains in the form of the earth and gas pressure in space, even though rarefied. There is air of 10^{-9} mmHg at an altitude of 800 km in space, for example. In addition, a man-made satellite stationed at an altitude of approx. 36,000 km tilts approximately 1 degree per year in its orbit being affected by other heavenly bodies.



	Mass (1 for the Sun)	Equatorial radius	Eccentricity	Averaged distance from the Sun (a)
Sun	1.000	696 000 km	—	—
Mercury	0.166×10^{-6}	2 440	0.20563	0.57910×10^8 km
Venus	2.448×10^{-6}	6 056	0.00678	1.08210 "
Earth	30.034×10^{-7}	6 378	0.01672	1.49600 "
Mars	3.227×10^{-7}	3 390	0.09338	2.27944 "
Jupiter	95.479×10^{-5}	71 400	0.04829	7.7834 "
Saturn	2.856×10^{-4}	60 400	0.05604	14.2700 "
Uranus	4.373×10^{-5}	23 700	0.04613	28.7103 "
Neptune	5.178×10^{-5}	25 110	0.01004	44.971 "
Pluto	0.552×10^{-6}	3 400	0.24842	59.136 "
Moon	3.694×10^{-8}	1 738	0.0549*	384 400 km*

Mass of the Sun = 1.991×10^{30} kg

* Value to the earth

Source: Chronological Table of Science

■ Programming

Let's formulate an algorithm for solving systems of simultaneous linear equations.

- 1 Assign the number of unknowns to variable N.
Prepare a 1-dimensional array X(N) to store the value of the unknowns.
Prepare a 2-dimensional array A(N, N+1) and to it assign the coefficients of the equations in (1).
- 2 Call the subroutine for solving systems of simultaneous linear equations.
- 3 Print the values of the unknowns (that is, the contents of X(1) to X(N)) on the CRT display unit.

Subroutine (for solving systems of simultaneous linear equations)

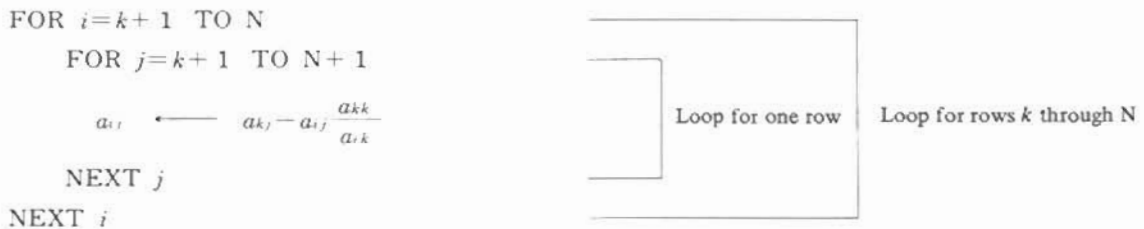
- 4 Perform the elimination process N-1 times to change the contents of array A to the coefficients of the equations in (4).
- 5 Find the values of the unknowns in sequence and assign them to X(N) through X(1).

In this manner, we can clearly identify the subroutine (for solving systems of simultaneous linear equations) as a basic module which takes the value of variable N as the number of unknowns and the contents of 2-dimensional array A(N, N+1) as the coefficients of the equations in (1), finds values of the unknowns, and assign them to array X(N) from X(N) to X(1).

First, let's consider step [4] which is the most important elimination step. As seen from the equations in (2), the kth ($k = 1$ to $N-1$) elimination process is carried out basically by repetitions of assignment

$$a_{ij} \leftarrow a_{ij} - a_{ik} \frac{a_{kk}}{a_{ik}} \quad (\leftarrow \text{denotes assignment.}) \quad (5)$$

for coefficient a_{ij} . This can be accomplished by executing the 2-level loop



Step [4] can be programmed in this way using variables K, I, and J as follows:

```

FOR K=1 TO N-1
  FOR I=K+1 TO N
    FOR J=K+1 TO N+1
       $A(I, J) = A(K, J) - A(I, K) * A(K, K) / A(I, K)$ 
    NEXT J
  NEXT I
NEXT K

```

These statements can be combined to one as NEXT J, I, K.

Now proceed to step [5], where the values of the unknowns are found in sequence. To find the value of unknown x_i , all that is required is to assign x_{i+1} through x_n to the set of equations in (5). Consequently, we obtain

```

FOR j=i+1 TO N
   $a_{i,N+1} \leftarrow a_{i,N+1} - a_{ij} x_j$ 
NEXT j
 $x_i \leftarrow a_{i,N+1} / a_{ii}$ 

```

Although the program code

```

FOR I=N TO 1 STEP -1
  FOR J=I+1 TO N
    A(I, N+1)=A(I, N+1)-A(I, J)*X(J)
  NEXT J
  X(I)=A(I, N+1)/A(I, I)
NEXT I

```

seems satisfactory, in this program, when $I = N$, J has a value of $N + 1$ and the computer executes the statement within the loop controlled by J . As it stands, a dimensional overflow would occur at $X(J)$ or an incorrect answer would result (even when $X(N+1)$ is defined) if its content is nonzero. We can avoid such errors by placing the step for finding the value of unknown x_n outside of the loop; that is, by changing the I -controlled loop as follows:

```

X(N)=A(N, N+1)/A(N, N)
FOR I=N-1 TO 1 STEP -1
.....
NEXT I

```

We can complete the subroutine for solving systems of simultaneous linear equations by adding a RETURN statement to the end of the above program code.

The essential problem in step [1] is in how to assign the unknowns and the coefficients of the simultaneous linear equations in question to variable N and 2-dimensional array $A(N, N+1)$. Many methods are possible, such as using the READ and DATA statements; however, we have decided here to enter them one at a time from the keyboard.

In step [3], we decided to display the values of the unknowns on the CRT display unit in the format:

```

X1 = .....
X2 = .....
.....

```

These steps can be coded without difficulty. Finally, we obtain a complete program as follows:

```

5  REM ..... Read data
10 INPUT "Number of unknown numbers = "; N
20 DIM A(N, N+1), X(N)
30 FOR S1=1 TO N: FOR S2=1 TO N+1
40 INPUT A(S1, S2)
50 NEXT S2, S1
100 GOSUB 1000
195 REM ..... Print Xi
200 FOR J=1 TO N
210 PRINT "X": STR$(J); "=" ; X(J)
220 NEXT J
230 END
995 REM ..... Elimination
1000 FOR K=1 TO N-1
1010 FOR I=K+1 TO N
1020 FOR J=K+1 TO N+1
1030 A(I, J)=A(K, J)-A(I, K)*A(K, K)/A(I, K)
1040 NEXT J, I, K
1095 REM ..... Find Xi
2000 X(N)=A(N, N+1)/A(N, N)
2010 FOR I=N-1 TO 1 STEP -1
2020 FOR J=I+1 TO N
2030 A(I, N+1)=A(I, N+1)-A(I, J)*X(J)
2040 NEXT J
2050 X(I)=A(I, N+1)/A(I, I)
2060 NEXT I
2070 RETURN

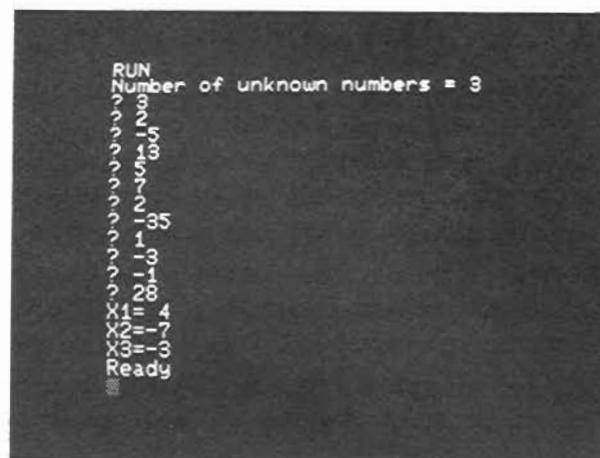
```


Run the program and solve the following system of simultaneous linear equations in three unknowns.

$$\begin{cases} 3x_1 + 2x_2 - 5x_3 = 13 \\ 5x_1 + 7x_2 + 2x_3 = -35 \\ x_1 - 3x_2 - x_3 = 28 \end{cases} \quad (6)$$

When the program is run and the coefficient values and the values which appear on the right-hand side of the equations are entered as shown in figure, the following solution is obtained:

$$\begin{aligned} x_1 &= 4 \\ x_2 &= -7 \\ x_3 &= -3 \end{aligned}$$



■ Exercises

1. Solve the following systems of simultaneous linear equations:

$$\begin{cases} x_1 + 2x_2 + 3x_3 - x_4 = 5 \\ x_1 - 3x_2 + 5x_3 + 2x_4 = 3 \\ -x_1 - 4x_2 + x_3 + 7x_4 = -7 \\ -x_1 + x_2 + 11x_3 - 3x_4 = -22 \end{cases}$$

$$\begin{cases} x_1 + 2x_2 + 4x_3 + 8x_4 + 16x_5 = 32 \\ x_1 + 3x_2 + 9x_3 + 27x_4 + 81x_5 = 243 \\ x_1 + 4x_2 + 16x_3 + 64x_4 + 256x_5 = 1024 \\ x_1 + 5x_2 + 25x_3 + 125x_4 + 625x_5 = 3125 \\ x_1 + 6x_2 + 36x_3 + 216x_4 + 1296x_5 = 7776 \end{cases}$$

The program constructed above does not specify the number of unknowns explicitly. In practice, however, the number of unknowns is restricted by the space available for defining the necessary arrays (or the size of the BASIC text area). If you use it on actual problems, however, you will find that the program can solve systems of simultaneous linear equations with more than 80 unknowns when it is used on the MZ-80A. Try solving various systems of simultaneous linear equations in which there are a large number of unknowns.

2. After running program several times, you may encounter the error message

* Error 2 in 1030.

which indicates that an overflow error occurred during execution of line number 1030. This condition will occur, for example, if an attempt is made to solve the system of simultaneous linear equations:

$$\begin{cases} x_1 + 2x_2 + 3x_3 = -26 \\ 3x_1 + 5x_2 + 2x_3 = -39 \\ 2x_1 + 4x_2 + x_3 = -27 \end{cases} \quad (7)$$

The overflow condition is caused because x_1 and x_2 are eliminated simultaneously from the third equation during the first elimination operation and the denominator of the division on line number 1030 is set to 0 during the second elimination operation. This type of error can be avoided if we exchange the second and third rows before entering data.

Since the pivot position moves diagonally as program execution proceeds, undesirable conditions will occur if a diagonal element happens to be zero. Try developing of countermeasures for this problem.

■ Find 1000 prime numbers

Introduction to methodological study of programming [2]

It is essential to always keep the objective and the procedure for accomplishing it in mind when formulating a program. Most programmers, however, are seldom conscious of this problem and create complicated, inscrutable programs in their own style. Frequently, such programs can hardly be understood even by those who wrote them, much less by others.

Such problems arise because the programmer does not clarify the relationship between the structure of the problem and the algorithm for solving it when the program is written. Consideration of this problem has led to active methodological study of programming itself. E.W. Dijkstra is one of the leaders in this field, and has written many outstanding books on this subject. In one of these books†, he introduced his own idea about programming (called structured programming), using the problem of finding prime numbers as an example. In this section, we briefly explain his concepts using the same problem.

Problem:

Print 1000 prime numbers 2, 3, 5, 7, 11, . . . in increasing order of magnitude.

■ Approach to the problem

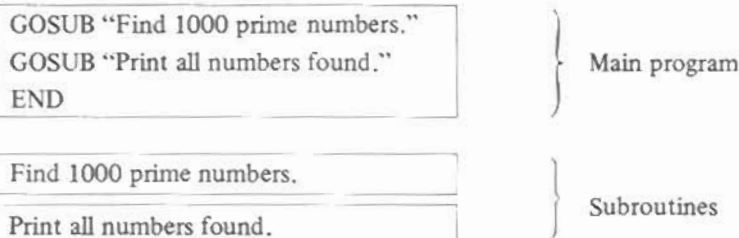
Many different programs could be used to solve this problem. The first major proposition, however, is that the program constructed must be a practical one. An extreme approach might be as follows.

"Find and print prime numbers starting at the smallest one? 2 is the smallest prime number, so, let's print 2 first. Next is 3, next is 5, next is 7. All we have to do is to print them using the PRINT statement. . . ."

This is not, however, an efficient method of using the BASIC interpreter, and cannot truly be regarded as programming.

According to Dijkstra, many decisions must be made until a program is completed. We should make such decisions only when they are actually required, rather than making them without discipline. In other words, programming should be conducted in stages.

The first decision to be made in our problem is whether the 1000 prime numbers are to be found first, then printed all at once, or whether they are to be printed as they are found. By deciding on the former method, we place the program in perspective as follows.



†) Dijkstra, Hoar, Dahr: Structured Programming, 1969 ALGOL is used as the programming language in this publication.

Notice that the problem has been divided neatly into a main program and two subroutines. This is one of the basic principles of structured programming suggested by Dijkstra.

Now, proceed to the next step. Since we are to find 1000 prime numbers before printing them, we need to store them somehow in a storage location. The next step is to determine the method of storing the prime numbers.

It would be unwise to declare a numeric array of 1000 elements simply because 1000 prime numbers must be memorized. It is possible to define a string array and place T's (true) in locations in the string designated by subscripts which happen to be prime numbers and F's (false) in other locations. Another possible method is to record the prime numbers in a data file on cassette tape. It is easy to identify prime numbers if they are stored in a string array and identified by the characters "T" and "F"; and it is possible to store prime numbers for a long time if they are recorded on cassette tape. What method should be used?

For an algorithm in which unprocessed numbers are divided by the prime numbers already found to identify prime numbers, the first method is most appropriate; that is, to prepare a numeric array of 1000 elements. Accordingly, we declare a numeric array of 1000 elements at the beginning of the main program. DIM "Numeric array of 1000 elements".

DIM "Numeric array of 1000 elements"

Next, the structure of the array must be determined. We can use PRIM as the array name (though the array name is identified only by the first two characters) and use a 2-dimensional numeric array. This is because, since the maximum subscript value for 1-dimensional arrays is 255, it is not possible to identify all array elements with a 1-dimensional array. Since all array elements must be identified within the subscript range of 255, we use an array structure such that the 1000 elements are grouped into 10 subarrays of 100 elements each. The DIM statement for the required array is as follows:

DIM PRIM (9, 99)

With this decided, we can go on to determine the format for display of the 1000 prime numbers.

There are many ways of outputting the results, such as displaying them on the CRT display unit or printing them on the printer. As for format, one prime number may be printed on one line or they may be printed in a tabular form; and so on. We will use the simplest format; that is, sequentially printing the numbers on the CRT screen without formatting them. The following subroutine ("Print all numbers found") will be adequate for this purpose:

```
FOR M=0 TO 9 : FOR N=0 TO 99
  PRINT PRIM (M, N) :
NEXT N, M
```

Now we have finished that main program and the subroutine "Print all numbers found." The remaining task is to formulate the subroutine "Find 1000 prime numbers." Since we are going to use the array described above, it is natural to find the prime numbers sequentially, starting with the smallest one, and to place them into the array in increasing order of its subscripts.

Assuming that we have a subroutine "Find the next prime number" which, given parameter I, examines I + 1, I + 2, . . . in sequence, places the first prime number found into I, and returns control to the calling program, we can form the subroutine "Find 1000 prime numbers" as follows:

```
I←1
FOR M=0 TO 9 : FOR N=0 TO 99
  GOSUB "Find the next prime number"
  PRIM (M, N)←I
NEXT N, M
RETURN
```

Now we are approaching the nucleus of the program for finding prime numbers. The subroutine "Find the next prime number" must find and assign to I the smallest prime number which is greater than I . A simple algorithm which you will think of immediately is to divide I by 2, 3, 4, 5, ..., $I-1$, and identify I as a prime number when I is indivisible by any of them. With this algorithm, you must perform 99 divisions using divisors from 2 to 100 to recognize 101 as a prime number. You will soon recognize that this algorithm wastes a great amount of time. It is apparent that numbers which are indivisible by 2 are also indivisible by multiples of 2 (e.g., 4, 6, 8, ...), numbers which are indivisible by 3 are also indivisible by multiples of 3, and so on. Since our goal is to find prime numbers sequentially starting with the smallest one, to determine whether a number is a prime or not we need only to determine whether it is divisible by any of the prime numbers which have been found so far. For example, to determine whether 101 is a prime number or not, we need only divide it by a total of 25 prime numbers, i.e., 2, 3, 5, 7, 11, 13, 17, 19, 23, 29, 31, 37, 41, 43, 47, 53, 59, 61, 67, 71, 73, 79, 83, 89, and 97.

Even this algorithm wastes a considerable amount of time. For example, we need not divide 101 by 11. When a number p can be divided by a number other than itself, it can be expressed as $p = f \cdot g$. Assuming that f is not greater than g ($f \leq g$), we find $f^2 \leq f \cdot g = p$. Accordingly, we need only to examine integers which are not greater than \sqrt{p} . For 101, we need only divide it by four prime numbers (2, 3, 5, and 7), if it is not indivisible by these numbers, we can regard 101 as a prime number. This fact affects programming efficiency greatly. In fact, to determine whether 10100 (which is 100 times greater than 101), is a prime number or not, we only need to divide it by 25 prime numbers from 2 to 97. Otherwise, as you will see later, you would have to divide it by far more than 1000 prime numbers.

Taking the above into consideration, we can formulate the algorithm for the subroutine "Find the next prime number" as follows:

```

11  I ← I + 1 : X ← 0 : Y ← 0
12  While (PRIM(X, Y))2 < I
    Divide I by PRIM(X, Y)
    If divisible GOTO 11
    Otherwise
        Determine the subscripts X and Y of the array element containing the next prime number
        GOTO 12
Return

```

The above algorithm is coded as follows:

```

1500  REM --- Find the next prime number ---
1510  I = I + 1 : X = 0 : Y = 0
1520  IF PRIM(X, Y) * PRIM(X, Y) > I THEN RETURN
1530  L = I / PRIM(X, Y)
1540  IF L - INT(L) = 0 THEN 1510
1550  Y = Y + 1
1560  IF Y < 100 THEN 1520
1570  X = X + 1 : Y = 0 : GOTO 1520

```

The statement on line 1520 determines whether the square of the prime number by which the parameter I is to be divided exceeds I .

The reason the power operator is not used in this statement is that an expression containing the power operator is inappropriate as a condition clause for the IF statement because evaluation of expressions containing the power operator are internally conducted by approximation.†

Now let's finish the program. Do not forget, however, to assign the first prime number (i.e., 2) to PRIM(0, 0). This is because the subroutine "Find the next prime number" assumes that there is a preceding prime.

†) If the power operator is to be used on line 1520, the line must be coded as follows:

```
1520 IF INT (PRIM(X, Y) ↑ 2 + 0.00000001) > I THEN RETURN
```

```
10 REM ..... 1000 prime numbers
20 DIM PRIM (9,99)
30 PRIM (0,0) = 10
40 GOSUB 1000
50 GOSUB 3000
60 END

1000 REM ..... Find 1000 prime numbers
1010 I = 1
1020 FOR M = 0 TO 9 : FOR N = 0 TO 99
1030 GOSUB 2000
1040 PRIM (M, N) = I
1050 NEXT N, M
1060 RETURN

2000 REM ..... Find the next prime number
2010 I = I + 1 : X = 0 : Y = 0
2020 IF PRIM (X, Y) * PRIM (X, Y) > I THEN RETURN
2030 L = I / PRIM (X, Y)
2040 IF L - INT (L) = 0 THEN 2000
2050 Y = Y + 1
2060 IF Y < 100 THEN 2020
2070 X = X + 1 : Y = 0 : GOTO 2020

3000 REM ..... Print 1000 prime numbers
3010 FOR M = 0 TO 9 : FOR N = 0 TO 99
3020 PRINT/P PRIM (M, N),
3030 NEXT N, M
3040 RETURN
```

Print listing of 1000 prime numbers on the line printer MZ-80P5.

2	3	5	7	11	13	17	19
23	29	31	37	41	43	47	53
59	61	67	71	73	79	83	89
97	101	103	107	109	113	127	131
137	139	149	151	157	163	167	173
179	181	191	193	197	199	211	223
227	229	233	239	241	251	257	263
269	271	277	281	283	293	307	311
313	317	331	337	347	349	353	359
367	373	379	383	389	397	401	409
419	421	431	433	439	443	449	457
461	463	467	479	487	491	499	503
509	521	523	541	547	557	563	569
571	577	587	593	599	601	607	613
617	619	631	641	643	647	653	659
661	673	677	683	691	701	709	719
727	733	739	743	751	757	761	769
773	787	797	809	811	821	823	827
829	839	853	857	859	863	877	881
883	887	907	911	919	929	937	941
947	953	967	971	977	983	991	997
1009	1013	1019	1021	1031	1033	1039	1049
1051	1061	1063	1069	1087	1091	1093	1097
1103	1109	1117	1123	1129	1151	1153	1163
1171	1181	1187	1193	1201	1213	1217	1223

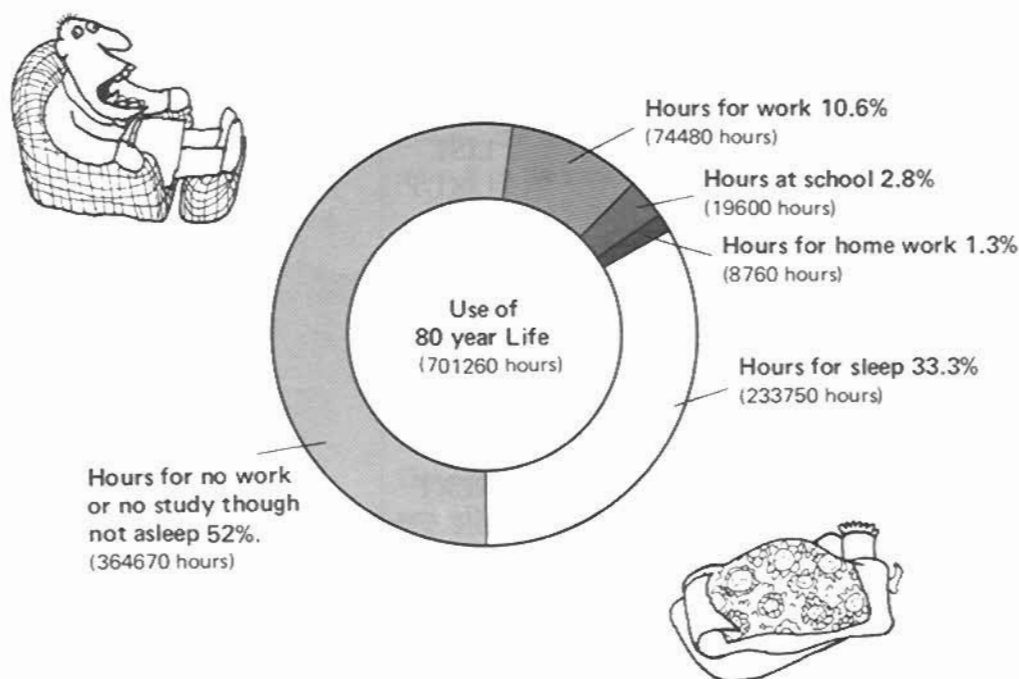
.....
omitted
.....

6143	6151	6163	6173	6197	6199	6203	6211
6217	6221	6229	6247	6257	6263	6269	6271
6277	6287	6299	6301	6311	6317	6323	6329
6337	6343	6353	6359	6361	6367	6373	6379
6389	6397	6421	6427	6449	6451	6469	6473
6481	6491	6521	6529	6547	6551	6553	6563
6569	6571	6577	6581	6599	6607	6619	6637
6653	6659	6661	6673	6679	6689	6691	6701
6703	6709	6719	6733	6737	6761	6763	6779
6781	6791	6793	6803	6823	6827	6829	6833
6841	6857	6863	6869	6871	6883	6899	6907
6911	6917	6947	6949	6959	6961	6967	6971
6977	6983	6991	6997	7001	7013	7019	7027
7039	7043	7057	7069	7079	7103	7109	7121
7127	7129	7151	7159	7177	7187	7193	7207
7211	7213	7219	7229	7237	7243	7247	7253
7283	7297	7307	7309	7321	7331	7333	7349
7351	7369	7393	7411	7417	7433	7451	7457
7459	7477	7481	7487	7489	7499	7507	7517
7523	7529	7537	7541	7547	7549	7559	7561
7573	7577	7583	7589	7591	7603	7607	7621
7639	7643	7649	7669	7673	7681	7687	7691
7699	7703	7717	7723	7727	7741	7753	7757
7759	7789	7793	7817	7823	7829	7841	7853
7867	7873	7877	7879	7883	7901	7907	7919

■ 701,260 Hours

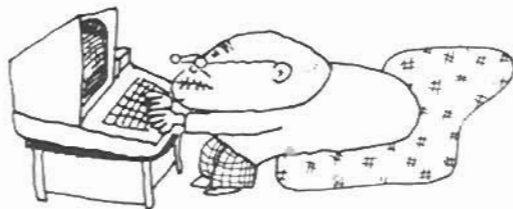
You have mastered your computer to use it as if it were part of your brains, haven't you? What did you say? You are too busy to have time for that. Indeed, we are living in this busy world, aren't we?

By the way, let's predict, using the computer, what a busy life you are leading. Calculations are made for a life of 80 years that is a little longer than the English average. For education 16 years are spent at infant school, primary school, high school and university or college. 245 days a year are for going to school and 5 hours a day for lessons. After school, 1.5 hours are for home work every day throughout the year. After graduation from university, 8 hours a day for 245 days a year are for work as a salaried man with 8 hours a day for sleep. 365.24 days a year are assumed. Based on the above, calculations are made, with results as follows:



How did you enjoy the calculations? During a life of 80 years, studying accounts for 4.1% and working 10.6%. Why not analyze and predict the use of your own time for your future reference and consideration?

Don't forget to add your time working on your computer.



1.4 Reserved word

A BASIC sentence is composed of reserved words—also called key words—which include statements, built-in functions and special signs (and also commands), and other elements, such as constants, variables, arrays and expressions. Table 1.1 shows all reserved words of the BASIC interpreter SA-5510.

A	ABS		INT		RETURN
	ASC	L	LEFTS		RIGHTS
	ATN		LEN		RND
	AUTO		LET		ROPEN/T
C	CHARACTERS		LIMIT	S	RUN
	CHRS		LIST		SAVE
	CLOSE/T		LIST/P		SET
	CLR		LN		SGN
	CONT		LOAD		SIN
	COPY/P		LOG		SIZE
	COS	M	MIDS		SPACES
	CSRH		MON		SQR
	CSRV		MUSIC		STEP
	CURSOR	N	NEW		STOP
D	DATA		NEXT		STRS
	DEF FN	O	ON		STRINGS
	DIM		OUT	T	TAB
E	END	P	PAGE/P		TAN
	EXP		PEEK		TEMPO
F	FOR		POKE		THEN
G	GET		PRINT		TIS
	GOSUB		PRINT/P		TO
	GOTO		PRINT/T	U	USR
I	IF	R	READ	V	VAL
	INP		REM		VERIFY
	INPUT		RESET	W	WOPEN/T
	INPUT/T		RESTORE		

TABLE 1.1 All reserved words of the BASIC interpreter SA-5510

1.5 List of BASIC interpreter SA-5510 commands, statements and functions

1.5.1 Commands

LOAD	LOAD "A"	Loads the BASIC text assigned the file name "A" from the cassette tape into the text area.
	LIMIT SA000: LOAD "B"	To load a machine language program file to be linked with a BASIC text, the BASIC area of memory must be partitioned from the machine language area by the LIMIT statement. Note: When a LOAD command is executed for a BASIC text file, the text area is cleared of any programs previously stored.
SAVE	SAVE "C"	Assigns the file name "C" to the BASIC text in the text area and stores it on the cassette tape. File name is valid up to 16 characters.
RUN	RUN	Executes the BASIC text in the text area from the top. Note: The RUN command clears all variables (fills them with 0 or null string) before running text.
	RUN 1000	Executes the BASIC text starting at line number 1000.
VERIFY	VERIFY "C"	This command compares the program contained in the BASIC text area with its equivalent text assigned the file name "C" in the cassette tape file.
AUTO	AUTO	Automatically generates and assigns line numbers 10, 20, 30 during creation.
	AUTO 200, 20	Automatically generates line numbers at intervals 20 starting at line 200. 200, 220, 240 An AUTO command is terminated by pressing the BREAK key.
LIST	LIST	Displays all lines of BASIC text currently contained in the text area.
	LIST -500	Displays all lines of BASIC text up through line 500.
LIST/P	LIST/P	Prints out all lines contained in the BASIC text area on the line printer.
NEW	NEW	Clears the text area and variable area. Further, disestablishes the machine language program area set by a LIMIT statement by removing the partition.

CONT	CONT	Continues program execution which was halted by a STOP statement or the BREAK key, starting at the statement following the STOP statement or the statement halted by the BREAK key.
MON	MON	Transfers system control from the BASIC interpreter to the MONITOR. (To transfer system control from the MONITOR to the BASIC interpreter, execute monitor command J.)

1.5.2 Assignment statement

LET	<LET> A = X + 3	Substitutes X + 3 into numeric variable A. LET may be omitted.
-----	-----------------	--

1.5.3 Input/output statements

PRINT	10 PRINT A	Displays the numeric value of A on the CRT screen.
	? AS	Displays the character string of variable AS on the CRT screen.
	100 PRINT A; AS, B; BS	Combinations of numeric variables and string variables can be specified in a PRINT statement. When a semicolon is used as the separator, no space is displayed between the data strings. When a colon is used, variable data to the right of the colon is displayed from the next tab set position. (A tab is set every 10 character positions.)
	110 PRINT "COST="; CS	Displays the string between double quotation marks as is, and CS.
	120 PRINT	Performs a new line operation (i.e., advances the cursor one line).
INPUT	10 INPUT A	Obtains numeric data for variable A from the keyboard.
	20 INPUT AS	Obtains string data for string variable AS from the keyboard.
	30 INPUT "VALUE?"; D	Displays "VALUE?" on the screen before obtaining data from the keyboard. A semicolon separates the string from the variable.
	40 INPUT X, XS, Y, YS	Numeric variables and string variables can be used in combination by separating them from each other with a comma. The types of data entered from the keyboard must be the same as those of the corresponding variables.
GET	10 GET N	Obtains a numeral for variable N from the keyboard. When no key is pressed, zero is substituted into N.
	20 GET KS	Obtains a character for variable KS from the keyboard. When no key is pressed, a null is substituted into KS.

<p>READ~DATA</p>	<pre>10 READ A, B, C 1010 DATA 25, -0.5, 500 10 READ HS, H, SS, S 30 DATA HEART, 3 35 DATA SPADE, 11</pre>	<p>Substitutes constants specified in the DATA statement into the corresponding variables specified in the READ statement. The corresponding constant and variable must be of the same data type.</p> <p>In READ and DATA statements at left, values of 25, -0.5 and 500 are substitutes for variables A, B and C, respectively.</p> <p>In the example at left, the first string constant of the DATA statement on line number 10 is substituted into the first variable of the READ statement; that is, "HEART" is substituted into HS. Then, numeric constant 3 is substituted into numeric variable H, and so on.</p>
<p>RESTORE</p>	<pre>10 READ A, B, C 20 RESTORE 30 READ D, E 100 DATA 3, 6, 9, 12, 15</pre>	<p>With a RESTORE statement, data in the following DATA statement which has already been read by preceding READ statements can be re-read from the beginning by the following READ statements.</p> <p>The READ statement on line number 10 substitutes 3, 6 and 9 into variables A, B and C, respectively. Because of the RESTORE statement, the READ statement on line number 30 substitutes not 12 and 15, but 3 and 6 again into D and E, respectively.</p>

1.5.4 Loop statement

<p>FOR ~ TO NEXT</p>	<pre>10 FOR A=1 TO 10 20 PRINT A 30 NEXT A</pre>	<p>The statement on line number 10 specifies that the value of variable A is varied from 1 to 10 in increments of one. The initial value of A is 1. The statement on line number 20 displays the value of A. The statement on line number 30 increments the value of A by one and returns program execution to the statement on line number 10. Thus, the loop is repeated until the value of A becomes 10. (After the specified number of loops has been completed, the value of A is 11.)</p>
	<pre>10 FOR B=2 TO 8 STEP 3 20 PRINT B ↑ 2 30 NEXT 10 FOR A=1 TO 3 20 FOR B=10 TO 30 30 PRINT A, B 40 NEXT B 50 NEXT A</pre>	<p>The statement on line number 10 specifies that the value of variable B is varied from 2 to 8 in increments of 3. The value of STEP may be made negative to decrement the value of B.</p> <p>The FOR-NEXT loop for variable A includes the FOR-NEXT loop for variable B. As is shown in this example, FOR-NEXT loops can be enclosed in other FOR-NEXT loops at different levels. Lower level loops must be completed within higher level loops. The maximum number of levels of FOR-NEXT loops is 16.</p>
	<pre>60 NEXT B, A 70 NEXT A, B</pre>	<p>In substitution for NEXT statement at line numbers 40 and 50, a statement at line number 60 shown at left can be used. However, statement at line number 70 cannot be used, causing an error to occur.</p>

1.5.5 Branch statements

GOTO	100 GOTO 200	Jumps to the statement on line number 200.
GOSUB ~ RETURN	100 GOSUB 700 800 RETURN	Calls the subroutine starting on line number 700. At the end of subroutine, program execution returns to the statement following the corresponding GOSUB statement.
IF ~ THEN	10 IF A>20 THEN 200	Jumps to the statement on line number 200 when the value of variable A is more than 20; otherwise the next line is executed.
	50 IF B<3 THEN B=B+3	Substitutes B+3 into variable B when the value of B is less than 3; otherwise the next line is executed.
IF ~ GOTO	100 IF A>=B THEN 10	Jumps to the statement on line number 10 when the value of variable A is equal to or greater than the value of B; otherwise the next line is executed.
IF ~ GOSUB	30 IF A=B*2 GOSUB 90	Jumps to the subroutine starting on line number 700 when the value of variable A is twice the value of B; otherwise the next statement is executed. (When other statements follow a conditional statement on the same line and the conditions are not satisfied, those following an ON statement are executed sequentially, but those following an IF statement are ignored and the statement on the next line is executed.)
ON ~ GOTO	50 ON A GOTO 70, 80, 90	Jumps to the statement on line number 70 when the value of variable A is 1, to the statement on line number 80 when it is 2 and to the statement on line number 90 when it is 3. When the value of A is 0 or more than 3, the next statement is executed. This statement has the same function as the INT function, so that when the value of A is 2.7, program execution jumps to the statement on line number 80.
ON ~ GOSUB	90 ON A GOSUB 700, 800	Jumps to the subroutine on line number 700 when the value of variable A is 1 and jumps to the subroutine on line number 800 when it is 2.

1.5.6 Definition statements

DIM		When an array is used, the number of array elements must be declared with a DIM statement. The number of elements ranges from 0 to 255.
	10 DIM A(20)	Declares that 21 array elements, A(0) through A(20), are used for one-dimensional numeric array A(n).

	20 DIM B(79, 79)	Declares that 6400 array elements, B(0, 0) through B(79, 79), are used for two-dimensional numeric array B(m, n).
	30 DIM C1\$(10)	Declares that 11 array elements, C1\$(0) through C1\$(10), are used for one-dimensional string array C1\$(n).
	40 DIM K\$(7, 5)	Declares that 48 array elements, K\$(0, 0) through K\$(7, 5), are used for two-dimensional string array K\$(m, n).
DEF FN	100 DEF FNA(X)=X ² -X 110 DEF FNB(X)=LOG(X) +1 120 DEF FNZ(Y)=LN(Y)	A DEF FN statement defines a function. The statement on line number 100 defines FNA(X) as $X^2 - X$. The statement on line number 110 defines FNB(X) as $\log_{10} X + 1$ and the statement on line number 120 defines FNZ(Y) as $\log_e Y$. The number of variables included in the function must be 1.

1.5.7 Comment and control statements

REM	200 REM JOB-1	Comment statement (not executed).
STOP	850 STOP	Stops program execution and awaits a command entry. When a CONT command is entered, program execution is continued.
END	1999 END	Declares the end of a program. Although the program is stopped, the following program is executed if a CONT command is entered.
CLR	300 CLR	Clears all variables and arrays, that is, fills all numeric variables and arrays with zeros and all string variables and arrays with nulls.
CURSOR	50 CURSOR 25, 15 60 PRINT "ABC"	The CURSOR command moves the cursor to any position on the screen. The first operand represents the horizontal location of the destination, and must be between 0 and 39. The second operand represents the vertical location of the destination and must be between 0 and 24. The left example displays "ABC" starting at location (25, 15) (the 26th position from the left side and the 16th position from the top).
CSRH		System variable indicating the X-coordinate (horizontal location) of the cursor.
CSRV		System variable indicating the Y-coordinate (vertical location) of the cursor.
SIZE	? SIZE	Displays the amount of unused memory area in bytes.
TIS	100 TIS = "102030"	Sets the built-in clock to 10:20:30 AM. Data between the double quotation marks must be numerals.

1.5.8 Music control statements

MUSIC		The MUSIC statement generates a melody from the speaker according to the melody string data enclosed in quotation marks or string variables at the tempo specified by the TEMPO statement.
TEMPO		The TEMPO statement on line number 300 specifies tempo 7. The MUSIC statement on line number 310 generates a melody consisting of D, E, F sharp, G and A. Each note is a quarter note. When the TEMPO statement is omitted, default tempo is set.
	300 TEMPO 7	
	310 MUSIC "DE#FGA"	
	300 M1\$ = "C3EG + C"	In this example, the melody is divided into 3 parts and substituted in 3 string variables. The following melody is generated from the speaker at tempo 4.
	310 M2\$ = "+E+C+E+G"	
	320 M3\$ = "+#B8R5"	
	330 MUSIC M1\$,M2\$,M3\$	



1.5.9 Graphic control statements

SET		Sets a dot in the specified position on the CRT screen. The first operand specifies the X-coordinates (0-79) and the second operand specifies the Y-coordinates (0-49).
	300 SET 40, 25	Displays a dot in the center of the screen.
RESET		Resets a dot in the specified position on the CRT screen.
	310 RESET 40, 25	Resets a dot from the center of the screen.

1.5.10 Cassette data file input/output statements

WOPEN/T	10 WOPEN/T "DATA-1"	Defines the file name of a cassette data file to be created as "DATA-1" and opens.
PRINT/T	20 PRINT/T A, A\$	Writes the contents of variable A and string variable A\$ in order in the cassette data file which was opened by a WOPEN/T statement.
CLOSE/T	30 CLOSE/T	Closes the cassette data file which was opened by a WOPEN/T statement.
ROPEN/T	110 ROPEN/T "DATA-2"	Opens the cassette data file specified with file name "DATA-2".
INPUT/T	120 INPUT/T B, B\$	Reads data sequentially from the beginning of the cassette data file which was opened by the ROPEN/T statement and substitutes numerical data into variable B and string data into string variable B\$ respectively.
CLOSE/T	130 CLOSE/T	Closes the cassette data file which was opened by a ROPEN/T statement.

1.5.11 Machine language control statements

LIMIT	100 LIMIT 49151	Limits the area in which BASIC programs can be loaded to the area up to address 49151 (\$BFFF in hexadecimal).
	100 LIMIT A	Limits the area in which BASIC programs can be loaded to the area up to the address indicated by variable A.
	100 LIMIT \$BFFF	Limits the area in which BASIC programs can be loaded to the area up to \$BFFF (hexadecimal). Hexadecimal numbers are indicated by a dollar sign as shown at left.
	300 LIMIT MAX	Set the maximum address of the area in which BASIC programs can be loaded to the maximum address of the memory installed.
	200 LIMIT \$BFFF 210 LOAD "S-R1"	Loads machine language program (object program) "S-R1" in the machine language link area from the cassette tape when the loading address of the program is \$C000 or higher.
POKE	120 POKE 49450, 175	Stores 175 (\$AF in hexadecimal) in address 49450.
	130 POKE AD, DA	Stores data (between 0 and 255) specified by variable DA into the address indicated by variable AD.
PEEK	150 A=PEEK (49450)	Substitutes data stored in address 49450 into variable A.
	160 B=PEEK (C)	Substitutes the contents of the address indicated by variable C into variable B.
USR	500 USR (49152)	Transfers program control to address 49152. This function is the same as that performed by the CALL instruction, which calls a machine language program. When a RET command is encountered in the machine language program, program control is returned to the BASIC program.
	550 USR (AD)	Calls the program starting at the address specified by variable AD.
	570 USR (\$C000)	Calls the program starting at address \$C000.
	770 USR (AD, DAS)	When string data is given together with address data, this USR function places the first address of the memory area containing string data DAS in the CPU's DE register and the length of DAS in the BC register prior to execution of a CALL instruction.

1.5.12 Printer control statements

PRINT/P	10 PRINT/P A, A\$	Performs the nearly same operation as the PRINT statement on the optional printer (MZ-80P4, P5 or P6). Prints the numeric value of A and the character string of variable A\$ on the line printer.
	20 PRINT/P CHR\$(5)	Executes paper home feed. (CHR\$(5) is a control code.)
	30 PRINT/P CHR\$(18)	Sets the enlarged character print mode. (CHR\$(18) is also a control code.)
COPY/P	10 COPY/P 1	Causes the printer to copy the character display.
PAGE/P	100 PAGE/P 20	Specifies 20 lines to be contained in one page of the line printer.

1.5.13 I/O input/output statements

INP	10 INP @12, A	Reads data on the specified I/O port. The statement on line number 10 reads data on I/O port 12.
	20 PRINT A	
OUT	30 B = ASC ("A")	Outputs data to the specified I/O port. The statement on line 40 outputs the ASCII code of the character "A" to I/O port 13.
	40 OUT @13, B	

1.5.14 Arithmetic functions

ABS	100 A = ABS (X)	Substitutes the absolute value of variable X into variable A. X may be either a constant or an expression. Ex) ABS (-3) = 3 ABS (12) = 12
INT	100 A = INT (X)	Substitutes the greatest integer which is less than X into variable A. X may be either a numeric constant or an expression. Ex) INT (3.87) = 3 INT (0.6) = 0 INT (-3.87) = -4
SGN	100 A = SGN (X)	Substitutes one of the following values into variable A: -1 when X<0, 0 when X=0 and 1 when X>0. X may be either a constant or an expression. Ex) SGN (0.4) = 1 SGN (0) = 0 SGN (-400) = -1

SQR	100 A = SQR (X)	Substitutes the square root of variable X into variable A. X may be either a numeric constant or an expression; however, it must be greater than or equal to 0.
SIN	100 A = SIN (X)	Substitutes the sine of variable X in radians into variable A. X may be either a numeric constant or an expression. The relationship between degrees and radians is as follows. $1 \text{ degree} = \frac{\pi}{180} \text{ radians}$
	110 A = SIN (30* π /180)	Therefore, when substituting the sine of 30° into A, the statement is written as shown on line number 110 at left.
COS	200 A = COS (X)	Substitutes the cosine of variable X in radians into variable A. X may be either a numeric constant or an expression. The same relationship as shown in the explanation of the SIN function is used to convert degrees into radians. The statement shown on line number 210 substitutes the cosine of 200° into variable A.
	210 A = COS (200* π /180)	
TAN	300 A = TAN (X)	Substitutes the tangent of variable X in radians into variable A. X may be either a numeric constant or an expression. The statement on line number 310 is used to substitute the tangent of numeric variable Y in degrees into variable A.
	310 A = TAN (Y* π /180)	
ATN	400 X = ATN (A)	Substitutes the arctangent of variable A into variable X in radians. A may be either a numeric constant or an expression. Only the result between $-\pi/2$ and $\pi/2$ is obtained. The statement on line number 410 is used to substitute the arctangent in degrees.
	410 Y = 180/ π *ATN (A)	
EXP	100 A = EXP (X)	Substitutes the value of exponential function e^x into variable A. X may be either a numeric constant or an expression.
LOG	100 A = LOG (X)	Substitutes the value of the common logarithm of variable X into variable A. X may be either a numeric constant or an expression; however, it must be positive.
LN	100 A = LN (X)	Substitutes the natural logarithm of variable X into variable A. X may be either a numeric constant or an expression; however, it must be positive.
	110 A = LOG (X)/LOG (Y)	To obtain the logarithm of X with the base Y, the statement on line number 110 or line number 120 is used.
	120 A = LN (X)/LN (Y)	
RND		This function generates random numbers which take any value between 0.00000001 and 0.99999999, and works in two manners depending upon the value in parentheses.

	100 A = RND (1) 110 B = RND (10)	When the value in parentheses is positive, the function gives the random number following the one previously given in the random number group generated. The value obtained is independent of the value in parentheses.
	100 A = RND (0) 110 B = RND (-3)	When the value in parentheses is less than or equal to 0, the function gives the initial value of the random number group generated. Therefore, statements on line numbers 100 and 110 both give the same value to variables A and B.

1.5.15 String control functions

LEFT \$	10 A\$ = LEFT\$ (X\$, N)	Substitutes the first N characters of string variable X\$ into string variable A\$. N may be either a constant, a variable or an expression.
MID \$	20 B\$ = MID\$ (X\$, M, N)	Substitutes the N characters following the Mth character from the beginning of string variable X\$ into string variable B\$.
RIGHT \$	30 C\$ = RIGHT \$ (X\$, N)	Substitutes the last N characters of string variable X\$ into string variable C\$.
SPACE \$	40 D\$ = SPACE \$ (N)	Substitutes the N spaces into string variable D\$.
STRING \$	50 E\$ = STRING \$ ("*", 10)	Substitutes the ten repetitions of "*" into string variable E\$.
CHR \$	60 F\$ = CHR \$ (A)	Substitutes the character corresponding to the ASCII code in numeric variable A into string variable F\$. A may be either a constant, a variable or an expression.
ASC	70 A = ASC (X\$)	Substitutes the ASCII code (in decimal) corresponding to the first character of string variable X\$ into numeric variable A.
STR\$	80 N\$ = STR\$ (I)	Converts the numeric value of numeric variable I into string of numerals and substitutes it into string variable N\$.
VAL	90 I = VAL (N\$)	Converts string of numerals contained in string variable N\$ into the numeric data as is and substitutes it into numeric variable I.
LEN	100 LX = LEN (X\$)	Substitutes the length (number of bytes) of string variable X\$ into numeric variable LX.
	110 LS = LEN (X\$ + Y\$)	Substitutes the length (number of bytes) of string variable X\$ and Y\$ into numeric variable LX.

1.5.16 Tabulation function

TAB	10 PRINT TAB (X); A	Displays the value of variable A at the Xth position from the left side.
-----	---------------------	--

1.5.17 Arithmetic operators

The number to the left of each operator indicates its operational priority. Any group of operations enclosed in parentheses has first priority.

① ↑	10 A = X ↑ Y (power)	Substitutes X^Y into variable A. (If X is negative and Y is not an integer, an error results.)
② -	10 A = -B (negative sign)	Note that "-" in -B is the negative sign and "-" in O-B represents subtraction.
③ *	10 A = X * Y (multiplication)	Multiplies X by Y and substitutes the result into variable A.
③ /	10 A = X/Y (division)	Divides X by Y and substitutes the result into variable A.
④ +	10 A = X + Y (addition)	Adds X and Y and substitutes the result into variable A.
④ -	10 A = X - Y (subtraction)	Subtracts X from Y and substitutes the result into variable A.

1.5.18 Logical operators

=	10 IF A=X THEN ... 20 IF A\$="XYZ" THEN ...	If the value of variable A is equal to X, the statement following THEN is executed. If the content of variable A\$ is "XYZ", the statement following THEN is executed.
>	10 IF A > X THEN ...	If the value of variable A is greater than X, the statement following THEN is executed.
<	10 IF A < X THEN ...	If the value of variable A is less than X, the statement following THEN is executed.
<> or <>	10 IF A <> X THEN ...	If the value of variable A is not equal to X, the statement following THEN is executed.
>= or =>	10 IF A >= X THEN ...	If the value of variable A is greater than or equal to X, the statement following THEN is executed.
<= or =<	10 IF A <= X THEN ...	If the value of variable A is less than or equal to X, the statement following THEN is executed.
*	40 IF (A > X)*(B > Y) THEN ...	If the value of variable A is greater than X and the value of variable B is greater than Y, the statement following THEN is executed.
+	50 IF (A > X)+(B > Y) THEN ...	If the value of variable A is greater than X or the value of variable B is greater than the value of Y, the statement following to THEN is executed.

1.5.19 Other symbols

?	200 ? "A + B ="; A + B 210 PRINT "A + B ="; A + B	Can be used instead of PRINT. Therefore, the statement on line number 200 is identical in function to that on line number 210.
:	220 A=X : B=X↑2 : ?A, B	Separates two statements from each other. This separator is used when multiple statements are written on the same line. Three statements are written on line number 220.
;	230 PRINT "AB"; "CD"; "EF"	Displays characters to the right of separators following characters on the left. The statement on line 230 displays "ABCDEF" on the screen with no spaces between characters.
	240 INPUT "X = " ; XS	Displays "X =" on the screen and awaits entry of data for XS from the keyboard.
,	250 PRINT "AB", "CD", "E"	Displays character strings in a tabulated format; i.e. AB first appears, then CD appears in the position corresponding to the starting position of A plus 10 spaces and E appears in the position corresponding to the starting position of C plus 10 spaces.
	300 DIM A(20), B\$(3, 6)	A comma is used to separate two variables.
" "	320 A\$ = "SHARP BASIC" 330 B\$ = "MZ-80A"	Indicates that characters between double quotation marks form a string constant.
\$	340 C\$ = "ABC" + CHR\$(3)	Indicates that the variable followed by a dollar sign is a string variable.
	500 LIMIT \$BFFF	Indicates that numeric data following a dollar sign is represented in hexadecimal notation.
π	550 S = SIN (X * π / 180)	π represents 3.1415927 (ratio of the circumference of a circle to its diameter).

1.5.20 Error Message Table

Error No.	Meaning
1	Syntax error
2	Operation result overflow
3	Illegal data
4	Data type mismatch
5	String length exceeded 255 characters
6	Insufficient memory capacity
7	The size of an array defined was larger than that defined previously.
8	The length of a BASIC text line was too long.
9	
10	The number of levels of GOSUB nests exceeded 16.
11	The number of levels of FOR-NEXT nests exceeded 16.
12	The number of levels of functions exceeded 6.
13	NEXT was used without a corresponding FOR.
14	RETURN was used without a corresponding GOSUB.
15	Undefined function was used.
16	Unused reference line number was specified in a statement.
17	CONT command cannot be executed.
18	A writing statement was issued to the BASIC control area.
19	Direct mode commands and statements are mixed together.
20	
21	
22	
23	
24	A READ statement was used without a corresponding DATA statement.
25	
26	
27	
28	
29	
30	
31	
32	
33	
34	
35	

Error No.	Meaning
36	
37	
38	
39	
40	
41	
42	
43	OPEN statement (ROPEN or WOPEN) was issued to a file which is already open.
44	
45	
46	
47	
48	
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50	
51	
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53	
54	
55	
56	
57	
58	
59	
60	
61	
62	
63	Out of file
64	
65	The printer is not ready.
66	Printer hardware error
67	Out of paper
68	
69	
70	Check sum error

1.6 How to obtain copied BASIC tapes

The BASIC tape will not be exchanged for new ones after purchase. It is recommended that the original BASIC tape be copied using the following procedure to generate a copied BASIC tape, and that the copied BASIC tape be used ordinarily. Be sure to keep the original BASIC tape in a safe place.

Activate BASIC interpreter by the original BASIC tape. Execute the following command.

USR (\$I1FD)

the message “↓RECORD. PLAY” is displayed. Set the new cassette tape into the deck and press the **RECORD** and **PLAY** buttons.

Upon completion of writing, a copied BASIC tape will be obtained. Any number of copied BASIC tapes can be made using this procedure. However, copied BASIC tape cannot be made by copying another copied BASIC tape.

Monitor Program of the MZ-80A

Chapter

2

2.1 Monitor SA-1510 Commands and subroutines

A monitor program generally monitors system programs such as the BASIC interpreter. The MZ-80A uses a Monitor program called MONITOR SA-1510 in 4K bytes ROM. It includes various functional subroutines which control the keyboard, display, sound circuit, cassette tape deck, etc. These subroutines are called by the BASIC interpreter when it executes INPUT statement, SAVE command, MUSIC statement or other commands or statements. Monitor subroutines may also be called by the user at will.

MONITOR SA-1510 occupies 4K bytes of memory and is stored in Monitor ROM addresses \$0000 through \$0FFF. Its required work area is included within memory addresses \$1000 through \$11FF.

2.1.1 Using monitor commands

Following shows the message when the power switch of the MZ-80A is turned on.

```
* * MONITOR SA-1510 * *
*  
```

The cursor flickers to inform the operator that system control is in Monitor command level. Monitor commands are as follows:

L CR Loads the cassette tape file into memory.

J xxxx CR Transfers system control to the specified address, that is, loads the specified address in the program counter of the CPU.

xxxx : 4-digit hexadecimal number

The start addresses of the BASIC SA-5510 are as follows:

Warm start address = \$1250

Cold start address = \$1200

F CR Transfers system control to the floppy disk drive control routine which is stored on floppy disk drive interface card.

B CR Sets or resets the key-entry-bell alternately.

2.1.2 Monitor subroutines

MONITOR SA-1510 subroutines are listed in Table 2.1. The subroutine names indicated are the same as the labels shown in the monitor program assembly listing in 2.2. Each name is a mnemonic representing the subroutine's function.

Table 2.1 Monitor Subroutine List

Subroutine name (hexadecimal address)	Function	Registers preserved
CALL LETNL (S0006)	To change the line and set the cursor to the beginning of the next line.	All registers except AF
CALL NL (S0009)	Changes the line and sets cursor to its beginning if the cursor is not already located at the beginning of a line.	All registers except AF
CALL PRNTS (S000C)	Displays one space only at the cursor position on the display screen.	All registers except AF
CALL PRNT (S0012)	Handles data in A register as ASCII code and displays it on the screen, starting at the cursor position. However, a carriage return is performed for 0DH and the various cursor control operations are performed for 11H-16H when these are included.	All registers except AF
CALL MSG (S0015)	Displays a message, starting at the cursor position on the screen. The starting address of the message must be specified in the DE register in advance. The message is written in ASCII code and must end in 0DH. A carriage return is not executed, however, cursor control codes (11H-16H) are.	All registers
CALL MSGX (S0018)	Almost the same as MSG, except that cursor control codes are for reverse character display.	All registers
CALL BELL (S003E)	Sounds a tone (approximately 880 Hz) momentarily.	All registers except AF
CALL MELDY (S0030)	Plays music according to music data. The starting address of the music data must be specified in advance in the DE register. As with BASIC, the musical interval and the duration of notes of the musical data are expressed in that order in ASCII code. The end mark must be either 0DH or C8H "■". The melody is over if C flag is 0 when a return is made; if C flag is 1 it indicates that SHIFT + BREAK were pressed.	All registers except AF
CALL XTEMP (S0041)	Sets the musical tempo. The tempo data (01 to 07) is set in and called from A register. 01 : Slowest 04 : Medium speed 07 : Fastest Care must be taken here to ensure that the tempo data is entered in A register in binary code, and not in the ASCII code corresponding to the numbers 1 to 7 (31H to 37H).	All registers
CALL MSTA (S0044)	Continuously sounds a note according to a specified division factor. The division factor nn' consists of two bytes of data; n' is stored at address 11A1H and n is stored at address 11A2H. The relationship between the division factor and the frequency produced is $2 \text{ MHz}/nn'$.	BC and DE only

Subroutine name (hexadecimal address)	Function	Registers preserved																										
CALL MSTP (S0047)	Discontinues a tone being sounded.	All registers except AF																										
CALL TIMST (S0033)	Sets the built-in clock. (The clock is activated by this call.) The call conditions are: A register ← 0 (AM), A register ← 1 (PM) DE register ← the time in seconds (2 bytes)	All registers except AF																										
CALL TIMRD (S003B)	Reads the value of the built-in clock. The conditions upon return are: A register ← 0 (AM), A register ← 1 (PM) DE register ← the time in seconds (2 bytes)	All registers except AF and DE																										
CALL BRKEY (S001E)	Checks whether SHIFT + BREAK were pressed. Z flag is set if they were pressed, and Z flag is reset if they were not.	All registers except AF																										
CALL GETL (S0003)	Inputs one line entered from the keyboard. The starting address where the data input is to be stored must be specified in advance in the DE register. CR functions as the end mark. 80 is the maximum number of characters which can be input (including the end mark 0DH). Key input is displayed on the screen and cursor control is also accepted. The BREAK code (1BH) followed by a carriage return code (0DH) is set at the beginning of the address specified in the DE register when SHIFT + BREAK are pressed.	All registers																										
CALL GETKY (S001B)	Takes one character only into A register from the keyboard in ASCII code. A return is made after 00 is set in A register if no key is pressed when the subroutine is executed. However, key input is not displayed on the screen. Codes which are taken into A register when these special keys are pressed are shown below.	All registers except AF																										
	<table><tr><th>Special key</th><th>Code taken into A register</th></tr><tr><td>DEL</td><td>60 H</td></tr><tr><td>INST</td><td>61 H</td></tr><tr><td>GRPH { graphic mode</td><td>62 H</td></tr><tr><td>normal mode</td><td>63 H</td></tr><tr><td>BREAK</td><td>64 H</td></tr><tr><td>CR or ENT</td><td>66 H</td></tr><tr><td>CTRL + A ~ Z</td><td>01 H ~ 1 AH</td></tr><tr><td>CTRL + [</td><td>1 BH</td></tr><tr><td>CTRL + \</td><td>1 CH</td></tr><tr><td>CTRL +]</td><td>1 DH</td></tr><tr><td>CTRL + ^</td><td>1 EH</td></tr><tr><td>CTRL + _</td><td>1 FH</td></tr></table>	Special key	Code taken into A register	DEL	60 H	INST	61 H	GRPH { graphic mode	62 H	normal mode	63 H	BREAK	64 H	CR or ENT	66 H	CTRL + A ~ Z	01 H ~ 1 AH	CTRL + [1 BH	CTRL + \	1 CH	CTRL +]	1 DH	CTRL + ^	1 EH	CTRL + _	1 FH	
Special key	Code taken into A register																											
DEL	60 H																											
INST	61 H																											
GRPH { graphic mode	62 H																											
normal mode	63 H																											
BREAK	64 H																											
CR or ENT	66 H																											
CTRL + A ~ Z	01 H ~ 1 AH																											
CTRL + [1 BH																											
CTRL + \	1 CH																											
CTRL +]	1 DH																											
CTRL + ^	1 EH																											
CTRL + _	1 FH																											

Subroutine name (hexadecimal address)	Function	Registers preserved																																			
CALL PRTHL (S03BA)	Displays the contents of the HL register on the display screen as a 4-digit hexadecimal number.	All registers except AF																																			
CALL PRTHX (S03C3)	Displays the contents of the A register on the display screen as a 2-digit hexadecimal number.	All registers except AF																																			
CALL ASC (S03DA)	Converts the contents of the lower 4 bits of A register from hexadecimal to ASCII code and returns after setting the converted data in A register.	All registers except AF																																			
CALL HEX (S03F9)	Converts the 8 bits of A register from ASCII code to hexadecimal and returns after setting the converted data in the lower 4 bits of A register. When C flag = 0 upon return A register ← hexadecimal When C flag = 1 upon return A register is not assured	All registers except AF																																			
CALL HLHEX (S0410)	Handles a consecutive string of 4 characters in ASCII code as hexadecimal string data and returns after setting the data in the HL register. The call and return conditions are as follows. DE ← starting address of the ASCII string (string "3" "1" "A" "5") CALL HLHEX C flag = 0 HL ← hexadecimal number (e.g., HL = 31A5H) C flag = 1 HL is not assured.	All registers except AF and HL																																			
CALL 2HEX (S041F)	Handles 2 consecutive ASCII strings as hexadecimal strings and returns after setting the data in A register. The call and return conditions are as follows. DE ← starting address of the ASCII string (e.g., "3" "A") CALL 2HEX C flag = 0 A register ← hexadecimal number (e.g., A register = 3AH) C flag = 1 A register is not assured.	All registers except AF and DE																																			
CALL ??KEY (S09B3)	Awaits key input while causing the cursor to flash. When a key entry is made it is converted to display code and set in A register, then a return is made.	All registers except AF																																			
CALL ?ADCN (S0BB9)	Converts an ASCII value to display code. Call and return conditions are as follows. A register ← ASCII value CALL ?ADCN A register ← display code	All registers except AF																																			
CALL ?DACN (S0BCE)	Converts a display code to an ASCII value. Call and return conditions are as follows. A register ← display code CALL ?DACN A register ← ASCII value	All registers except AF																																			
CALL ?DPCT (S0DDC)	Controls the display on the display screen. The relationship between A register at the time of the call and control is as follows.																																				
	<table><tr><th>A reg.</th><th>Same function</th><th>A reg.</th><th>Same function</th></tr><tr><td>C0H</td><td>Scrolling</td><td>C8H</td><td>INST</td></tr><tr><td>C1H</td><td>↓</td><td>C9H</td><td>GRPH (graphic → normal)</td></tr><tr><td>C2H</td><td>↑</td><td>CAH</td><td>GRPH (normal → graphic)</td></tr><tr><td>C3H</td><td>→</td><td>CCH</td><td>CTRL + @ (rev. ↔ norm.)</td></tr><tr><td>C4H</td><td>←</td><td>CDH</td><td>CR or ENT</td></tr><tr><td>C5H</td><td>HOME</td><td>CEH</td><td>CTRL + D (roll up)</td></tr><tr><td>C6H</td><td>CLR</td><td>CFH</td><td>CTRL + E (roll down)</td></tr><tr><td>C7H</td><td>DEL</td><td></td><td></td></tr></table>	A reg.	Same function	A reg.	Same function	C0H	Scrolling	C8H	INST	C1H	↓	C9H	GRPH (graphic → normal)	C2H	↑	CAH	GRPH (normal → graphic)	C3H	→	CCH	CTRL + @ (rev. ↔ norm.)	C4H	←	CDH	CR or ENT	C5H	HOME	CEH	CTRL + D (roll up)	C6H	CLR	CFH	CTRL + E (roll down)	C7H	DEL		
A reg.	Same function	A reg.	Same function																																		
C0H	Scrolling	C8H	INST																																		
C1H	↓	C9H	GRPH (graphic → normal)																																		
C2H	↑	CAH	GRPH (normal → graphic)																																		
C3H	→	CCH	CTRL + @ (rev. ↔ norm.)																																		
C4H	←	CDH	CR or ENT																																		
C5H	HOME	CEH	CTRL + D (roll up)																																		
C6H	CLR	CFH	CTRL + E (roll down)																																		
C7H	DEL																																				

Subroutine name (hexadecimal address)	Function	Registers preserved
CALL ?BLNK (S0DA6)	Checks vertical blanking of the display screen. Waits until the vertical blanking interval starts and then returns when blanking takes place.	All registers
CALL ?PONT (S0FB1)	Sets the current position of the cursor on the display screen in register HL. The return conditions are as follows. CALL ?PONT HL ← cursor position on the display screen (V-RAM address) (Note) The X-Y coordinates of the cursor are contained in DSPXY (1171 H). The current position of the cursor is loaded as follows. LD HL, (DSPXY) ; H ← Y coordinate on the screen L ← X coordinate on the screen The cursor position is set as follows. LD (DSPXY), HL	All registers except AF and HL
CALL WRINF (S0021)	Writes the current contents of a certain part of the header buffer (described later) onto the tape, starting at the current tape position. Return conditions C flag = 0 No error occurred. C flag = 1 The BREAK key was pressed.	All registers except AF
CALL WRDAT (S0024)	Writes the contents of the specified memory area onto the tape as a CMT data block in accordance with the contents of a certain part of the header buffer. Return conditions C flag = 0 No error occurred. C flag = 1 The BREAK key was pressed.	All registers except AF
CALL RDINF (S0027)	Reads the first CMT header found starting at the current tape position into a certain part of the header buffer. Return conditions C flag = 0 No error occurred. C flag = 1, A register = 1 A check sum error occurred. C flag = 1, A register = 2 The BREAK key was pressed.	All registers except AF
CALL RDDAT (S002A)	Reads in the CMT data block according to the current contents of a certain part of the header buffer. Return conditions C flag = 0 No error occurred. C flag = 1, A register = 1 A check sum error occurred. C flag = 1, A register = 2 The BREAK key was pressed.	All registers except AF
CALL VERIFY (S002D)	Compares the first CMT file found following the current tape position with the contents of the memory area indicated by its header. Return conditions C flag = 0 No error occurred. C flag = 1, A register = 1 A match was not obtained. C flag = 1, A register = 2 The BREAK key was pressed.	All registers except AF

(Note) The contents of the header buffer at the specific addresses are as follows. The buffer starts at address \$10F0 and consists of 116 bytes.

Address	Contents							
IBUFE (\$10F0)	This byte indicates one of the following file modes. 01. Object file (machine language program) 02. BASIC text file 03. BASIC data file 04. Source file (ASCII file) 05. Relocatable file (relocatable binary file) A0. PASCAL interpreter text file A1. PASCAL interpreter data file							
IBU1 (\$10F1~\$1101)	These 17 bytes indicate the file name. However, since 0DH is used as the end mark, in actuality the file name is limited to 16 bytes. Example: <table><tr><td>S</td><td>A</td><td>M</td><td>P</td><td>L</td><td>E</td><td>0D</td></tr></table>	S	A	M	P	L	E	0D
S	A	M	P	L	E	0D		
IBU18 (\$1102~\$1103)	These two bytes indicate the byte size of the data block which is to follow.							
IBU20 (\$1104~\$1105)	These two bytes indicate the data address of the data block which is to follow. The loading address of the data block which is to follow is indicated by "CALL RDDAT". The starting address of the memory area which is to be output as the data block is indicated by "CALL WRDAT".							
IBU22 (\$1106~\$1107)	These two bytes indicate the execution address of the data block which is to follow.							
IBU24 (\$1108~\$1163)	These bytes are used for supplemental information, such as comments.							

Example

Address	Content	
10F0	01	; indicates an object file (machine language program).
10F1	'S'	; the file name is "SAMPLE".
10F2	'A'	
10F3	'M'	
10F4	'P'	
10F5	'L'	
10F6	'E'	
10F7	0D	
10F8	} Variable	
1101		
1102	00	; the size of the file is 2000H bytes.
1103	20	
1104	00	; the data address of the file is 1200H.
1105	12	
1106	50	; the execution address of the file is 1250H.
1107	12	

2.2 MONITOR SA-1510 Assembly Listing

The MONITOR SA-1510 assembly listing is shown in following pages.

This assembly listing was obtained with the Z80-Assembler of the MZ-80A Floppy Disk Operating System. The meaning of each column is as follows.

Relative address		Mnemonic (Op Code)		Comment
	Relocatable OBJ code	Label	Operand	
08 0000		MONIT:	ENT	
09 0000	C34A00		JP	START
10 0003		GETL:	ENT	
11 0003	C3A807		JP	?GETL
12 0006		LETNL:	ENT	
13 0006	C38009		JP	?LTNL
14 0009		NL:	ENT	
15 0009	C37B09		JP	?NL
16 000C		PRNTS:	ENT	
17 000C	C39309		JP	?PRTS
18 000F		PRNTT:	ENT	
19 000F	C38409		JP	?PRTT
20 0012		PRNT:	ENT	
21 0012	C39509		JP	?PRNT
22 0015		MSG:	ENT	
23 0015	C39308		JP	?MSG
24 0018		MSGX:	ENT	
25 0018	C3A108		JP	?MSGX ; RST 3
26 001B		GETKY:	ENT	
27 001B	C3B308		JP	?GET
28 001E		BRKEY:	ENT	
29 001E	C3110D		JP	?BRK

Figure 2.1

Since the first address of MONITOR SA-1510 is \$0000, relative addresses and relocatable OBJ codes may be regarded as absolute addresses and OBJ codes without interpretation.

This assembly listing is for reference only. The Sharp corporation is not obliged to answer any questions about the contents of this program.


```

** 180 ASSEMBLER SB-7201 <M1-80A.MONITOR> PAGE 01 09/04/81
01 0000 1
02 0000 1
03 0000 1
04 0000 1
05 0000 1
06 0000 1
07 0000 1
08 0000 1
09 0000 C34A00
10 0003 GETL: ENT
11 0003 C3A807 JP
12 0006 LETNL: ENT
13 0006 C3B009 JP
14 0009 NL: ENT
15 0009 C37809 JP
16 000C PRNTS: ENT
17 000C C39309 JP
18 000F PRNTT: ENT
19 000F C38409 JP
20 0012 PRNT: ENT
21 0012 C39509 JP
22 0015 MSG: ENT
23 0015 C39308 JP
24 0018 MSGX: ENT
25 0018 C3A108 JP
26 001B GETKY: ENT
27 001B C3B308 JP
28 001E BRKEY: ENT
29 001E C3110D JP
30 0021 WRINF: ENT
31 0021 C33604 JP
32 0024 WRDAT: ENT
33 0024 C37004 JP
34 0027 RDINF: ENT
35 0027 C3CF04 JP
36 002A RDDAT: ENT
37 002A C3EF04 JP
38 002D VERFY: ENT
39 002D C37505 JP
40 0030 MELDY: ENT
41 0030 C38801 JP
42 0033 TIMST: ENT
43 0033 C3FA02 JP
44 0036 NOP
45 0037 00
46 0038 C3B810 JP
47 003B TIMRD: ENT
48 003B C34403 JP
49 003E BELL: ENT
50 003E C3E302 JP
51 0041 XTEMP: ENT
52 0041 C3EC02 JP
53 0044 MSTAI: ENT
54 0044 C3A802 JP
55 0047 MSTPI: ENT
56 0047 C3BE02 JP
57 004A 11111
58 004A 1
59 004A 1
60 004A 31F010

MONITOR PROGRAM
(M1-80A)
SA-1510
REV. 81.8.26
START
JP
?OETL
?LTNL
?NL
?PRTS
?PRTT
?PRNT
?MSG
?MSGX
?GETY
?BRKEY
?WRINF
?WRDAT
?RDINF
?RDDAT
?VERFY
?MELDY
?TIMST
?TIMRD
?BELL
?XTEMP
?MSTAI
?MSTPI
?11111
?START: LD SP,SP

** 180 ASSEMBLER SB-7201 <M1-80A.MONITOR> PAGE 02 09/04/81
01 004D ED56
02 004F C4D007 CALL ?MODE
03 0052 06FF LD B,FFH
04 0054 21F110 LD HL,NAME
05 0057 C0D80F CALL ?CLER
06 005A 3E16 LD A,16H
07 005C C01200 CALL PRNT
08 005F 3ECF LD A,CFH
09 0061 210008 LD HL,DB00H
10 0064 1803 JR +5
11 0066 C33510 JP 1035H
12 0069 CDE309 CALL ?CLR8
13 006C 217903 LD HL,TIMIN
14 006F 3EC3 LD A,C3H
15 0071 323810 LD (1038H),A
16 0074 223910 LD (1039H),HL
17 0077 3E04 LD A,04
18 0079 329E11 LD (TEMPW),A
19 007C C0BE02 CALL MLDSP
20 007F C00900 CALL NL
21 0082 110001 LD DE,MSG?3
22 0085 DF RST 3
23 0086 CDE502 CALL ?BEL
24 0089 3EFF LD A,FFH
25 008B 329D11 LD (SWRK),A
26 008E 2100E8 LD HL,E800H
27 0091 3455 LD (HL),55H
28 0093 1835 JR FD2
29 0095 C00900 CALL NL
30 0098 3E2A LD A,2AH
31 009A C01200 CALL PRNT
32 009D 11A311 LD DE,BUFER
33 00A0 C00300 CALL ?ETL
34 00A3 1A LD A,(DE)
35 00A4 13 INC DE
36 00A5 FE0D CP 0DH
37 00A7 28EC JR Z,ST1
38 00A9 FE4A CP 'J'
39 00AB 280E JR Z,GOTO
40 00AD FE4C CP 'L'
41 00AF 2828 JR Z,LOAD
42 00B1 FE46 CP 'F'
43 00B3 2812 JR Z,FD
44 00B5 FE42 CP 'B'
45 00B7 2808 JR Z,SG
46 00B9 18E8 JR ST2
47 00BB 1 JUMP COMMAND
48 00BB 1
49 00BB 1
50 00BB C01004 GOTO: CALL HLHEX
51 00BE 38D5 JR C,ST1
52 00C0 E9 JP (HL)
53 00C1 53 00C1
54 00C1 54 00C1
55 00C1 55 00C1
56 00C1 3A9D11 SG: LD A,(SWRK)
57 00C4 2F CPL
58 00C5 18C4 JR SS+2
59 00C7 59 00C7
60 00C7 60 00C7

SS:
ST1:
ST2:

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** 280 ASSEMBLER SB-7201 <M2-80A.MONITOR> PAGE 04

```

01 012B OF
02 012C D2860E
03 012F 2E00
04 0131 24
05 0132 FE18
06 0134 2804
07 0136 24
08 0137 C3660E
09 013A 227111
10 013D
11 0130
12 0130
13 0130
14 0130 01C003
15 0140 1100D0
16 0143 2128D0
17 0146 ED80
18 0148 EB
19 0149 0628
20 014B CDD80F
21 014E 011A00
22 0151 117311
23 0154 217411
24 0157 ED80
25 0159 3600
26 015B 3A7311
27 015E B7
28 015F CAE50E
29 0162 217211
30 0165 35
31 0166 18D5
32 0168
33 0168
34 0168
35 0168
36 0168
37 0168 3D01
38 016A 5D0E
39 016C 6E0E
40 016E 780E
41 0170 950E
42 0172 0904
43 0174 B30E
44 0176 F20E
45 0178 2D0F
46 017A E10E
47 017C EE0E
48 017E E50E
49 0180 170A
50 0182 2801
51 0184 E50E
52 0186 E50E
53 0188
54 0188
55 0188
56 0188
57 0188
58 0188
59 0188
60 0188

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** 280 ASSEMBLER SB-7201 <M2-80A.MONITOR> PAGE 03

```

01 00C7
02 00C7 2100F0
03 00CA 7E
04 00CB B7
05 00CC 20C7
06 00CE E9
07 00CF
08 00CF
09 00CF
10 00CF
11 00CF FE02
12 00D1 28C2
13 00D3 111801
14 00D6 DF
15 00D7 18BC
16 00D9
17 00D9
18 00D9
19 00D9
20 00D9 CDEF04
21 00DC 38F1
22 00DE CD0900
23 00E1 11F700
24 00E4 DF
25 00E5 11F110
26 00E8 DF04
27 00E9 CDEF04
28 00EC 38E1
29 00EE 2A0611
30 00F1 7C
31 00F2 FE12
32 00F4 389F
33 00F6 E9
34 00F7 4C
35 00F8 B7A1
36 00FA 9CA6
37 00FC B097
38 00FE 20D0
39 0100 2A2A2020
40 0104 40AF4E49
41 0108 54AF5220
42 010C 53412D31
43 0110 35313020
44 0114 202A2A
45 0117 0D
46 0118
47 0118 43
48 0118 43
49 0119 9892
50 011B 9FA9
51 011D 20A4
52 011F A5B3
53 0121 2092
54 0123 9D9D
55 0125 B79D
56 0127 0D
57 0128
58 0128
59 0128
60 0128 CD2B0A

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** 280 ASSEMBLER SB-7201 <M2-80A.MONITOR> PAGE 04

```

01 012B OF
02 012C D2860E
03 012F 2E00
04 0131 24
05 0132 FE18
06 0134 2804
07 0136 24
08 0137 C3660E
09 013A 227111
10 013D
11 0130
12 0130
13 0130
14 0130 01C003
15 0140 1100D0
16 0143 2128D0
17 0146 ED80
18 0148 EB
19 0149 0628
20 014B CDD80F
21 014E 011A00
22 0151 117311
23 0154 217411
24 0157 ED80
25 0159 3600
26 015B 3A7311
27 015E B7
28 015F CAE50E
29 0162 217211
30 0165 35
31 0166 18D5
32 0168
33 0168
34 0168
35 0168
36 0168
37 0168 3D01
38 016A 5D0E
39 016C 6E0E
40 016E 780E
41 0170 950E
42 0172 0904
43 0174 B30E
44 0176 F20E
45 0178 2D0F
46 017A E10E
47 017C EE0E
48 017E E50E
49 0180 170A
50 0182 2801
51 0184 E50E
52 0186 E50E
53 0188
54 0188
55 0188
56 0188
57 0188
58 0188
59 0188
60 0188

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** 180 ASSEMBLER SB-7201 <M2-80A.MONITOR>

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PAGE 05

** 180 ASSEMBLER SB-7201 <M2-80A.MONITOR>

```

01 0188      1      ?MLDY:      ENT      BC      INC      DE
02 0189      2      PUSH      BC      RET      BC
03 0188 C5      PUSH      DE      INC      HL
04 0189 D5      PUSH      HL      INC      HL
05 018A E5      LD      A,02H      LD      E,(HL)
06 018B 3E02    LD      (OCTV),A      LD      D,(HL)
07 018D 32A011 LD      B,01      LD      DE,HL
08 0190 0601    LD      A,(DE)      LD      A,H
09 0192 1A      CP      0DH      OR      A
10 0193 FE0D    JR      Z,MLD4      JR      Z,*11
11 0195 253B    CP      C8H      LD      A,(OCTV)
12 0197 FEC8    JR      Z,MLD4      DEC      A
13 0199 2837    CP      CFH      JR      Z,*5
14 019B FECF    JR      Z,MLD2      ADD      HL,HL
15 019D 2827    CP      2DH      JR      -4
16 019F FE2D    JR      Z,MLD2      LD      (RATIO),HL
17 01A1 2823    CP      28H      LD      HL,OCTV
18 01A3 FE2B    JR      Z,MLD3      LD      (HL),2
19 01A5 2827    CP      07H      LD      HL
20 01A7 FED7    JR      Z,MLD3      DEC      HL
21 01A9 2823    CP      23H      POP      DE
22 01AB FE23    LD      HL,MTBL      INC      DE
23 01AD 212902 LD      NZ,*6      LD      A,(DE)
24 01B0 2004    LD      HL,MTBL      LD      B,A
25 01B2 214102 LD      DE      AND      FOH
26 01B5 13      INC      DE      CP      30H
27 01B6 CDD001 CALL      ONPU      JR      Z,*5
28 01B9 38D7    JR      C,MLD1      LD      A,(HL)
29 01BB CDC802 CALL      RYTHM      JR      +7
30 01BE 3815    JR      C,MLD5      INC      DE
31 01C0 CDAB02 CALL      MLDST      LD      A,B
32 01C3 41      LD      B,C      AND      OFH
33 01C4 18CC    JR      MLD1      LD      (HL),A
34 01C6 3E03    LD      A,*3      LD      HL,OPTBL
35 01C8 32A011 LD      (OCTV),A      LD      A,L
36 01CB 13      INC      DE      LD      L,A
37 01CC 18C4    JR      MLD1      LD      C,(HL)
38 01CE 3E01    LD      A,1      LD      A,(TEMPH)
39 01D0 18F6    JR      MLD2+2      LD      B,A
40 01D2 CDC802 JR      RYTHM      XOR      A
41 01D5 F5      PUSH      AF      JP      ONP3
42 01D6 CDE02   CALL      MLDSP
43 01D9 F1      POP      AF
44 01DA C9F06   JR      RET3
45 01DD      1      ONPU TO RATIO CONV      1      C
46 01DD      1      1      ONPU TO RATIO CONV      1      D
47 01DD      1      1      1      ONPU TO RATIO CONV      1      E
48 01DD      1      1      1      1      ONPU TO RATIO CONV      1      F
49 01DD      1      1      1      1      1      ONPU TO RATIO CONV      1      G
50 01DD      1      1      1      1      1      1      ONPU TO RATIO CONV      1      H
51 01DD C5      ONPU:      PUSH      BC      DEF8 43H
52 01DE 0608    LD      B,8      DEF8 0777H
53 01E0 1A      LD      A,(DE)      DEF8 44H
54 01E1 BE      CP      (HL)      DEF8 06A7H
55 01E2 2809    JR      Z,ONP2      DEF8 45H
56 01E4 23      INC      HL      DEF8 05EDH
57 01E5 23      INC      HL      DEF8 46H
58 01E6 23      INC      HL      DEF8 0598H
59 01E7 10F8    DJNZ      -6      DEF8 47H
60 01E9 37      SCF      DEF8 04FCH
      DEF8 41H
      DEF8 41H
      DEF8 0471H
      DEF8 42H
      DEF8 03F5H
      DEF8 52H
      DEF8 0
      DEF8 43H
      MTBL:      DEF8 43H

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** I80 ASSEMBLER SB-7201 <M1-80A.MONITOR>

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** I80 ASSEMBLER SB-7201 <M1-80A.MONITOR>

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```

01 0242 0C07      DEFN 070CH      ! #D
02 0244 44        DEFB 44H
03 0245 4706      DEFB 0447H
04 0247 45        DEFB 45H
05 0248 9805      DEFN 0598H
06 024A 46        DEFB 46H
07 024B 4805      DEFN 0548H
08 024D 47        DEFB 47H
09 024E 8404      DEFN 0484H
10 0250 41        DEFB 41H
11 0251 3104      DEFN 0431H
12 0253 42        DEFB 42H
13 0254 BB03      DEFN 03BBH
14 0256 52        DEFB 52H
15 0257          !
16 0257 0000      DEFN 0
17 0259 01        DEFB 1
18 025A 02        DEFB 2
19 025B 03        DEFB 3
20 025C 04        DEFB 4
21 025D 06        DEFB 6
22 025E 08        DEFB 8
23 025F 0C        DEFB 0CH
24 0260 10        DEFB 10H
25 0261 18        DEFB 18H
26 0262 20        DEFB 20H
27 0263          !
28 0263          !
29 0263          !
30 0263          !
31 0263 219211    ?SAVE: LD HL,FLSDT
32 0266 36EF      LD (HL),EFH
33 0268 3A7011    LD A,(KANAF)
34 026B 87        OR A
35 026C 2802      JR Z,KSLO
36 026E 36FF      LD (HL),FFH
37 0270 7E        LD A,(HL)
38 0271 F5        PUSH AF
39 0272 CDB10F    CALL ?PONT
40 0275 7E        LD A,(HL)
41 0276 328E11    LD (FLASH),A
42 0279 F1        POP AF
43 027A 77        LD (HL),A
44 027B AF        XOR A
45 027C 2100E0    LD HL,KEYPA
46 027F 77        LD (HL),A
47 0280 2F        CPL
48 0281 77        LD (HL),A
49 0282 C9        RET
50 0283          !
51 0283          !
52 0283          !
53 0283          !
54 0283          !
55 0283          !
56 0283 F5        MGP,I: PUSH AF
57 0284 E5        PUSH HL
58 0285 217C11    LD HL,MGPNT
59 0288 7E        LD A,(HL)
60 0289 3C        INC A

01 028A FE33      CP 51
02 028C 2001      JR NZ,MGP0
03 028E AF        XOR A
04 028F E5        MGP0: PUSH HL
05 0290 6F        LD L,A
06 0291 3A9111    LD A,(SPAGE)
07 0294 B7        OR A
08 0295 7D        LD A,L
09 0296 E1        POP HL
10 0297 2001      JR NZ,*3
11 0299 77        LD (HL),A
12 029A E1        POP HL
13 029B F1        POP AF
14 029C C9        RET
15 029D          !
16 029D          !
17 029D          !
18 029D F5        MGP,D: PUSH AF
19 029E E5        PUSH HL,MGPNT
20 029F 217C11    LD L,A,(HL)
21 02A2 7E        DEC A
22 02A3 3D        JP P,MGP0
23 02A4 F28F02    LD A,30
24 02A7 3E32      JR MGP0
25 02A9 18E4      !
26 02AB          !
27 02AB          !
28 02AB          !
29 02AB          !
30 02AB          !
31 02AB 2AA111    MLDST: LD HL,(RATIO)
32 02AE 7C        LD A,H
33 02AF B7        OR A
34 02B0 280C      JR Z,MLDSP
35 02B2 D5        PUSH DE
36 02B3 EB        EX DE,HL
37 02B4 2104E0    LD HL,CONTO
38 02B7 73        LD (HL),E
39 02B8 72        LD (HL),D
40 02B9 3E01      LD A,1
41 02BB D1        POP DE
42 02BC 1806      JR MLDST
43 02BE          !
44 02BE 3E34      MLDSP: LD A,34H
45 02C0 3207E0    LD (CONTF),A
46 02C3 AF        XOR A
47 02C4 3208E0    MLDST: LD (SUNDG),A
48 02C7 C9        RET
49 02C8          !
50 02C8          !
51 02C8          !
52 02C8          !
53 02C8          !
54 02C8          !
55 02C8          !
56 02C8 2100E0    RYTHM: LD HL,KEYPA
57 02CB 36F0      LD (HL),FOH
58 02CD 23        INC HL
59 02CE 7E        LD A,(HL)
60 02CF E681      AND SIM

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** I80 ASSEMBLER SB-7201 <M1-80A.MONITOR>

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** I80 ASSEMBLER SB-7201 <M1-80A.MONITOR>

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```

01 0201 2002      JR      NZ,+4
02 0203 37        SCF
03 0204 C9        RET
04 0205 2A08E0    LD      A,(TEMP)
05 0208 0F        RRCA
06 0209 38FA     JR      C,-4
07 020B 3A08E0    LD      A,(TEMP)
08 020E 0F        RRCA
09 020F 30FA     JR      NC,-4
10 0211 10F2     DJNZ    -12
11 0213 AF        XOR     A
12 0214 C9        RET
13 0215          : BELL
14 0215          :
15 0215          ?BEL: ENT  DE
16 0215          LD      DE,?BELD
17 0215          RST     6
18 0215          POP     DE
19 0215          RET
19 0215          : CALL MELDY
20 0215          :
21 0215          :
22 0215          :
23 0215          :
24 0215          :
25 0215          :
26 0215          :
27 0215          :
28 0215          :
29 0215          :
30 0215          :
31 0215          :
32 0215          :
33 0215          :
34 0215          :
35 0215          :
36 0215          :
37 0215          :
38 0215          :
39 0215          :
40 0215          :
41 0215          :
42 0215          :
43 0215          :
44 0215          :
45 0215          :
46 0215          :
47 0215          :
48 0215          :
49 0215          :
50 0215          :
51 0215          :
52 0215          :
53 0215          :
54 0215          :
55 0215          :
56 0215          :
57 0215          :
58 0215          :
59 0215          :
60 0215          :

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<M2-80A.MONITOR>

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<M2-80A.MONITOR>

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01 0357 ED52      SBC      HL,DE
02 0359 3810      JR      C,THR2
03 035B EB        EX      DE,HL
04 035C 3A9B11    LD      A,(AMPH)
05 035F E1        POP     HL
06 0360 C9        RET
07 0361 11C0A8    ?THR1:  LD      DE,ASCOH
08 0364 2A9B11    LD      A,(AMPH)
09 0367 EE01      XOR     I
10 0369 E1        POP     HL
11 036A C9        RET
12 036B F3        ?THR2:  DI
13 036C 210AEO    LD      HL,CONT2
14 036F 7E        LD      A,(HL)
15 0370 2F        CPL
16 0371 5F        LD      E,A
17 0372 7E        LD      A,(HL)
18 0373 2F        CPL
19 0374 57        LD      D,A
20 0375 FB        EI
21 0376 13        INC     DE
22 0377 18EB      JR      ?THR1+3
23 0379
24 0379
25 0379
26 0379
27 0379          ! TIME INTERRUPT
28 0379 F5        ENT     PUSH AF
29 037A C5        PUSH BC
30 037B D5        PUSH DE
31 037C E5        PUSH HL
32 037D 219B11    LD      HL,AMPH
33 0380 7E        LD      A,(HL)
34 0381 EE01      XOR     I
35 0383 77        LD      (HL),A
36 0384 2107EO    LD      HL,CONTF
37 0387 3680      LD      (HL),80H
38 0389 28        DEC     HL
39 038A E5        PUSH HL
40 038B 5E        LD      E,(HL)
41 038C 5A        LD      D,(HL)
42 038D 21C0A8    LD      HL,ASCOH
43 0390 19        ADD     HL,DE
44 0391 28        DEC     HL
45 0392 28        DEC     HL
46 0393 EB        EX      DE,HL
47 0394 E1        POP     HL
48 0395 73        LD      (HL),E
49 0396 72        LD      (HL),D
50 0397 E1        POP     HL
51 0398 D1        POP     DE
52 0399 C1        POP     BC
53 039A F1        POP     AF
54 039B FB        EI
55 039C C9        RET
56 039D
57 039D
58 039D
59 039D EB        .DSP03: EX  DE,HL
60 039E 3601      LD      (HL),+1

01 03A0 23        INC     HL
02 03A1 3600      LD      (HL),O
03 03A3 C37B0E    JP      CURSR
04 03A6
05 03A6
06 03A6
07 03A6          ! MANAGEMENT PAGE MODE1
08 03A6
09 03A6          !
10 03A6 3A7211    .MANG2:  LD      A,(DSPXY+1)
11 03A9 85        ADD     A,L
12 03AA 6F        LD      L,A
13 03AB 7E        LD      A,(HL)
14 03AC 23        INC     HL
15 03AD CB16      RL      (HL)
16 03AF BA        OR      (HL)
17 03B0 CB1E      RR      (HL)
18 03B2 0F        RRCA
19 03B3 EB        EX      DE,HL
20 03B4 2A7111    LD      HL,(DSPXY)
21 03B7 C9        RET
22 03B8
23 03B8
24 03BA          ! PRNTHL 9
25 03BA          !
26 03BA 7C        ORG     03BAH
27 03BB CDC303    LD      A,H
28 03BE 7D        CALL  PRTHX
29 03BF 1802      LD      A,L
30 03C1          JR      PRTHX
31 03C3          !
32 03C3          ! ORG     03C3H
33 03C3          !
34 03C3          ! (ACC) PRINT
35 03C3          ! PRTHX:  ENT     PUSH AF
36 03C3 F5        RRCA
37 03C4 0F        RRCA
38 03C5 0F        RRCA
39 03C6 0F        RRCA
40 03C7 0F        RRCA
41 03C8 CDDA03    CALL  ASC
42 03CB CD1200    CALL  PRNT
43 03CE F1        POP     AF
44 03CF CDDA03    CALL  ASC
45 03D2 C31200    JP      PRNT
46 03D5
47 03D5          !
48 03D5          ! GETL RETURN
49 03D5          !
50 03D5 D1        GETLL:  POP     DE
51 03D6 E1        POP     HL
52 03D7 C1        POP     BC
53 03D8 F1        POP     AF
54 03D9 C9        RET
55 03DA
56 03DA          !
57 03DA          !
58 03DA          ! ORG     03DAH
59 03DA          !
60 03DA          ! HEXA TO ASCII

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** 180 ASSEMBLER SB-7201 <M1-80A.MONITOR> PAGE 13 09/04/81
01 03DA ASC: ENT AND OFH
02 03DA E60F CP OAH
03 03DA FE0A JR C,NOADD
04 03DC FE0A CP A,7
05 03DE 3802 ADD A,30H
06 03E0 C607 NOADD: RET
07 03E2 C630
08 03E4 C9
09 03E5
10 03E5
11 03E5
12 03E5 FE30 HEXJ: CP 30H
13 03E7 DB RET C
14 03E8 FE3A CP 3AH
15 03EA 3806 JR C,HEX1
16 03EC D607 SUB 7
17 03EE FE40 CP 40H
18 03F0 3003 JR NC,HEX2
19 03F2 E60F HEX1: AND OFH
20 03F4 C9 RET
21 03F5 37 HEX2: SCF
22 03F6 C9 RET
23 03F7
24 03F7
25 03F9
26 03F9
27 03F9 18EA HEX: ENT
28 03FB JR HEXJ
29 03FB
30 03FB
31 03FB
32 03FB 2A7111 HOME: LD HL,(DSPXY)
33 03FE 3A7C11 LD A,(MGNT)
34 0401 94 SUB H
35 0402 3002 JR NC,HOM1
36 0404 C632 ADD A,50
37 0406 327C11 HOM1: LD (MGNT),A
38 0409 210000 HOM0: LD HL,0
39 040C C3690E JP CURS3
40 040F
41 040F
42 0410
43 0410
44 0410
45 0410
46 0410
47 0410
48 0410
49 0410
50 0410
51 0410
52 0410 D5 HLHEX: ENT
53 0411 CD1F04 PUSH DE
54 0414 3807 CALL 2HEX
55 0416 67 JR C,*9
56 0417 CD1F04 LD H,A
57 041A 3801 CALL 2HEX
58 041C 6F JR C,*3
59 041D D1 LD L,A
60 041E C9 POP DE
RET

** 180 ASSEMBLER SB-7201 <M1-80A.MONITOR> PAGE 14 09/04/81
01 041F
02 041F
03 041F
04 041F
05 041F
06 041F
07 041F
08 041F
09 041F
10 041F
11 041F
12 041F
13 041F C5 ENT
14 0420 1A PUSH BC
15 0421 13 LD A,(DE)
16 0422 CDF903 INC DE
17 0425 3800 CALL HEX
18 0427 0F JR C,*15
19 0428 0F RRCA
20 0429 0F RRCA
21 042A 0F RRCA
22 042B 4F LD C,A
23 042C 1A LD A,(DE)
24 042D 13 INC DE
25 042E CDF903 CALL HEX
26 0431 3801 JR C,*3
27 0433 B1 OR C
28 0434 C1 2HE1: POP BC
29 0435 C9 RET
30 0436
31 0436
32 0436
33 0436
34 0436
35 0436 F3
36 0437 D5
37 0438 C5
38 0439 E5
39 043A 16D7 LD D,D7H
40 043C 1ECC LD E,CCH
41 043E 21F010 LD HL,IBUFE
42 0441 018000 LD BC,80H
43 0444 CD1A07 CALL CKSUM
44 0447 CDA306 CALL MOTOR
45 044A 3818 JR C,WR13
46 044C 78 LD A,E
47 044D FECC CP CCH
48 044F 200D JR NZ,WR12
49 0451 CD0900 CALL NL
50 0454 D5 PUSH DE
51 0455 116704 LD DE,MSG#7
52 0458 DF RST 3
53 0459 11F110 LD DE,NAME
54 045C DF RST 3
55 045D D1 POP DE
56 045E CD7A07 WR12: CALL GAP
57 0461 CD8504 WR13: CALL WTAPE
58 0464 C35205 JP RET2
59 0467
60 0467 57 MSG#7: DEFH 'W'
WRITING

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** 180 ASSEMBLER SB-7201 (M2-80A-MONITOR) PAGE 15
01 0468 9DA6      DEFN A69DH
02 046A 9A46      DEFN A694H
03 046C B097      DEFN 9780H
04 046E 200D      DEFN 0020H
05 0470          !!
06 0470          !! WRITE DATA
07 0470          !!
08 0470          !! EXIT CF=0 : OK
09 0470          !! =1 : BREAK
10 0470          !!
11 0470          ?WRD: ENT
12 0470 F2      DI
13 0471 D5      PUSH DE
14 0472 C5      PUSH BC
15 0473 E5      PUSH HL
16 0474 16D7    LD D,D7H
17 0476 1E53    LD E,E53H
18 0478 ED80211 LD BC,(SIZE)
19 047C 2A0411  LD HL,(DTADR)
20 047F 78      LD A,B
21 0480 B1      OR C
22 0481 2848    JR Z,RET1
23 0483 18BF    JR WR11
24 0485          !!
25 0485          !! TAPE WRITE
26 0485          !!
27 0485          !! RC=BYTE SIZE
28 0485          !! HL=DATA LOW ADR.
29 0485          !!
30 0485          !! EXIT CF=0 : OK
31 0485          !! =1 : BREAK
32 0485          !!
33 0485          !!
34 0485 D5      WTAPE: PUSH DE
35 0486 C5      PUSH BC
36 0487 E5      PUSH HL
37 0488 16D2    LD D,D2
38 048A 3EFO    LD A,F0H
39 048C 320E0    LD (KEYPA),A
40 048F 7E      LD A,(HL)
41 0490 CD6707  CALL CD6707
42 0493 3A01E0  LD A,(KEYPB)
43 0496 E681    AND 81H
44 0498 C29E04  JP NZ,WTAPE2
45 049B 37      SCF
46 049C 182D    JR WTAPE3
47 049E 23      INC HL
48 049F 0B      DEC BC
49 04A0 78      LD A,B
50 04A1 B1      OR C
51 04A2 C28F04  JP NZ,WTAPE1
52 04A5 2A9711  LD HL,(SUMDT)
53 04A8 7C      LD A,H
54 04A9 CD6707  CALL CD6707
55 04AC 7D      LD A,L
56 04AD CD6707  CALL CD6707
57 04B0 CD570D  CALL LONG
58 04B3 15      DEC D
59 04B4 C28B04  JP NZ,**7
60 04B7 B7      OR A

** 180 ASSEMBLER SB-7201 (M2-80A-MONITOR) PAGE 16
01 04B8 C3CB04  JP WTAPE3
02 04B8 0600    LD B,0
03 04BD CD3E0D  CALL SHORT
04 04C0 05      DEC B
05 04C1 C2BD04  JP NZ,-4
06 04C4 E1      POP HL
07 04C5 C1      POP BC
08 04C6 C5      PUSH BC
09 04C7 E5      PUSH HL
10 04C8 C38F04  JP WTAPE1
11 04CB E1      RET1: POP HL
12 04CB E1      POP BC
13 04CC C1      POP DE
14 04CD D1      RET
15 04CE C9      RET
16 04CF          !!
17 04CF          !! READ INFORMATION
18 04CF          !!
19 04CF          !! EXIT ACC=0 : OK CF=0
20 04CF          !! =1 : ER CF=1
21 04CF          !! =2 : BREAK CF=1
22 04CF          !!
23 04CF          !!
24 04CF          ?RDI: ENT
25 04CF F3      DI
26 04D0 D5      PUSH DE
27 04D1 C5      PUSH BC
28 04D2 E5      PUSH HL
29 04D3 16D2    LD D,D2H
30 04D5 1ECC    LD E,CCH
31 04D7 018000  LD BC,80H
32 04DA 21F010  LD HL,1BUFE
33 04DD CDA306  CALL MOTOR
34 04E0 DA7005  JP C,RTF6
35 04E3 CD5806  CALL TMRK
36 04E6 DA7005  JP C,RTF6
37 04E9 CD0503  CALL RTAPE
38 04EC C35205  JP RTF4
39 04EF          !! READ DATA
40 04EF          !!
41 04EF          !! EXIT SAME UP
42 04EF          !!
43 04EF          !!
44 04EF          ?RDI: ENT
45 04EF F3      DI
46 04F0 D5      PUSH DE
47 04F1 C5      PUSH BC
48 04F2 E5      PUSH HL
49 04F3 16D2    LD D,D2H
50 04F5 1E53    LD E,E53H
51 04F7 ED80211 LD BC,(SIZE)
52 04FB 2A0411  LD HL,(DTADR)
53 04FE 78      LD A,B
54 04FF B1      OR C
55 0500 CA5205  JP 2,RTF4
56 0503 1808    JR RD1
57 0505          !!
58 0505          !! READ TAPE
59 0505          !!
60 0505          !!

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** 180 ASSEMBLER SB-7201 <M2-80A.MONITOR> PAGE 19 09/04/81

01 05B5 54 LD D,H
02 05B6 E1 POP HL
03 05B7 C1 POP BC
04 05B8 C5 PUSH BC
05 05B9 E5 PUSH HL
06 05BA CD2406 CALL RBYTE
07 05BD DA7005 JP C,RTF6
08 05C0 BE CP (HL)
09 05C1 C26C05 JP NZ,RTF7
10 05C4 23 INC HL
11 05C5 08 DEC BC
12 05C6 78 LD A,B
13 05C7 B1 OR C
14 05C8 C2BA05 JP NZ,TVF3
15 05CB 2A9911 LD HL,(CSMDT)
16 05CE CD2406 CALL RBYTE
17 05D1 BC CP H
18 05D2 2098 JP NZ,RTF7
19 05D4 CD2406 CALL RBYTE
20 05D7 8D CP L
21 05D8 2092 JR NZ,RTF7
22 05DA 15 DEC D
23 05DB CA5105 JP Z,RTF8
24 05DE 62 LD H,D
25 05DF 18BF JR TVF1
26 05E1 PRINT '00'
27 05E1
28 05E1
29 05E1 11FC09 GETLD: LD DE,00HSG
30 05E4 DF RST 3
31 05E5 C30708 JP AUTO2
32 05E8
33 05E8
34 05E8
35 05E8
36 05E8
37 05E8 217A11 ROLUP: LD HL,PBIAS
38 05EB 3A7F11 LD A,(ROLEND)
39 05EE BE CP (HL)
40 05EF CAE50E JP Z,TRSTR
41 05F2 C3A90F JP ROLU1
42 05F5
43 05F5
44 05F5
45 05F5
46 05F5 F5 FLASING DATA LOAD
47 05F6 3ABE11 *LOAD: PUSH AF
48 05F9 CDB10F LD A,(FLASH)
49 05FC 77 CALL ?PONT
50 05FD F1 LD (HL),A
51 05FE C9 POP AF
52 05FF RET
53 05FF
54 05FF
55 0601 ORG 0601H
56 0601
57 0601
58 0601
59 0601
60 0601
61 0601
62 0601
63 0601
64 0601
65 0601
66 0601
67 0601
68 0601
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97 0601
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99 0601
100 0601

** 180 ASSEMBLER SB-7201 <M2-80A.MONITOR> PAGE 20 09/04/81

01 0601 LD DE,CSTR
02 0601
03 0601
04 0601
05 0601 3EFO EDGE: LD A,FOH
06 0603 3200E0 LD (KEYFA),A
07 0606 00 NOP
08 0607 0A LD A,(BC)
09 0608 E681 EDG1: LD A,(BC)
10 060A C20F06 AND 81H
11 060D 37 JP NZ,*5
12 060E C9 SCF
13 060F 1A RET
14 0610 E620 LD A,(DE)
15 0612 C20706 AND 20H
16 0615 0A LD A,(BC)
17 0616 E681 EDG2: LD A,(BC)
18 0618 C21D06 AND 81H
19 061B 37 JP NZ,*5
20 061C C9 SCF
21 061D 1A RET
22 061E E620 LD A,(DE)
23 0620 CA1506 AND 20H
24 0623 C9 LD A,(BC)
25 0624 RET
26 0624
27 0624
28 0624
29 0624
30 0624
31 0624
32 0624
33 0624
34 0624
35 0624
36 0624 C5 RBYTE: PUSH BC
37 0625 D5 PUSH DE
38 0626 E5 PUSH HL
39 0627 210008 LD HL,0800H
40 062A 0101E0 LD BC,KEYPB
41 062D 1102E0 LD DE,CSTR
42 0630 CD0106 EDGE: LD A,DE
43 0633 DA5406 CALL DLY3
44 0636 CDA209 LD A,(DE)
45 0639 1A AND 20H
46 063A E620 LD A,(DE)
47 063C CA4906 LD A,(SUMDT)
48 063F E5 INC HL
49 0640 2A9711 INC HL
50 0643 23 LD (SUMDT),HL
51 0644 229711 POP HL
52 0647 E1 SCF
53 0648 37 LD A,L
54 0649 7D RLA
55 064A 17 LD L,A
56 064B 6F DEC H
57 064C 25 JP NZ,RBY1
58 064D C23006 CALL EDGE
59 0650 CD0106 LD A,L
60 0653 7D

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** 180 ASSEMBLER SB-7201 <M7-80A.MONITOR> PAGE 22

1 2 SEC DLY

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** 180 ASSEMBLER SB-7201 <M7-80A.MONITOR> PAGE 21

```

01 0654 E1 RBYJ: POP HL
02 0655 D1 POP DE
03 0656 C1 POP BC
04 0657 C9 RET
05 0658
06 0659
07 065A
08 065B
09 065C
10 065D
11 065E
12 065F
13 0660
14 0661
15 0662
16 0663
17 0664
18 0665
19 0666
20 0667
21 0668
22 0669
23 066A
24 066B
25 066C
26 066D
27 066E
28 066F
29 0670
30 0671
31 0672
32 0673
33 0674
34 0675
35 0676
36 0677
37 0678
38 0679
39 067A
40 067B
41 067C
42 067D
43 067E
44 067F
45 0680
46 0681
47 0682
48 0683
49 0684
50 0685
51 0686
52 0687
53 0688
54 0689
55 068A
56 068B
57 068C
58 068D
59 068E
60 068F

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01 06A8 3A02E0 LD A,(CSTR)
02 06A9 E810 AND 10H
03 06AA 280A JR Z,MOT4
04 06AB 06A6 LD B,A6H
05 06AC CDA70D CALL DLY12
06 06AD 10FB DJNZ -3
07 06AE AF XOR A
08 06AF 18E6 JR RET3
09 06B0 3E0A LD A,06H
10 06B1 2103E0 LD HL,CSTPT
11 06B2 77 LD (HL),A
12 06B3 3C INC A
13 06B4 77 LD (HL),A
14 06B5 10E5 DJNZ MOT1
15 06B6 C0900 CALL NL
16 06B7 7A LD A,D
17 06B8 FE07 CP D7H
18 06B9 2805 JR Z,MOT8
19 06BA 119E0D LD DE,MSG01
20 06BB 1807 JR MOT9
21 06BC 11F406 LD DE,MSG03
22 06BD 3 RST 3
23 06BE 11A00D LD DE,MSG02
24 06BF DF RST 3
25 06C0 3A02E0 LD A,(CSTR)
26 06C1 E810 AND 10H
27 06C2 2000 JR NZ,MOT2
28 06C3 CD110D CALL -8RK
29 06C4 20F4 JR NZ,MOT5
30 06C5 37 SCF
31 06C6 18D0 JR MOT7
32 06C7
33 06C8
34 06C9
35 06CA
36 06CB
37 06CC
38 06CD
39 06CE
40 06CF
41 06D0
42 06D1
43 06D2
44 06D3
45 06D4
46 06D5
47 06D6
48 06D7
49 06D8
50 06D9
51 06DA
52 06DB
53 06DC
54 06DD
55 06DE
56 06DF
57 06E0
58 06E1
59 06E2
60 06E3

```

```

MOT1: LD A,(CSTR)
MOT2: LD B,A6H
MOT3: LD HL,CSTPT
MOT4: LD (HL),A
MOT5: INC A
MOT6: LD (HL),A
MOT7: DJNZ MOT1
MOT8: CALL NL
MOT9: LD A,D
MOT10: CP D7H
MOT11: JR Z,MOT8
MOT12: LD DE,MSG01
MOT13: JR MOT9
MOT14: LD DE,MSG03
MOT15: RST 3
MOT16: LD DE,MSG02
MOT17: RST 3
MOT18: LD A,(CSTR)
MOT19: AND 10H
MOT20: JR NZ,MOT2
MOT21: CALL -8RK
MOT22: JR NZ,MOT5
MOT23: SCF
MOT24: JR MOT7
MOT25:
MOT26:
MOT27:
MOT28:
MOT29:
MOT30:
MOT31:
MOT32:
MOT33:
MOT34:
MOT35:
MOT36:
MOT37:
MOT38:
MOT39:
MOT40:
MOT41:
MOT42:
MOT43:
MOT44:
MOT45:
MOT46:
MOT47:
MOT48:
MOT49:
MOT50:
MOT51:
MOT52:
MOT53:
MOT54:
MOT55:
MOT56:
MOT57:
MOT58:
MOT59:
MOT60:

```

```

1 2 SEC DLY
1 CALL MSGX
1 CALL MSGX
1 PRESS RECORD
1 MSTOP 89

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280 ASSEMBLER SB-7201 <NI-80A.MONITOR>

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280 ASSEMBLER SB-7201 <NI-80A.MONITOR>

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01 0705 3A02E0      MST1: LD A,(CSTR)
02 0708 E610        AND 10H
03 070A 280B      JR 2,MST3
04 070C 3E0A      LD A,06H
05 070E 3203E0    LD (CSTPT),A
06 0711 3C        INC A
07 0712 3203E0    LD (CSTPT),A
08 0715 10EE      DJNZ MST1
09 0717 C3E0E     JP 2RSTR1
10 071A           1
11 071A           1
12 071A           1
13 071A           1
14 071A           1
15 071A           1
16 071A           1
17 071A           1
18 071A           1
19 071A           1
20 071A           1
21 071A C5        BC=SIZE
22 071B D5        HL=DATA ADDR.
23 071C E5        EXIT SUMDT=STORE
24 071D 110000    CSMDT=STORE
25 0720 78        OR C
26 0721 B1        JR NZ,CKS2
28 0724 EB        EX DE,HL
29 0725 229711   LD (SUMDT),HL
30 0728 229911   LD (CSMDT),HL
31 072B E1        POP HL
32 072C D1        POP DE
33 072D C1        POP BC
34 072E C9        RET
35 072F 7E        LD A,(HL)
36 0730 C5        PUSH BC
37 0731 0608      LD B,*8
38 0733 07        RLCA
39 0734 3001      JR NC,*3
40 0736 13        INC DE
41 0737 10FA      DJNZ CKS3
42 0739 C1        POP BC
43 073A 23        INC HL
44 073B 0B        DEC BC
45 073C 18E2      JR CKS1
46 073E           1
47 073E           1
48 073E           1
49 073E 07        SNEP PART2
50 073F 07        SNEP4: RLCA
51 0740 07        RLCA
52 0741 4F        LD C,A
53 0742 7B        LD A,E
54 0743 25        DEC H
55 0744 0F        RLCA
56 0745 30FC      JR NC,-2
57 0747 7C        LD A,H
58 0748 81        ADD A,C
59 0749 4F        LD C,A
60 074A C340A     JP SNEP01

01 074D           1
02 074D           1
03 074D           1
04 074D           1
05 074D 2103E0    LD HL,KEYPF
06 0750 368A      LD (HL),8AH
07 0752 3607      LD (HL),07H
08 0754 3605      LD (HL),05H
09 0756 3601      LD (HL),01H
10 0758 C9        RET
11 0759           1
12 0759           1
13 0759           1
14 0759           1
15 0759           1
16 0759           1
17 0759           1
18 0759           1
19 0759 3E0E      DLY1: LD A,14
20 075B 3D        DEC A
21 075C C25B07    JP NZ,-1
22 075F C9        RET
23 0760           1
24 0760           1
25 0760           1
26 0760 3E0D      DLY2: LD A,13
27 0762 3D        DEC A
28 0763 C26207    JP NZ,-1
29 0766 C9        RET
30 0767           1
31 0767           1
32 0767           1
33 0767           1
34 0767           1
35 0767 C5        MBYTE: PUSH BC
36 0768 0608      LD B,*8
37 076A CD570D    CALL LONG
38 076D 07        RLCA
39 076E DC570D    CALL C, LONG
40 0771 D43E0D    CALL NC, SHORT
41 0774 05        DEC B
42 0775 C26007    JP NZ, MBY1
43 0778 C1        POP BC
44 0779 C9        RET
45 077A           1
46 077A           1
47 077A           1
48 077A           1
49 077A           1
50 077A           1
51 077A           1
52 077A C5        GAP: PUSH BC
53 077B D5        PUSH DE
54 077C 7B        LD A,E
55 077D 01F055    LD BC,55F0H
56 0780 112828    LD DE,2828H
57 0783 FECC      CP CCHAP1
58 0785 C8E07     JP Z,GAP1
59 0788 01F82A    LD BC,2AF8H
60 078B 111414    LD DE,1414H

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** 280 ASSEMBLER SB-7201 <M2-80A.MONITOR> PAGE 25
01 078E CD3E0D      CALL SHORT
02 0791 08          DEC BC
03 0792 78          LD A,B
04 0793 B1          OR C
05 0794 20F8        JR NZ,-6
06 0796 CD570D      CALL LONG
07 0799 15          DEC D
08 079A 20FA        JR NZ,-4
09 079C CD3E0D      CALL SHORT
10 079F 1D          DEC E
11 07A0 20FA        JR NZ,-4
12 07A2 CD570D      CALL LONG
13 07A5 D1          POP DE
14 07A6 C1          POP BC
15 07A7 C9          RET
16 07A8 P
17 07A8 P          KEYPA: EQU E000H
18 07A8 P          KEYPB: EQU E001H
19 07A8 P          KEYPC: EQU E002H
20 07A8 P          KEYPD: EQU E003H
21 07A8 P          CSTR: EQU E004H
22 07A8 P          CSIP1: EQU E005H
23 07A8 P          CONTO: EQU E006H
24 07A8 P          CONT1: EQU E007H
25 07A8 P          CONT2: EQU E008H
26 07A8 P          SUNDG: EQU E009H
27 07A8 P          TEMP: EQU E00AH
28 07A8 P          SKP
29 07A8 P

** 280 ASSEMBLER SB-7201 <M2-80A.MONITOR> PAGE 26
01 07A8 P
02 07A8 P
03 07A8 P
04 07A8 P
05 07A8 P
06 07A8 P
07 07A8 P
08 07A8 P
09 07A8 P
10 07A8 P
11 07A8 P
12 07A8 P
13 07A8 P
14 07A8 P
15 07A8 P
16 07A8 P
17 07A8 P
18 07A8 P
19 07A8 P
20 07A8 P
21 07A8 P
22 07A8 P
23 07A8 P
24 07A8 P
25 07A8 P
26 07A8 P
27 07A8 P
28 07A8 P
29 07A8 P

GET 1 LINE STATEMENT *
DE = DATA STORE LOW ADR.
( END =CR )
ORG 07E6H

?GETL: ENT
PUSH AF
PUSH BC
PUSH HL
PUSH DE
CALL ?SAVE
CALL ?KEY
CP CBH
JR Z,-5
CALL ?KEY
CALL ?FLAS
JR Z,-6
PUSH AF
XOR A
LD (STROF),A
POP AF
LD B,A
CALL ?LOAD
LD A,(SWRK)
OR A
CALL Z,BEL
LD A,B
CP E7H
JP Z,GETL
CP E6H
JR Z,CHOPK
CP E5H
JR Z,CHGPA
CP E4H
JR Z,DMT
CP E3H
JR Z,LOCK
JR NC,GETL1
AND F0H
CP C0H
JR NZ,GETL2
LD A,B
CP CDH
JR Z,GETL3
CP CBH
JR Z,GETL4
CP C7H
JR NC,GETL5
LD A,(KANAF)
OR A
LD A,B
JR Z,GETL5
LD A,B
CALL ?DSP
CALL ?SAVE
LD A,(STROF)
OR A

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280 ASSEMBLER SB-7201 <M1-80A-MONITOR>

```

01 0871 23      INC HL
02 0872 CB9C    RES 3,H
03 0873 13      INC DE
04 0874 13      DJNZ -9
05 0875 10F5    EX DE,HL
06 0876 10F5    LD (HL),ODH
07 0877 2B      DEC HL
08 0878 7E      LD A,(HL)
09 0879 7E      CP 20H
10 087A 21-6     JR 21-6
11 087B 21-6     GETLR: CALL ?LTNL
12 087C 21-6     JP GETLR
13 087D 21-6     ;
14 087E 21-6     ;
15 087F 21-6     ;
16 0880 21-6     ;
17 0881 21-6     ;
18 0882 21-6     ;
19 0883 21-6     ;
20 0884 21-6     ;
21 0885 21-6     ;
22 0886 21-6     ;
23 0887 21-6     ;
24 0888 21-6     ;
25 0889 21-6     ;
26 088A 21-6     ;
27 088B 21-6     ;
28 088C 21-6     ;
29 088D 21-6     ;
30 088E 21-6     ;
31 088F 21-6     ;
32 0890 21-6     ;
33 0891 21-6     ;
34 0892 21-6     ;
35 0893 21-6     ;
36 0894 21-6     ;
37 0895 21-6     ;
38 0896 21-6     ;
39 0897 21-6     ;
40 0898 21-6     ;
41 0899 21-6     ;
42 089A 21-6     ;
43 089B 21-6     ;
44 089C 21-6     ;
45 089D 21-6     ;
46 089E 21-6     ;
47 089F 21-6     ;
48 08A0 21-6     ;
49 08A1 21-6     ;
50 08A2 21-6     ;
51 08A3 21-6     ;
52 08A4 21-6     ;
53 08A5 21-6     ;
54 08A6 21-6     ;
55 08A7 21-6     ;
56 08A8 21-6     ;
57 08A9 21-6     ;
58 08AA 21-6     ;
59 08AB 21-6     ;
60 08AC 21-6     ;

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280 ASSEMBLER SB-7201 <M1-80A-MONITOR>

```

01 080E 2014     JR NZ,AUT05
02 0810 1E14     LD E,20
03 0812 CDCA08   CALL ?KEY
04 0815 20AD     JR NZ,AUT03
05 0817 CDF109   CALL AUTCK
06 081A 389A     CALL C,GETL1
07 081C 1D       DEC E
08 081D 20F3     JR NZ,AUTOL*2
09 081F 3E01     LD A,1
10 0821 329311   LD (STRG1),A
11 0824 CDA70D   CALL DLY12
12 0827 CDA70D   CALL DLY12
13 082A CDCA08   CALL ?KEY
14 082D CDF09    CALL ?FLAS
15 0830 208C     JR NZ,AUT03-6
16 0832 CDF109   CALL AUTCK
17 0835 38E3     JR C,GETL11
18 0837 188C     JR AUT03*1
19 0839 CDDC0D   CALL ?DPTC
20 083C 18C9     JR AUT02
21 083E         ;
22 083F         ;
23 0840         ;
24 0841         ;
25 0842         ;
26 0843         ;
27 0844         ;
28 0845         ;
29 0846         ;
30 0847         ;
31 0848         ;
32 0849         ;
33 084A         ;
34 084B         ;
35 084C         ;
36 084D         ;
37 084E         ;
38 084F         ;
39 0850         ;
40 0851         ;
41 0852         ;
42 0853         ;
43 0854         ;
44 0855         ;
45 0856         ;
46 0857         ;
47 0858         ;
48 0859         ;
49 085A         ;
50 085B         ;
51 085C         ;
52 085D         ;
53 085E         ;
54 085F         ;
55 0860         ;
56 0861         ;
57 0862         ;
58 0863         ;
59 0864         ;
60 0865         ;

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** 180 ASSEMBLER SB-7201 <MZ-80A.MONITOR> PAGE 29 09/04/81
01 0883      1  IF NO KEY  ACC=00
02 0883      2GET: ENT
03 0883      PUSH BC
04 0883 C5   PUSH HL
05 0884 E5   LD B,9
06 0885 0609 LD HL,SMPW+1
07 0887 216511 CALL 7C1FFF
08 088A C0D80F POP HL
09 088D E1   POP BC
10 088E C1   CALL 7KEY
11 088F DCA08 SUB F0H
12 08C2 D6F0 RET 2
13 08C4 C8   ADD A,F0H
14 08C5 C6F0 JP 7DACN
15 08C7 C3CE0B      ORG 08CAH      7KEY 124
16 08CA      1  IKEY INPUT
17 08CA      2 IN B = KEY MODE(SHIFT,CTRL,GRAPH)
18 08CA      3 IN C=KEY DATA (COLUMN & ROW)
19 08CA      4 EXIT ACC=DISPLAY CODE=F0H
20 08CA      5 IF NO KEY  ACC=F0H
21 08CA      6KEY: PUSH BC
22 08CA C5   PUSH DE
23 08CA D5   PUSH HL
24 08CA E5   CALL 7SWEP
25 08CA 0000A LD A,B
26 08D1 07   RLCA C,7KY2
27 08D2 381E JR 7KY11
28 08D3 3EF0 LD A,F0H
29 08D4 3A6E11 CALL AUTCK
30 08D5 3F   LD A,A
31 08D6 5F   CALL AUTCK
32 08D7 CDF109 LD A,(KDATW)
33 08D8 87   OR A
34 08D9 280A JR 7KY11
35 08DA 3A6E11 CALL DLY12
36 08DB 87   OR A
37 08DC 280A JR 7KY11
38 08DD CDA70D CALL 7SWEP
39 08DE CDS00A LD A,B
40 08E6 78   RLCA
41 08E7 07   JR C,7KY2
42 08E8 3808 JR A,E
43 08EA 7B   CP F0H
44 08EB FEF0 JP NZ,7KY9
45 08ED 2050 JP RET3
46 08EF C39F06 RLCA
47 08F2 07   RLCA
48 08F3 07   RLCA
49 08F4 07   RLCA
50 08F5 DA706 JP C,7GRP
51 08F6 07   JP
52 08F9 DAC50B JP C,7BRK
53 08FC 2600 LD H,0
54 08FE 69   LD L,C
55 08FF 79   LD A,C
56 0900 FE38 CP 38H
57 0902 3026 JR NC,7KY6
58 0904 3A7011 LD A,(KANAF)
59 0907 87   OR A
60 0908 78   LD A,B

** 180 ASSEMBLER SB-7201 <MZ-80A.MONITOR> PAGE 30 09/04/81
01 0909 07   RLCA
02 090A 2023 JR NZ,7KY4
03 090C 47   LD B,A
04 090D 3A8F11 LD A,(SFTLK)
05 0910 87   OR A
06 0911 78   LD A,B
07 0912 2803 JR 2,5
08 0914 17   RLCA
09 0915 3F   CCF
10 0916 1F   RRA
11 0917 17   RLA
12 0918 17   RLA
13 0919 3007 JR NC,7KY3
14 091B 11DA0C LD DE,KTBLC
15 091E 19   LD HL,DE
16 091F 7E   LD A,(HL)
17 0920 18B4 JR 7KY1
18 0922 1F   RRA
19 0923 3005 JR NC,7KY6
20 0925 11320C LD DE,KTBLB
21 0928 18F4 JR 7KY5
22 092A 11EA0B LD DE,KTBL
23 092D 18EF JR 7KY5
24 092F 07   RLCA
25 0930 3808 JR C,7KY7
26 0932 07   RLCA
27 0933 38E6 JR 7KY5-3
28 0935 116A0C LD DE,KTBL0
29 0938 18E4 JR 7KY5
30 093A 11A20C LD DE,KTBL0S
31 093D 18DF JR 7KY5
32 093F CDF109 1  CALL AUTCK
33 0942 3C   INC A
34 0943 7B   LD A,E
35 0944 18A9 JR 7KY10
36 0946      1  ORG 0946H
37 0946      2 1PRT 109
38 0946      3 1
39 0946      4 1
40 0946      5 1
41 0946      6 1
42 0946      7 1
43 0946      8 1
44 0946      9 1
45 0946      10 1
46 0946      11 1
47 0946 79   7PRT: LD A,C
48 0947 CDB90B CALL 7ADCN
49 094A 4F   LD C,A
50 094B E6F0 AND F0H
51 094D FEF0 CP F0H
52 094F C8   RET Z
53 0950 FEC0 CP C0H
54 0952 79   LD A,C
55 0953 2017 JR NZ,7PRT3
56 0955 CDDC0D CALL 7DPCF
57 0958 FEC3 CP C3H
58 095A 2813 JR 2,7PRT4
59 095C FEC5 CP C5H
60 095E 2807 JR 2,7PRT2

01 0909 07   RLCA
02 090A 2023 JR NZ,7KY4
03 090C 47   LD B,A
04 090D 3A8F11 LD A,(SFTLK)
05 0910 87   OR A
06 0911 78   LD A,B
07 0912 2803 JR 2,5
08 0914 17   RLCA
09 0915 3F   CCF
10 0916 1F   RRA
11 0917 17   RLA
12 0918 17   RLA
13 0919 3007 JR NC,7KY3
14 091B 11DA0C LD DE,KTBLC
15 091E 19   LD HL,DE
16 091F 7E   LD A,(HL)
17 0920 18B4 JR 7KY1
18 0922 1F   RRA
19 0923 3005 JR NC,7KY6
20 0925 11320C LD DE,KTBLB
21 0928 18F4 JR 7KY5
22 092A 11EA0B LD DE,KTBL
23 092D 18EF JR 7KY5
24 092F 07   RLCA
25 0930 3808 JR C,7KY7
26 0932 07   RLCA
27 0933 38E6 JR 7KY5-3
28 0935 116A0C LD DE,KTBL0
29 0938 18E4 JR 7KY5
30 093A 11A20C LD DE,KTBL0S
31 093D 18DF JR 7KY5
32 093F CDF109 1  CALL AUTCK
33 0942 3C   INC A
34 0943 7B   LD A,E
35 0944 18A9 JR 7KY10
36 0946      1  ORG 0946H
37 0946      2 1PRT 109
38 0946      3 1
39 0946      4 1
40 0946      5 1
41 0946      6 1
42 0946      7 1
43 0946      8 1
44 0946      9 1
45 0946      10 1
46 0946      11 1
47 0946 79   7PRT: LD A,C
48 0947 CDB90B CALL 7ADCN
49 094A 4F   LD C,A
50 094B E6F0 AND F0H
51 094D FEF0 CP F0H
52 094F C8   RET Z
53 0950 FEC0 CP C0H
54 0952 79   LD A,C
55 0953 2017 JR NZ,7PRT3
56 0955 CDDC0D CALL 7DPCF
57 0958 FEC3 CP C3H
58 095A 2813 JR 2,7PRT4
59 095C FEC5 CP C5H
60 095E 2807 JR 2,7PRT2

01 0909 07   RLCA
02 090A 2023 JR NZ,7KY4
03 090C 47   LD B,A
04 090D 3A8F11 LD A,(SFTLK)
05 0910 87   OR A
06 0911 78   LD A,B
07 0912 2803 JR 2,5
08 0914 17   RLCA
09 0915 3F   CCF
10 0916 1F   RRA
11 0917 17   RLA
12 0918 17   RLA
13 0919 3007 JR NC,7KY3
14 091B 11DA0C LD DE,KTBLC
15 091E 19   LD HL,DE
16 091F 7E   LD A,(HL)
17 0920 18B4 JR 7KY1
18 0922 1F   RRA
19 0923 3005 JR NC,7KY6
20 0925 11320C LD DE,KTBLB
21 0928 18F4 JR 7KY5
22 092A 11EA0B LD DE,KTBL
23 092D 18EF JR 7KY5
24 092F 07   RLCA
25 0930 3808 JR C,7KY7
26 0932 07   RLCA
27 0933 38E6 JR 7KY5-3
28 0935 116A0C LD DE,KTBL0
29 0938 18E4 JR 7KY5
30 093A 11A20C LD DE,KTBL0S
31 093D 18DF JR 7KY5
32 093F CDF109 1  CALL AUTCK
33 0942 3C   INC A
34 0943 7B   LD A,E
35 0944 18A9 JR 7KY10
36 0946      1  ORG 0946H
37 0946      2 1PRT 109
38 0946      3 1
39 0946      4 1
40 0946      5 1
41 0946      6 1
42 0946      7 1
43 0946      8 1
44 0946      9 1
45 0946      10 1
46 0946      11 1
47 0946 79   7PRT: LD A,C
48 0947 CDB90B CALL 7ADCN
49 094A 4F   LD C,A
50 094B E6F0 AND F0H
51 094D FEF0 CP F0H
52 094F C8   RET Z
53 0950 FEC0 CP C0H
54 0952 79   LD A,C
55 0953 2017 JR NZ,7PRT3
56 0955 CDDC0D CALL 7DPCF
57 0958 FEC3 CP C3H
58 095A 2813 JR 2,7PRT4
59 095C FEC5 CP C5H
60 095E 2807 JR 2,7PRT2

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** Z80 ASSEMBLER SB-7201 (M1-80A-MONITOR)

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** Z80 ASSEMBLER SB-7201 (M1-80A-MONITOR)

```

01 0960 FECD CP CDH : CR
02 0962 2803 JR Z,PRNT2
03 0964 FEC6 CP C6H : CLR
04 0966 C0 RET N2
05 0967 AF PRNT2: XOR A
06 0968 329411 LD (DPRT),A
07 0969 C9 RET
08 096C CDB500 PRNT3: CALL 70SP
09 096F 3A9411 PRNT4: LD A,(DPRT)
10 0972 3C INC A
11 0973 FE50 CP *80
12 0975 3802 JR C,*4
13 0977 D650 SUB *80
14 0979 18ED JR PRNT2+1
15 097B :
16 097E ENT :NL: ENT
17 097B 3A9411 LD A,(DPRT)
18 097E B7 OR A
19 097F C8 RET Z
20 0980 :
21 0980 : NEW LINE
22 0980 :
23 0980 :
24 0980 :
25 0980 3ECD LD A,CDH : CR
26 0982 18D1 JR PRNT5
27 0984 :
28 0984 : PRINT TAB 1
29 0984 :
30 0984 :
31 0984 C0C000 ?PRNT: ENT
32 0984 3A9411 CALL PRNT5
33 0987 3A9411 LD A,(DPRT)
34 098A B7 OR A
35 098B C8 RET Z
36 098C D60A SUB *10
37 098E 38F4 JR C,-10
38 0990 20FA JR N2,-4
39 0992 C9 RET
40 0993 :
41 0993 : PRINT SPACE
42 0993 :
43 0993 :
44 0993 3E20 ?PRTS: ENT
45 0995 : LD A,20H
46 0995 : PRINT ROUTINE 1
47 0995 :
48 0995 :
49 0995 FE0D ?PRNT: ENT
50 0997 28E7 CP ODH
51 0999 C5 JR Z,7LTLN
52 099A 4F PUSH BC
53 099B 47 LD C,A
54 099C D4609 LD B,A
55 099F 78 LD 7FRT
56 09A0 C1 LD A,B
57 09A1 C9 POP BC
58 09A2 : RET
59 09A2 :
60 09A2 : DLY3 324MICRO SEC DELAY

```

```

01 09A2 :
02 09A2 : DLY2*3
03 09A2 :
04 09A2 ED44 DLY3: NEG
05 09A4 ED44 LD A,42
06 09A6 3E2A LD DLY2*2
07 09A8 C36207 JP
08 09AB :
09 09AB : ONPU 2
10 09AB :
11 09AB 91 ONP3: ADD A,C
12 09AC 10FD DJNZ -1
13 09AE C1 POP BC
14 09AF 4F LD C,A
15 09B0 AF XOR A
16 09B1 C9 RET
17 09B2 :
18 09B2 :
19 09B2 :
20 09B3 : ORG 09B3H
21 09B3 :
22 09B3 : KEY ROAD SEARCH 1
23 09B3 : & DISPLAY CODE CONV. 1
24 09B3 :
25 09B3 : EXIT A = DISPLAY CODE
26 09B3 : WITH CURSOR DISPLAY
27 09B3 :
28 09B3 C5 ?KEY: PUSH BC
29 09B4 D5 PUSH DE
30 09B5 E5 PUSH HL
31 09B6 CD6302 CALL 7SAVE
32 09B9 C0CA08 CALL 7KEY
33 09BC C0FF09 CALL 7FLAS
34 09BF 28F8 JR Z,KSL2
35 09C1 C0F505 CALL 7LOAD
36 09C4 C39F06 JP RET3
37 09C7 :
38 09C7 :
39 09C7 : PAGE TOP CALCULATION:
40 09C7 : (PAGE*P)=(PRIAS)*8
41 09C7 :
42 09C7 D5 PAGE: PUSH DE
43 09C8 E5 PUSH HL
44 09C9 217A11 LD HL,PRIAS
45 09CC AF XOR A
46 09CD ED6F RLD
47 09CF 57 LD D,A
48 09D0 5E LD E,(HL)
49 09D1 ED67 RRD
50 09D3 AF XOR A
51 09D4 CB1A RR D
52 09D6 CB1B RR E
53 09D8 2100D0 LD HL,SCRN
54 09DB 19 ADD HL,DE
55 09DC 227D11 LD (PAGE*P),HL
56 09DF E1 POP HL
57 09E0 D1 POP DE
58 09E1 C9 RET
59 09E2 :
60 09E2 :

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```

01 09E2      1 CLEAR 2
02 09E2      1
03 09E2 AF    #CLR8: XOR A
04 09E3 010008  #CLR8: LD BC,0800H
05 09E6 D5    CLEAR: PUSH DE
06 09E7 57    LD D,A
07 09E8 72    CLEAR: LD (HL),D
08 09E9 23    INC HL
09 09EA 0B    DEC BC
10 09EB 78    LD A,B
11 09EC B1    OR C
12 09ED 20F9  JR NZ,CLEAR1
13 09EF D1    POP DE
14 09F0 C9    RET
15 09F1
16 09F1
17 09F1      1 AUTO REPEAT CHECK
18 09F1
19 09F1 216E11  AUTCK: LD HL,XDATW
20 09F4 7E    LD A,(HL)
21 09F5 23    INC HL
22 09F6 56    LD D,(HL)
23 09F7 77    LD (HL),A
24 09F8 92    SUB D
25 09F9 D0    RET NC
26 09FA 34    INC HL
27 09FB C9    RET
28 09FC
29 09FC      1 00 MESSAGE
30 09FC
31 09FC 3030    00MS0: DEFN 3030H
32 09FE 0D    DEFB 0DH
33 09FF
34 09FF
35 09FF
36 09FF
37 09FF
38 09FF
39 09FF
40 09FF
41 09FF
42 09FF F5    ?FLAS: PUSH AF
43 0A00 E5    PUSH HL
44 0A01 3A02E0  LD A,(KEYPC)
45 0A04 07    RLCA
46 0A05 07    RLCA
47 0A06 380A    JR C,FLAS1
48 0A08 3A9211  LD A,(FLSDT)
49 0A0B CDB10F  LD A,(FLSDT)
50 0A0E 77    LD (HL),A
51 0A0F E1    POP HL
52 0A10 F1    POP AF
53 0A11 C9    RET
54 0A12 3A8E11  FLAS1: LD A,(FLASH)
55 0A15 18F4    JR FLAS2
56 0A17
57 0A17      1 REVERSE CRT
58 0A17
59 0A17 219011  REV: LD HL,REVFLG
60 0A1A 7E    LD A,(HL)

01 0A1B B7    OR A
02 0A1C 2F    CPL
03 0A1D 77    LD (HL),A
04 0A1E 2805  JR Z,REV1
05 0A20 3A14E0  LD A,(E014H)
06 0A23 1803  JR *5
07 0A25 3A15E0  REV1: LD A,(E015H)
08 0A28 C3E0E  JP 7RSTR
09 0A2B
10 0A2B      1 CRT MANAGEMENT
11 0A2B      1 EXIT HL:DSPLY
12 0A2B      1 DE:MANG ADP. (ON DSPXY)
13 0A2B      1 B:MANG BIT POSITION
14 0A2B      1 A:MANG DATA
15 0A2B      1 CY:MANG=1
16 0A2B
17 0A2B      1 MANG: LD HL,MANG
18 0A2B 217311  LD A,(SPAGE)
19 0A2E 3A9111  OR A
20 0A31 B7    JP NZ,MANG2
21 0A32 C2A603  LD A,(M0PNT)
22 0A35 3A7C11  SUB 08H
23 0A38 0A08  INC HL
24 0A3A 23    INC NC,-3
25 0A3B 30FB  ADD A,08H
26 0A3D C608  LD C,(HL)
27 0A3F 4E    DEC HL
28 0A40 2B    LD B,A
29 0A41 47    INC B
30 0A42 04    PUSH BC
31 0A43 C5    LD A,(HL)
32 0A44 7E    RRA
33 0A45 CB19  DJNZ -3
34 0A47 1F    POP BC
35 0A48 10FB  EX DE,HL
36 0A4A C1    HL,(DSPXY)
37 0A4B EB    RET
38 0A4C 2A7111  MANG1: LD HL,(DSPXY)
39 0A4F C9    RET
40 0A50
41 0A50
42 0A50
43 0A50
44 0A50
45 0A50
46 0A50
47 0A50
48 0A50
49 0A50
50 0A50
51 0A50
52 0A50
53 0A50
54 0A50
55 0A50 D5    ?SWEP: PUSH DE
56 0A51 E5    PUSH HL
57 0A52 AF    XOR A
58 0A53 32E11  LD (KDATW),A
59 0A56 06FA  LD B,FAH
60 0A58 57    LD D,A

01 0A1B B7    OR A
02 0A1C 2F    CPL
03 0A1D 77    LD (HL),A
04 0A1E 2805  JR Z,REV1
05 0A20 3A14E0  LD A,(E014H)
06 0A23 1803  JR *5
07 0A25 3A15E0  REV1: LD A,(E015H)
08 0A28 C3E0E  JP 7RSTR
09 0A2B
10 0A2B      1 CRT MANAGEMENT
11 0A2B      1 EXIT HL:DSPLY
12 0A2B      1 DE:MANG ADP. (ON DSPXY)
13 0A2B      1 B:MANG BIT POSITION
14 0A2B      1 A:MANG DATA
15 0A2B      1 CY:MANG=1
16 0A2B
17 0A2B      1 MANG: LD HL,MANG
18 0A2B 217311  LD A,(SPAGE)
19 0A2E 3A9111  OR A
20 0A31 B7    JP NZ,MANG2
21 0A32 C2A603  LD A,(M0PNT)
22 0A35 3A7C11  SUB 08H
23 0A38 0A08  INC HL
24 0A3A 23    INC NC,-3
25 0A3B 30FB  ADD A,08H
26 0A3D C608  LD C,(HL)
27 0A3F 4E    DEC HL
28 0A40 2B    LD B,A
29 0A41 47    INC B
30 0A42 04    PUSH BC
31 0A43 C5    LD A,(HL)
32 0A44 7E    RRA
33 0A45 CB19  DJNZ -3
34 0A47 1F    POP BC
35 0A48 10FB  EX DE,HL
36 0A4A C1    HL,(DSPXY)
37 0A4B EB    RET
38 0A4C 2A7111  MANG1: LD HL,(DSPXY)
39 0A4F C9    RET
40 0A50
41 0A50
42 0A50
43 0A50
44 0A50
45 0A50
46 0A50
47 0A50
48 0A50
49 0A50
50 0A50
51 0A50
52 0A50
53 0A50
54 0A50
55 0A50 D5    ?SWEP: PUSH DE
56 0A51 E5    PUSH HL
57 0A52 AF    XOR A
58 0A53 32E11  LD (KDATW),A
59 0A56 06FA  LD B,FAH
60 0A58 57    LD D,A

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```

01 0A59 CD110D      CALL 7BRK
02 0A5C 2004        JR NZ,SWEP6
03 0A5E 1A88        LD D,8BH
04 0A60 1828        JR SWEP9
05 0A62 216411      SWEP6: LD HL,SWPH
06 0A65 E5          PUSH HL
07 0A66 3026        JR NC,SWEP11
08 0A68 57          LD D,A
09 0A69 E660        AND 60H
10 0A6B 2021        JR NZ,SWEP11
11 0A6D 7A          LD A,D
12 0A6E AE          XOR (HL)
13 0A6F CB67        BIT 6,A
14 0A71 72          LD (HL),D
15 0A72 2802        JR Z,SWEP0
16 0A74 CBFA        SWEP0: SET 7,D
17 0A76 05          SWEP0: POP HL
18 0A77 E1          INC HL
19 0A78 23          LD A,B
20 0A79 78          LD (KEYPA),A
21 0A7A 3200E0       CP F0H
22 0A7D FEF0        JR NZ,SWEP3
23 0A7F 2011        LD A,(HL)
24 0A81 7E          CP 03H
25 0A82 FE03        JR C,SWEP9
26 0A84 3804        LD (HL),0
27 0A86 3600        RES 7,D
28 0A88 CBBA        LD B,D
29 0A8A 42          POP HL
30 0A8B E1          POP DE
31 0A8C D1          RET
32 0A8D C9          ;
33 0A8E             SWEP11: LD (HL),0
34 0A8E 3600        JR SWEP0
35 0A90 18E4        SWEP3: LD A,(KEYPB)
36 0A92 3A01E0       LD E,A
37 0A95 5F          CPL
38 0A96 2F          AND (HL)
39 0A97 A6          LD (HL),E
40 0A98 73          PUSH HL
41 0A99 E5          LD HL,KDATH
42 0A9A 216E11      PUSH BC
43 0A9D C5          LD B,8
44 0A9E 0608        SWEP8: RLC E
45 0AA0 CB03        INC (HL)
46 0AA2 3801        SWEP7: DJNZ SWEP8
47 0AA4 34          OR A
48 0AA5 10F9        SWEP7: POP BC
49 0AA7 C1          OR A
50 0AA8 B7          JR Z,SWEP0
51 0AA9 28CB        LD E,A
52 0AAB 5F          SWEP2: LD H,8
53 0AAC 2408        LD A,B
54 0AAE 78          DEC A
55 0AAF 3D          AND 0FH
56 0AB0 E60F        JP SWEP4
57 0AB2 C3E07       ; ASCII TO DISPLAY CODE TABL
58 0AB5             ;
59 0AB5             ;
60 0AB5             ;

```


ORG	OBCEH	1?DACN	472
01 0C02	DEFB 28H	1	18 - 1F
02 0C03	DEFB 27H	1	18 - 1F
03 0C04	DEFB 09H	1	18 - 1F
04 0C05	DEFB 15H	1	18 - 1F
05 0C06	DEFB 0AH	1	18 - 1F
06 0C07	DEFB 08H	1	18 - 1F
07 0C08	DEFB 0EH	1	18 - 1F
08 0C09	DEFB 00H	1	18 - 1F
09 0C0A	DEFB 20H	1	18 - 1F
10 0C0B	DEFB 29H	1	18 - 1F
11 0C0C	DEFB 10H	1	18 - 1F
12 0C0D	DEFB 0FH	1	18 - 1F
13 0C0E	DEFB 0CH	1	18 - 1F
14 0C0F	DEFB 0BH	1	18 - 1F
15 0C10	DEFB 2FH	1	18 - 1F
16 0C11	DEFB 0DH	1	18 - 1F
17 0C12	DEFB BEH	1	18 - 1F
18 0C13	DEFB 2AH	1	18 - 1F
19 0C14	DEFB 52H	1	18 - 1F
20 0C15	DEFB 55H	1	18 - 1F
21 0C16	DEFB 4FH	1	18 - 1F
22 0C17	DEFB 2CH	1	18 - 1F
23 0C18	DEFB 2DH	1	18 - 1F
24 0C19	DEFB 2EH	1	18 - 1F
25 0C1A	DEFB C5H	1	18 - 1F
26 0C1B	DEFB 59H	1	18 - 1F
27 0C1C	DEFB C3H	1	18 - 1F
28 0C1D	DEFB C2H	1	18 - 1F
29 0C1E	DEFB CDH	1	18 - 1F
30 0C1F	DEFB 54H	1	18 - 1F
31 0C20	DEFB 00H	1	18 - 1F
32 0C21	DEFB 49H	1	18 - 1F
33 0C22	DEFB 28H	1	18 - 1F
34 0C23	DEFB 27H	1	18 - 1F
35 0C24	DEFB 25H	1	18 - 1F
36 0C25	DEFB 24H	1	18 - 1F
37 0C26	DEFB 22H	1	18 - 1F
38 0C27	DEFB 21H	1	18 - 1F
39 0C28	DEFB E7H	1	18 - 1F
40 0C29	DEFB 20H	1	18 - 1F
41 0C2A	DEFB 6AH	1	18 - 1F
42 0C2B	DEFB 29H	1	18 - 1F
43 0C2C	DEFB 2AH	1	18 - 1F
44 0C2D	DEFB 26H	1	18 - 1F
45 0C2E	DEFB 00H	1	18 - 1F
46 0C2F	DEFB 23H	1	18 - 1F
47 0C30	DEFB 00H	1	18 - 1F
48 0C31	DEFB 2EH	1	18 - 1F
49 0C32	DEFB 2EH	1	18 - 1F
50 0C33	DEFB 62H	1	18 - 1F
51 0C34	DEFB 61H	1	18 - 1F
52 0C35	DEFB 97H	1	18 - 1F

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01 0C35 91 DEF 91H 1 q
02 0C36 81 DEF 81H 1 a
03 0C37 C8 DEF C8H 1 INSERT
04 0C38 00 DEF 00H 1 NULL
05 0C39 9A DEF 9AH 1 z
1S1
06 0C3A 64 DEF 64H 1 $
07 0C3B 63 DEF 63H 1 #
08 0C3C 92 DEF 92H 1 r
09 0C3D 85 DEF 85H 1 e
10 0C3E 84 DEF 84H 1 d
11 0C3F 93 DEF 93H 1 s
12 0C40 98 DEF 98H 1 x
13 0C41 83 DEF 83H 1 c
1S2
14 0C42 66 DEF 66H 1 k
15 0C43 65 DEF 65H 1 x
16 0C44 99 DEF 99H 1 y
17 0C45 94 DEF 94H 1 t
18 0C46 87 DEF 87H 1 g
19 0C47 86 DEF 86H 1 f
20 0C48 96 DEF 96H 1 v
21 0C49 82 DEF 82H 1 b
1S3
22 0C4A 68 DEF 68H 1 (
23 0C4B 67 DEF 67H 1 )
24 0C4C 89 DEF 89H 1 i
25 0C4D 95 DEF 95H 1 u
26 0C4E 8A DEF 8AH 1 j
27 0C4F 88 DEF 88H 1 n
28 0C50 8E DEF 8EH 1 h
29 0C51 00 DEF 00H 1 SPACE
1S4
30 0C52 BF DEF BFH 1 -
31 0C53 69 DEF 69H 1 )
32 0C54 90 DEF 90H 1 p
33 0C55 8F DEF 8FH 1 o
34 0C56 8C DEF 8CH 1 j
35 0C57 8B DEF 8BH 1 k
36 0C58 51 DEF 51H 1 <
37 0C59 8D DEF 8DH 1 m
1S5
38 0C5A A5 DEF A5H 1 ~
39 0C5B 28 DEF 28H 1 =
40 0C5C BC DEF BCH 1 (
41 0C5D A4 DEF A4H 1 .
42 0C5E 6B DEF 6BH 1 *
43 0C5F 6A DEF 6AH 1 +
44 0C60 45 DEF 45H 1 >
45 0C61 57 DEF 57H 1 >
1S6
46 0C62 C6 DEF C6H 1 CLR
47 0C63 80 DEF 80H 1 i
48 0C64 C4 DEF C4H 1 CURSOR *LEFT
49 0C65 C1 DEF C1H 1 CURSOR *DOWN
50 0C66 CD DEF CDH 1 CR
51 0C67 40 DEF 40H 1 )
52 0C68 00 DEF 00H 1 NULL
53 0C69 50 DEF 50H 1 t
KTBLG:
04 0C6A

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** 180 ASSEMBLER SB-7201 <M2-80A.MONITOR> PAGE 44
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01 0C6A 3E DEF 3EH 1 #2
02 0C6B 37 DEF 37H 1 #1
03 0C6C 38 DEF 38H 1 #H
04 0C6D 3C DEF 3CH 1 #0
05 0C6E 53 DEF 53H 1 #A
06 0C6F C7 DEF C7H 1 DELETE
07 0C70 00 DEF 00H 1 NULL
08 0C71 76 DEF 76H 1 #Z
1S1
09 0C72 78 DEF 78H 1 #4
10 0C73 7F DEF 7FH 1 #3
11 0C74 30 DEF 30H 1 #R
12 0C75 34 DEF 34H 1 #E
13 0C76 47 DEF 47H 1 #D
14 0C77 44 DEF 44H 1 #S
15 0C78 6D DEF 6DH 1 #X
16 0C79 DE DEF DEH 1 #C
1S2
17 0C7A SE DEF SEH 1 #6
18 0C7B 3A DEF 3AH 1 #5
19 0C7C 75 DEF 75H 1 #Y
20 0C7D 71 DEF 71H 1 #T
21 0C7E 4B DEF 4BH 1 #0
22 0C7F 4A DEF 4AH 1 #F
23 0C80 DA DEF DAH 1 #V
24 0C81 6F DEF 6FH 1 #B
1S3
25 0C82 BD DEF BDH 1 #8
26 0C83 1F DEF 1FH 1 #7
27 0C84 7D DEF 7DH 1 #1
28 0C85 79 DEF 79H 1 #U
29 0C86 5C DEF 5CH 1 #J
30 0C87 72 DEF 72H 1 #H
31 0C88 32 DEF 32H 1 #N
32 0C89 00 DEF 00H 1 SPACE
1S4
33 0C8A 9C DEF 9CH 1 #0
34 0C8B A1 DEF A1H 1 #9
35 0C8C D6 DEF D6H 1 #P
36 0C8D B0 DEF B0H 1 #0
37 0C8E 84 DEF 84H 1 #L
38 0C8F 5B DEF 5BH 1 #K
39 0C90 60 DEF 60H 1 -PAI.#
40 0C91 1C DEF 1CH 1 #M
1S5
41 0C92 9E DEF 9EH 1 #
42 0C93 D2 DEF D2H 1 #
43 0C94 D8 DEF D8H 1 #L
44 0C95 B2 DEF B2H 1 #0
45 0C96 B6 DEF B6H 1 #1
46 0C97 42 DEF 42H 1 #1
47 0C98 DB DEF DBH 1 #/
48 0C99 B8 DEF B8H 1 #.
1S6
49 0C9A C5 DEF C5H 1 HOME
50 0C9B D4 DEF D4H 1 #V
51 0C9C C3 DEF C3H 1 CURSOR *RIGHT
52 0C9D C2 DEF C2H 1 CURSOR #UP
53 0C9E CD DEF CDH 1 CR

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** 780 ASSEMBLER SB-7201 <M2-80A, MONITOR>

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** 780 ASSEMBLER SB-7201 <M2-80A, MONITOR>

```

01 0E44 77 LD (HL),A
02 0E45 23 INC HL
03 0E46 10FA DJNZ SCROL2
04 0E48 3A7A11 LD A,(PB1AS)
05 0E4B 6F LD L,A
06 0E4C 26E2 LD L,A
07 0E4E 7E LD A,(HL)
08 0E4F 217911 LD HL,MANGE
09 0E52 B7 OR A
10 0E53 0607 LD B,7
11 0E55 CB1E RR (HL)
12 0E57 2B DEC HL
13 0E58 10FB DJNZ -3
14 0E5A C3E0E JP 7RSTR
15 0E5D
16 0E5D
17 0E5D 2A7111 CURSD: LD HL,(DSPXY)
18 0E60 7C CP +24
19 0E61 FE18 JP 7CURS4
20 0E63 282E INC H
21 0E65 24 INC H
22 0E66 C08302 CURS1: CALL MGP.1
23 0E69 227111 CURS3: LD HL,(DSPXY),HL
24 0E6C 1877 JP 7RSTR
25 0E6E CURSU: LD HL,(DSPXY)
26 0E71 7C LD A,H
27 0E72 B7 OR A
28 0E73 2835 JP 7CURS5
29 0E75 25 DEC H
30 0E76 CD9D02 CURSU1: CALL MGP.D
31 0E79 18EE CURS3
32 0E7B CURSR: LD HL,(DSPXY)
33 0E7E 7D LD A,L
34 0E7F FE27 CP +39
35 0E81 3003 JP NC,CURS2
36 0E83 2C INC L
37 0E84 18E3 JR CURS3
38 0E86 2E00 LD L,0
39 0E88 24 INC H
40 0E89 7C LD A,H
41 0E8A FE19 CP +25
42 0E8C 38D8 JP C,CURS1
43 0E8E 2A18 LD H,24
44 0E90 227111 LD HL,(DSPXY),HL
45 0E93 1842 CURS4: JR CURS6
46 0E95
47 0E95 CURSL: LD HL,(DSPXY)
48 0E98 7D LD A,L
49 0E99 B7 OR A
50 0E9A 2803 JR Z,*5
51 0E9C 2D DEC L
52 0E9D 18CA JR CURS3
53 0E9F 2E27 LD L,*39
54 0EA1 25 DEC H
55 0EA2 F2760E JP P,CURSUI
56 0EA5 2600 LD H,0
57 0EA7 227111 LD HL,(DSPXY),HL
58 0EAA 3A9111 LD A,(SPAGE)
59 0EAB 3A9111
60 0EAC 3A9111

```

```

01 0EAD B7 OR A
02 0EAE 2035 JR NZ,7RSTR
03 0EB0 C3590F JP ROLD
04 0EB3 CLRS: LD HL,MANG
05 0EB3 217311 LD B,27
06 0EB6 061B CALL 7CLER
07 0EB8 CDD80F LD HL,D000H
08 0EBB 2100D0 PUSH HL
09 0EBE E5 CALL #CLR08
10 0EBF CDE209 POP HL
11 0EC2 E1 LD A,(SPAGE)
12 0EC3 3A9111 LD A,(SPAGE)
13 0EC6 B7 OR A
14 0EC7 2008 JR NZ,CLRS1
15 0EC9 227D11 LD (PAGEF1),HL
16 0ECC 3E7D LD A,7DH
17 0ECE 327F11 LD (ROLEND),A
18 0ED1 3A00E2 CLRS1: LD A,(E200H)
19 0ED4 C30904 HOM00: JP HOM0
20 0ED7
21 0ED7
22 0ED7
23 0ED7 3A9111 CURS6: LD A,(SPAGE)
24 0EDA B7 OR A
25 0EDB C23D01 JP NZ,SCROL
26 0EDE C39F0F JP ROLD
27 0EE1
28 0EE1
29 0EE1
30 0EE1 ORG 0EE1H
31 0EE1 ALPHA: XOR A
32 0EE1 AF ALPH1: LD (KANAF),A
33 0EE2 327011
34 0EE5
35 0EE5
36 0EE5
37 0EE5
38 0EE5 ENT
39 0EE5 E1 POP DE
40 0EE6 D1 POP BC
41 0EE7 C1 POP AF
42 0EE8 F1 RET
43 0EE9 C9
44 0EEA
45 0EEA
46 0EEA
47 0EEA P
48 0EEA P
49 0EEA
50 0EEA
51 0EEA
52 0EEA
53 0EEA
54 0EEA 3E01 KANA: LD A,*1
55 0EEF 18F0 JR ALPH1
56 0EF2
57 0EF2 2A7111 DEL: LD HL,(DSPXY)
58 0EF5 7C LD A,H
59 0EF6 B5 OR L
60 0EF6 B5

```

KANA STATUS PORT

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HOME ?

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** I80 ASSEMBLER SB-7201 (M2-80A.MONITOR)

 01 OFFD C39F06
 02 1000
 GAPCK3: JP RET3
 SKP H

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** I80 ASSEMBLER SB-7201 (M2-80A.MONITOR)

```

01 0FB4      ?PNT1: PUSH AF
02 0FB4 F5   PUSH BC
03 0FB5 C5   PUSH DE
04 0FB6 D5   PUSH HL
05 0FB7 E5   POP BC
06 0FB8 C1   LD DE,0028H
07 0FB9 112800 LD HL,SCRN-4C
08 0FBC 21D8CF LD A,(SPAGE)
09 0FBF 3A9111 OR A
10 0FC2 B7   JR NZ,?PNT2
11 0FC3 2005   LD HL,(PAGE1F)
12 0FC5 2A7D11 LD HL,DE
13 0FC8 ED52   SBC HL,DE
14 0FCA 19   ?PNT2: ADD HL,DE
15 0FCB 05   DEC B
16 0FCC F2CA0F JP P-2
17 0FCE 0600   LD B,*0
18 0FD1 09   ADD HL,BC
19 0FD2 CB9C   RES 3,H
20 0FD4 D1   POP DE
21 0FD5 C1   POP BC
22 0FD6 F1   POP AF
23 0FD7 C9   RET
24 0FD8      ;
25 0FD8      ;
26 0FD8      ;
27 0FD8      ;
28 0FD8      ;
29 0FD8      ;
30 0FD8      ;
31 0FD8      ;
32 0FD8      ;
33 0FD8      ;
34 0FD8 AF   ?CLER: ENT XOR A
35 0FD9 1802 JR A,*4
36 0FDB      ?CLRF: ENT LD A,FFH
37 0FDB 3EFF LD A,FFH
38 0FDD      ?DINT: ENT LD (HL),A
39 0FDD 77   LD HL
40 0FDE 23   INC HL
41 0FDF 10FC DJNZ -2
42 0FE1 C9   RET
43 0FE2      ;
44 0FE2      ;
45 0FE2      ;
46 0FE2      ;
47 0FE2 C5   GAPCK: PUSH BC
48 0FE3 D5   PUSH DE
49 0FE4 E5   PUSH HL
50 0FE5 0101E0 LD BC,KEYPB
51 0FE8 1102E0 LD DE,CSTR
52 0FE8 2664   LD H,100
53 0FE8 CD0106 GAPCK1: LD H,100
54 0FF0 3808   GAPCK2: CALL EDGE
55 0FF2 CDA209 JR C,GAPCK3
56 0FF5 1A   CALL DLY3
57 0FF6 E620 LD A,(DE)
58 0FF8 20F1 AND 20H
59 0FFA 25   JR NZ,GAPCK1
60 0FFB 20F0 DEC H
      JR NZ,GAPCK2
      ; CALL DLY2*3

```


Hardware Configuration of the MZ-80A

Chapter 3

3.1 The MZ-80A system configuration

Figure 3.1 shows the standard system configuration of the MZ-80A personal computer.

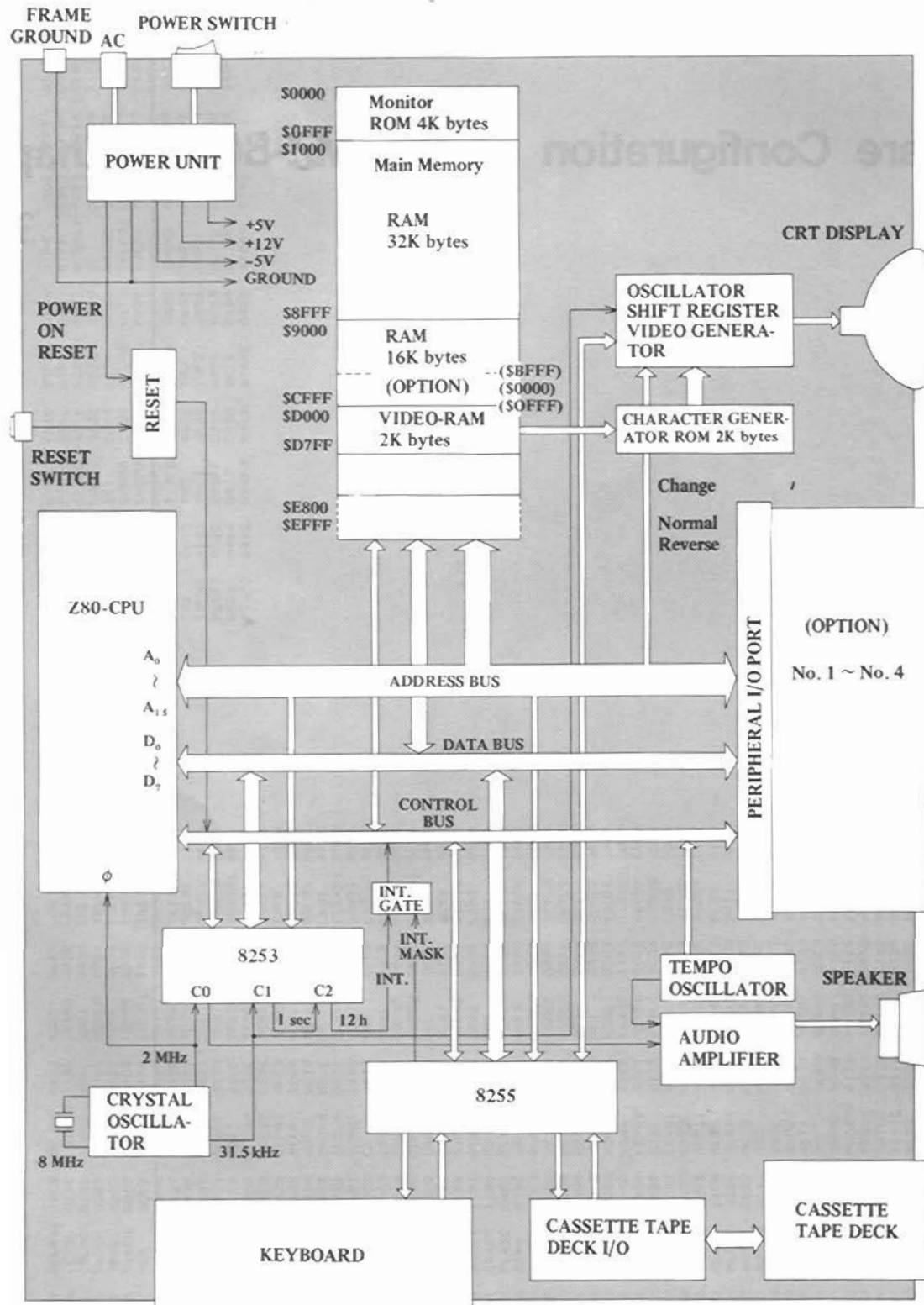


Figure 3.1 MZ-80A System Diagram

As is shown in the figure, a Z80 (Sharp LH0080) is used as the CPU, and is operated with a clock of 2 MHz. The CPU is reset when the power is turned on or when the reset switch is manually operated. The memory configuration corresponding to address buses \$0000-\$FFFF is as follows.

\$0000-\$0FFF is used for the monitor program (ROM); the large 48 K byte space from \$1000-\$CFFF is used as main memory (memory from \$9000-\$CFFF is optional); addresses from \$D000 on are used for video RAM, floppy control, and memory mapped I/O.

The keyboard and cassette tape deck are controlled by means of programmable peripheral interface 8255. Further, a rectangular audio wave generated by the output port of counter 1 of programmable interval timer 8253 is input to the sound generator, which outputs sound to the speaker. The two counters other than this IC serve as internal clocks for the MZ-80A.

Table 3.1 shows the configuration of MZ-80A memory mapped I/O.

Table 3.1 Assignment of memory mapped I/O.

Address	Memory Read	Memory Write	Device
\$E000		D ₇ : Resets cursor timer D ₃ ~ D ₀ : Key strobe	8255
\$E001	D ₇ ~ D ₀ : Key data		
\$E002	D ₇ : V-Blank D ₆ : Status of cursor timer D ₅ : Read data (cassette) D ₄ : READ/WRITE status (cassette)	D ₃ : Motor ON/OFF (cassette) D ₂ : Masking of timer interrupt D ₁ : Write data (cassette) D ₀ : V-Gate	
\$E003		Mode control	
\$E004		Setting of counter # 0	
\$E005	Reading of counter # 1	Setting of counter # 1	8253
\$E006	Reading of counter # 2	Setting of counter # 2	
\$E007		Mode control	
\$E008	D ₇ : Status of tempo timer D ₀ : H-Blank	D ₀ : Sound ON/OFF	
\$E00C	Memory swap		
\$E010	Resets memory swap		
\$E014	Normal (CRT display)		
\$E015	Reverse (CRT display)		
\$E200	Roll up/roll down		
~			
\$E2FF			

3.1.1 Memory configurations

The memory map for the MZ-80A is shown in Figure 3.2. The screened parts of the figure indicate user area, and the 32 K bytes of main memory RAM are the standard package. The remaining 16 K bytes of main memory RAM area optional, and can be installed in the RAM socket provided on the CPU board.

The 4 K bytes of main memory area which are indicated by the dark screening can be used for swapping the address spaces used by the MONITOR ROM. The left side of the figure shows the memory map under normal conditions, while the right side shows the memory map when the MONITOR ROM has been swapped. As is shown in Table 3.1, memory swaps are performed under control of memory mapped I/O by executing memory read instructions such as the following.

To place the memory in the state shown on the right LD A, (E00CH)

To place the memory in the state shown on the left LD A, (E010H)

The memory configuration shown on the right is especially effective when the system programs used start at address \$0000 and when the system programs utilized make active use of interrupt processing.

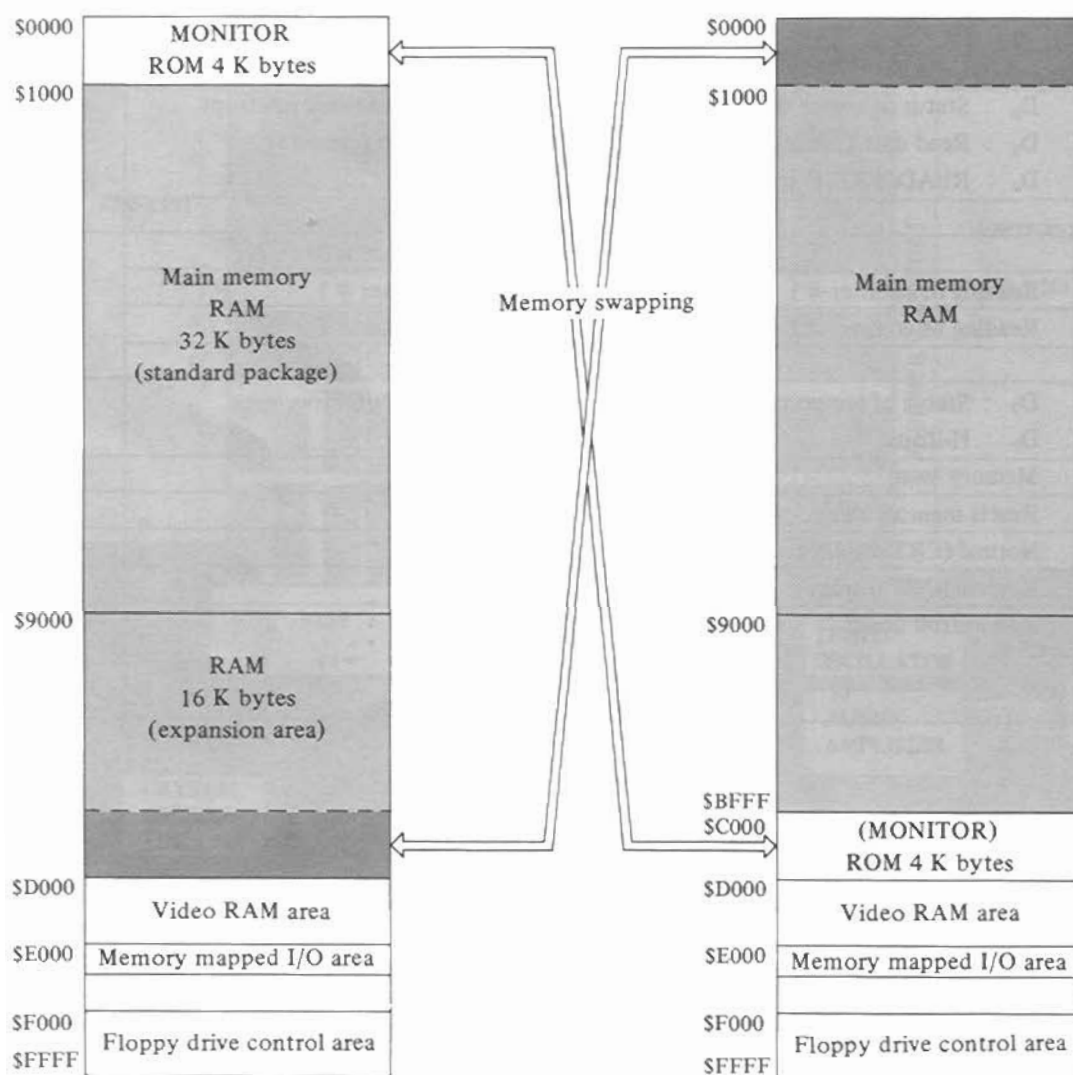


Figure 3.2 Memory maps for normal state and memory swap state.

When the memory is swapped, the 4 K ROM occupies the address space from \$C000-\$CFFF; however, the monitor program is ineffective in this condition. If necessary, it is possible to remove the monitor ROM from the socket on the CPU board and replace it with another user ROM which has been programmed to allow operation in the space from \$C000-\$CFFF. In such cases, use ROM 2732, which is the same as the monitor ROM. Also, if it is necessary to alter part of the monitor program for use, it can be modified by block-transferring it from \$C000-\$CFFF to \$0000-\$0FFF and making the changes in RAM.

The 2 K byte area from \$D000-\$D7FF is assigned to video RAM. This area is the standard package which is used for the MZ-80A main unit CRT display.

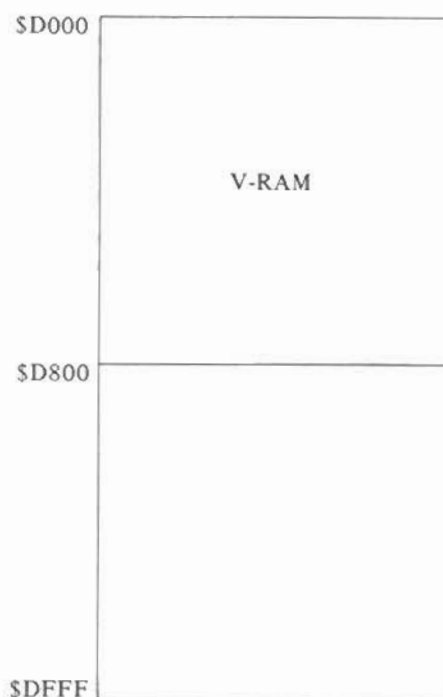


Figure 3.3 MZ-80A video RAM area.

Although up to 1,000 characters (40 characters \times 25 lines) can be displayed on the MZ-80A main unit CRT screen, twice this amount of memory area is provided in video RAM. This makes it possible to roll the screen displayed up or down.

Upon system reset, data is written into video RAM starting at address \$D000, and when more than 50 lines of data are written — that is, when data has been written into the area from \$D000 through \$D7CF — \$D028 through \$D7F7 become the actual video RAM area. When more data is written and one line scrolled, the area from \$D050-\$D7FF becomes the video RAM area, followed by the area from \$D000-\$D020. Thus, the video RAM is used in an anchored configuration. Figure 3.4 shows the relationship between video RAM and the display for the 2 K byte video RAM area when its first K byte, its middle K byte, or its last K byte is displayed on the CRT screen. However, in this example, the actual video RAM area capable of displaying data on the CRT screen is the 2000 bytes starting at \$D1E0.

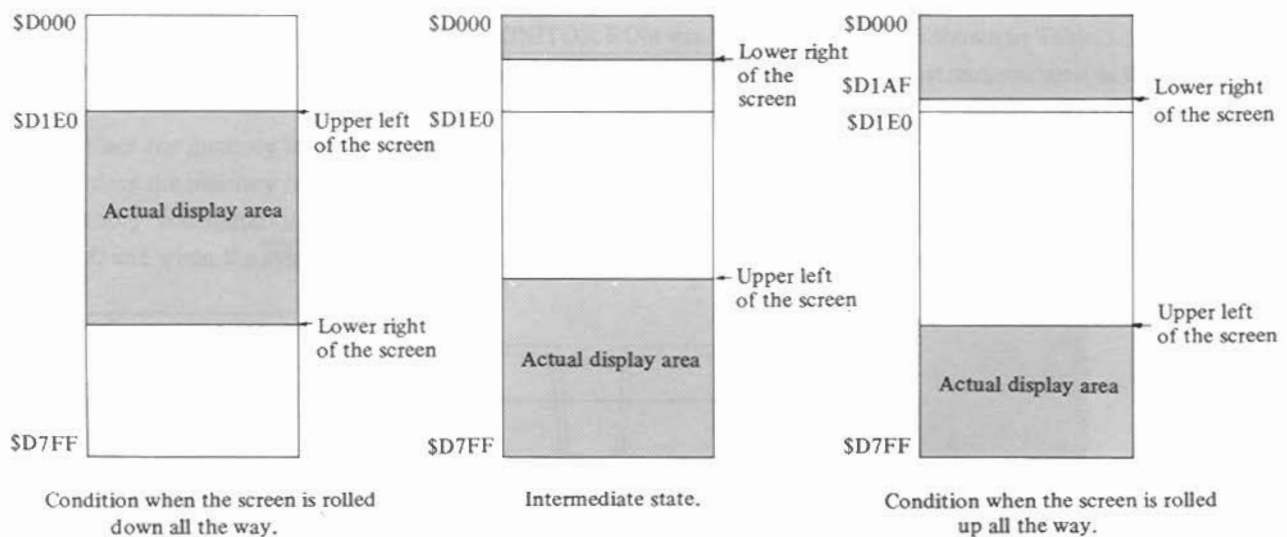


Figure 3.4 Relationship between video RAM and the area actually displayed.

Memory mapped I/O address \$E200-\$E2FF are used for rolling the display up and down. When a memory read instruction is executed against address \$E200 (such as LD A, (\$E200H)), the CRT display is rolled all the way down. When such an instruction is executed against address \$E2FF, the CRT display is rolled all the way up. The lower bytes of these addresses from 00H-FFH can be freely used to roll the screen up or down in 8-character units.

3.1.2 Key scanning system

The relationship between strobe signals and bit data during keyboard scanning is shown in Figure 3.6.

Strobe signals are delivered to four terminals (PA_3 , PA_2 , PA_1 , PA_0) of the 8255, fed into BCD-to-decimal decoder/driver 74145, then delivered to 10 keyboard strobe input terminals. Keys are discriminated by strobe signals and key data.

For instance when the strobe is '2H' and the key data is 'FBH', it indicates that the **[S]** key is being pressed.

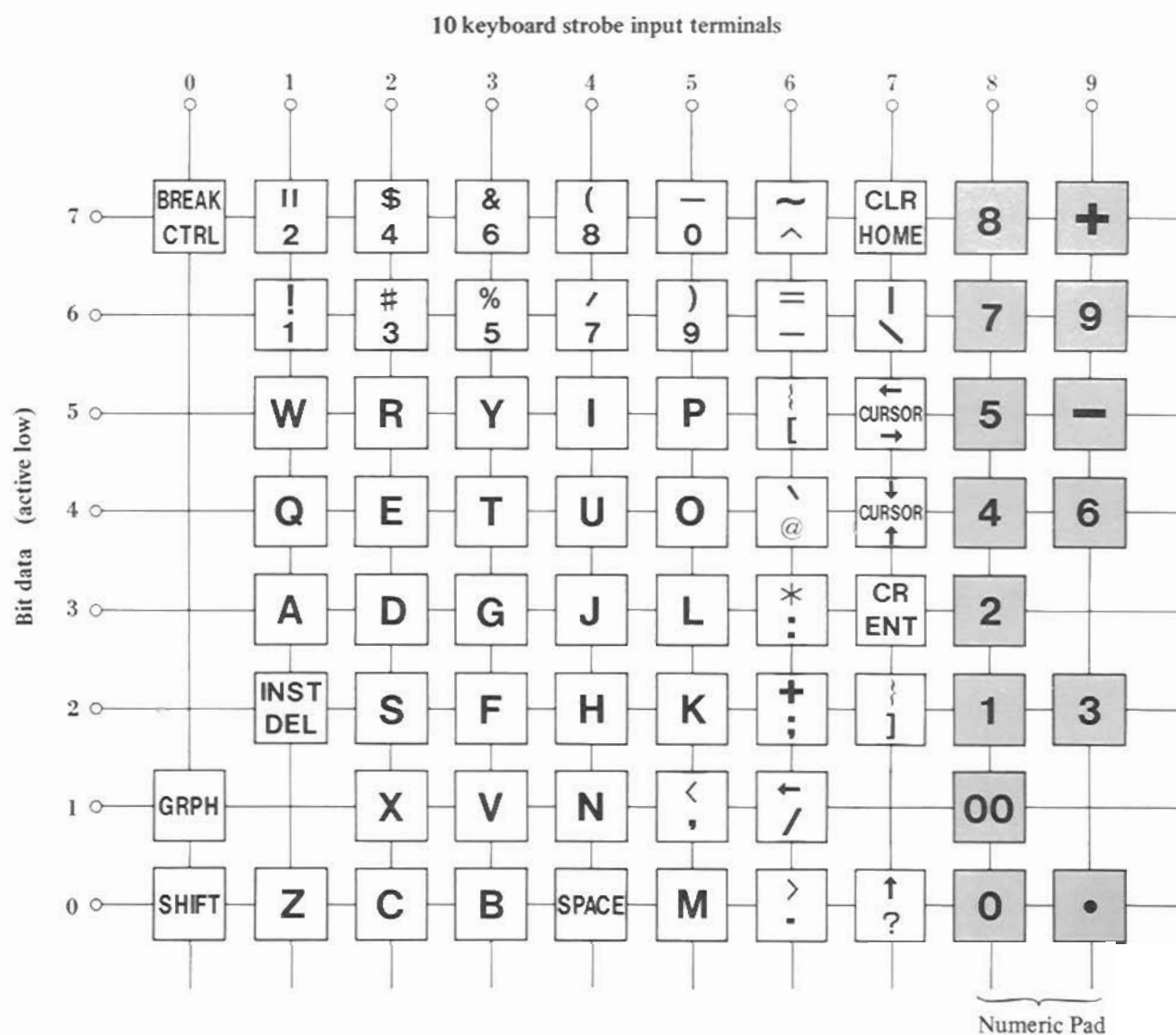


Figure 3.6 Key scanning strobe signal and bit data.

3.2 The MZ-80A circuit diagram

This section includes MZ-80A CPU board circuit diagrams for reference. These diagrams are arranged as follows:

- (1) CPU board, block 1 : CPU signal system
- (2) CPU board, block 2
- (3) CPU board, block 3 : RAM signal system
- (4) CPU board, block 4 : 8255 and 8253 signal system
- (5) CPU board, block 5 : Peripheral I/O port and power terminal

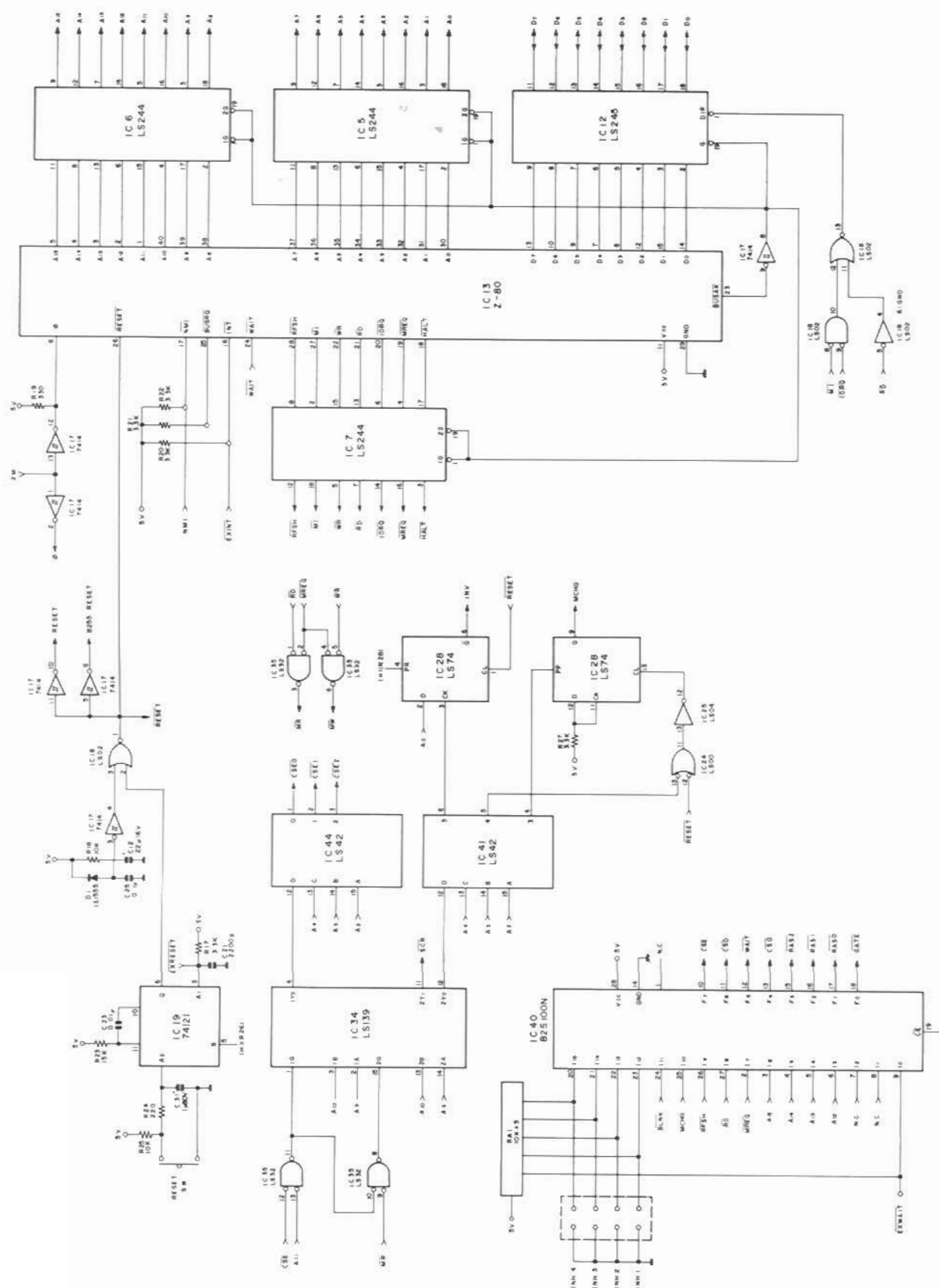


Figure 3.7 CPU board, block 1 : CPU signal system

MZ-80A (1/5)

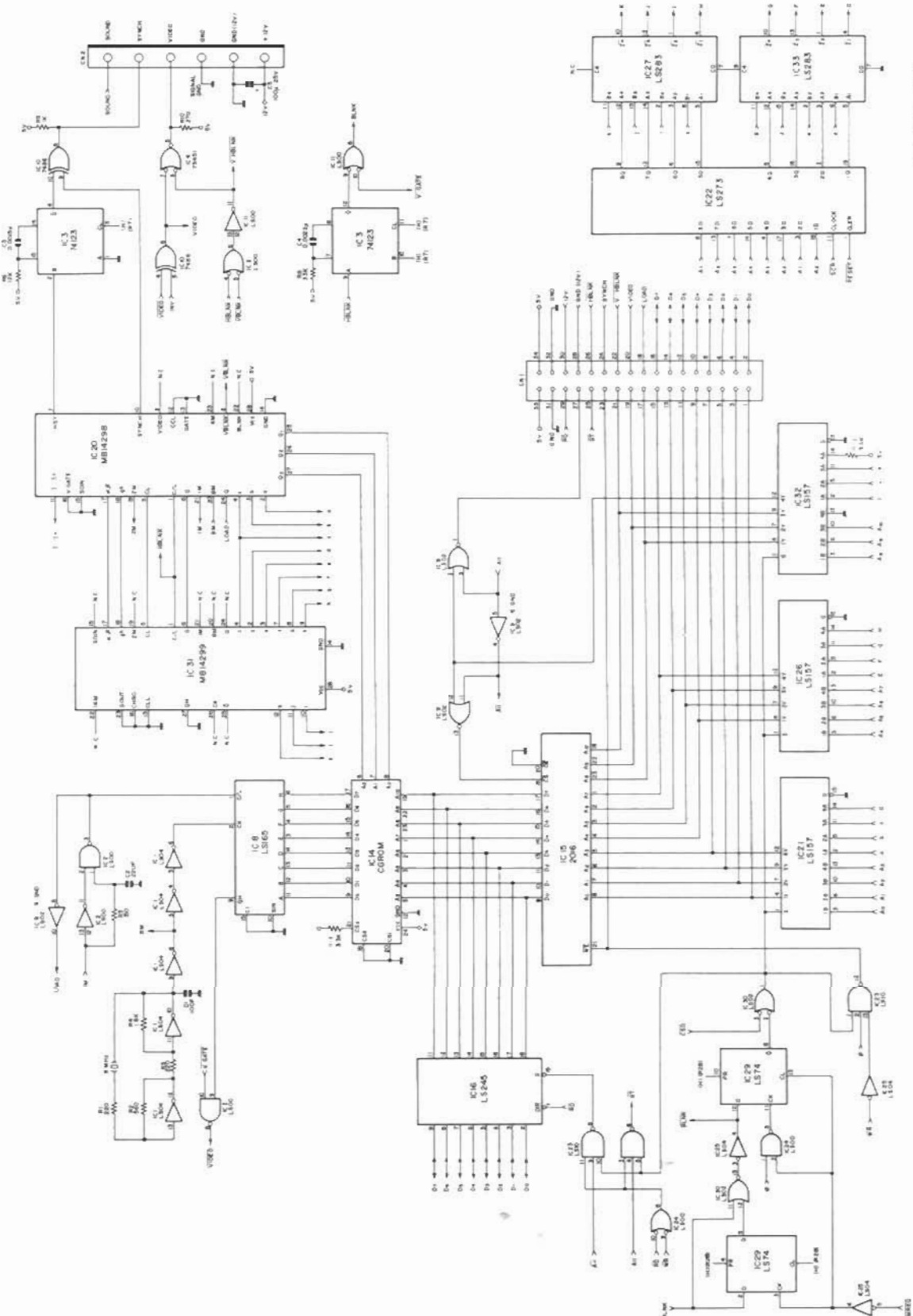


Figure 3.8 CPU board, block 2

MZ-80A (2/5)

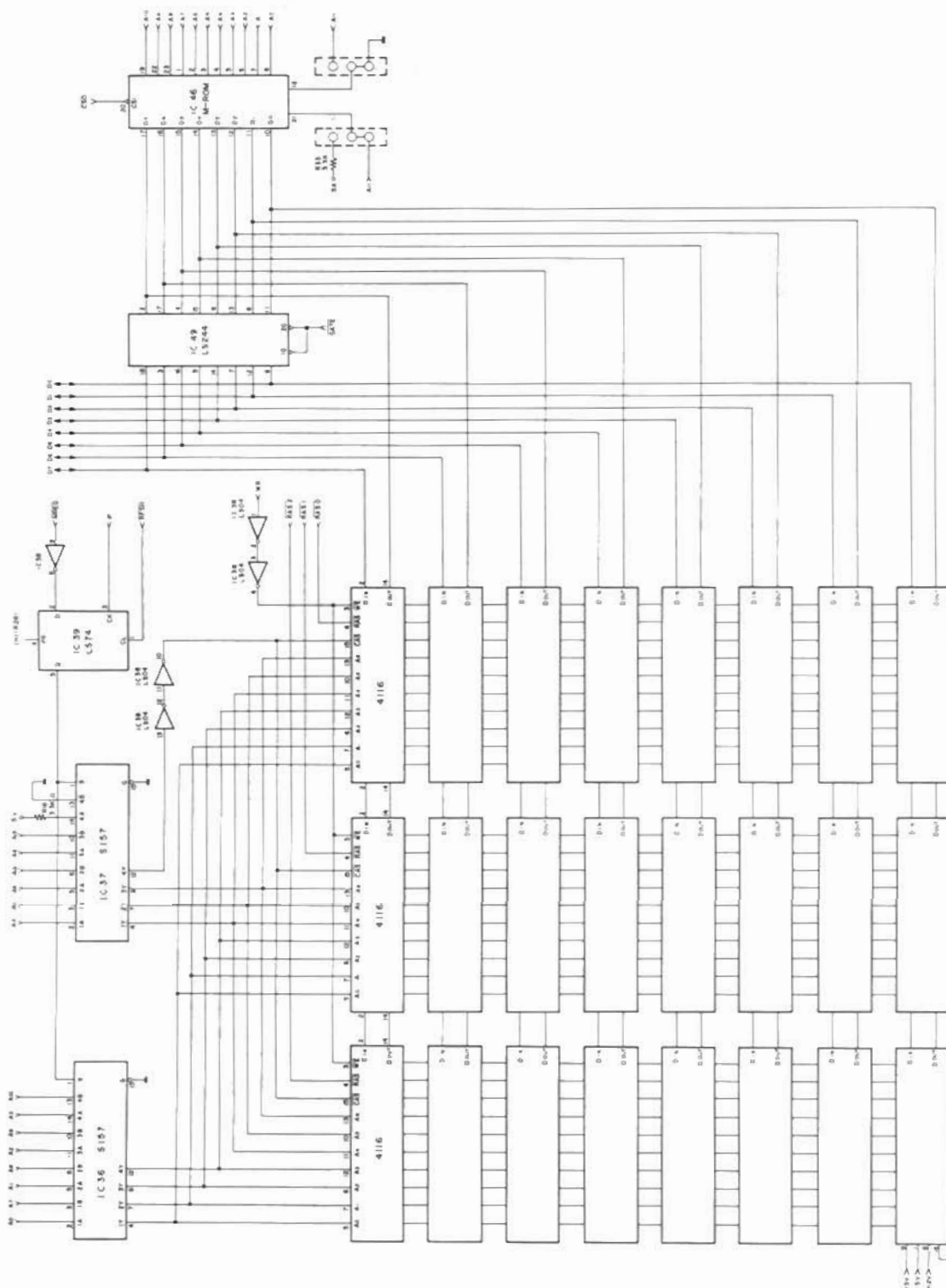


Figure 3.9 CPU board, block 3: RAM signal system

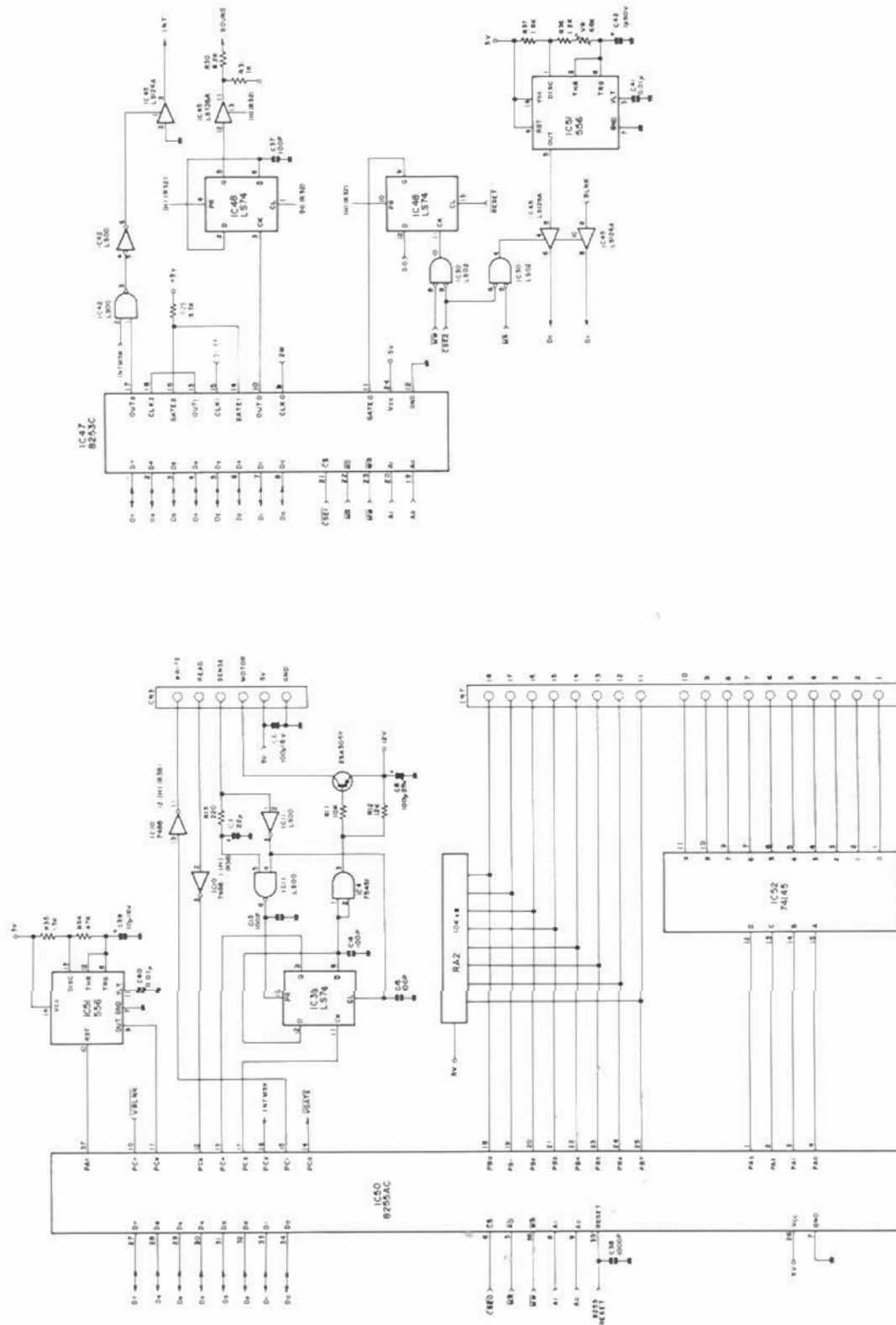
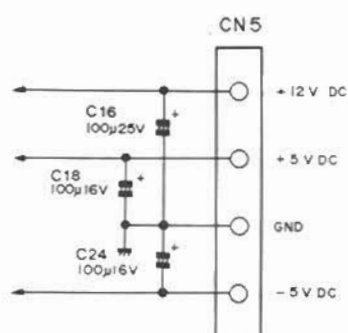


Figure 3.10 CPU board, block 4 : 8255 and 8253 signal system



CN 4

	A	B
1	D1	D0
2	D3	D2
3	GND	GND
4	D5	D4
5	D7	D6
6	GND	A0
7	RESET	A1
8	GND	A2
9	HALT	A3
10	M1	A4
11	GND	A5
12	WR	A6
13	RD	A7
14	GND	A8
15	IOREQ	A9
16	MREQ	A10
17	GND	A11
18	EXINT	A12
19	GND	A13
20	NMI	A14
21	EX WAIT	A15
22	EX RESET	Φ

A : PARTS SIDE

MZ-80A(5/5)

Figure 3.11 CPU board, block 5 : Peripheral I/O port and power terminal



3.3 Expansion equipments

A variety of peripheral devices is available for expanding the MZ-80A personal computer system. Figure 3.12 shows a typical expanded system configuration. With the floppy disk drive, numerous data and program files can be stored and accessed at high speed. With the printer, hard copies of listings and printed graphic patterns can be obtained. This improved processing efficiency, resulting in a wider range of applications.

The MZ-80FB dual floppy disk drive uses a double density mini-floppy diskette (286K bytes/diskette) with a diameter of 5.25 inches, both sides of which are used for recording. It enables use of the DISK BASIC interpreters, which is suitable for practical business applications of the double precision DISK BASIC interpreter, which performs 16 digit BCD operations. Thus, the expanded system exhibits an ability which is comparable with that of larger computers with the aid of a variety of the floppy disk operating system software.



Figure 3.12 Typical expansion system

Figure 3.13 shows peripheral devices which can be connected to the MZ-80A. Devices which are enclosed in a thick solid line are interface cards or RAM blocks and they are connected to the expansion I/O port via interface terminals or connected to the specified connectors in the main cabinet.

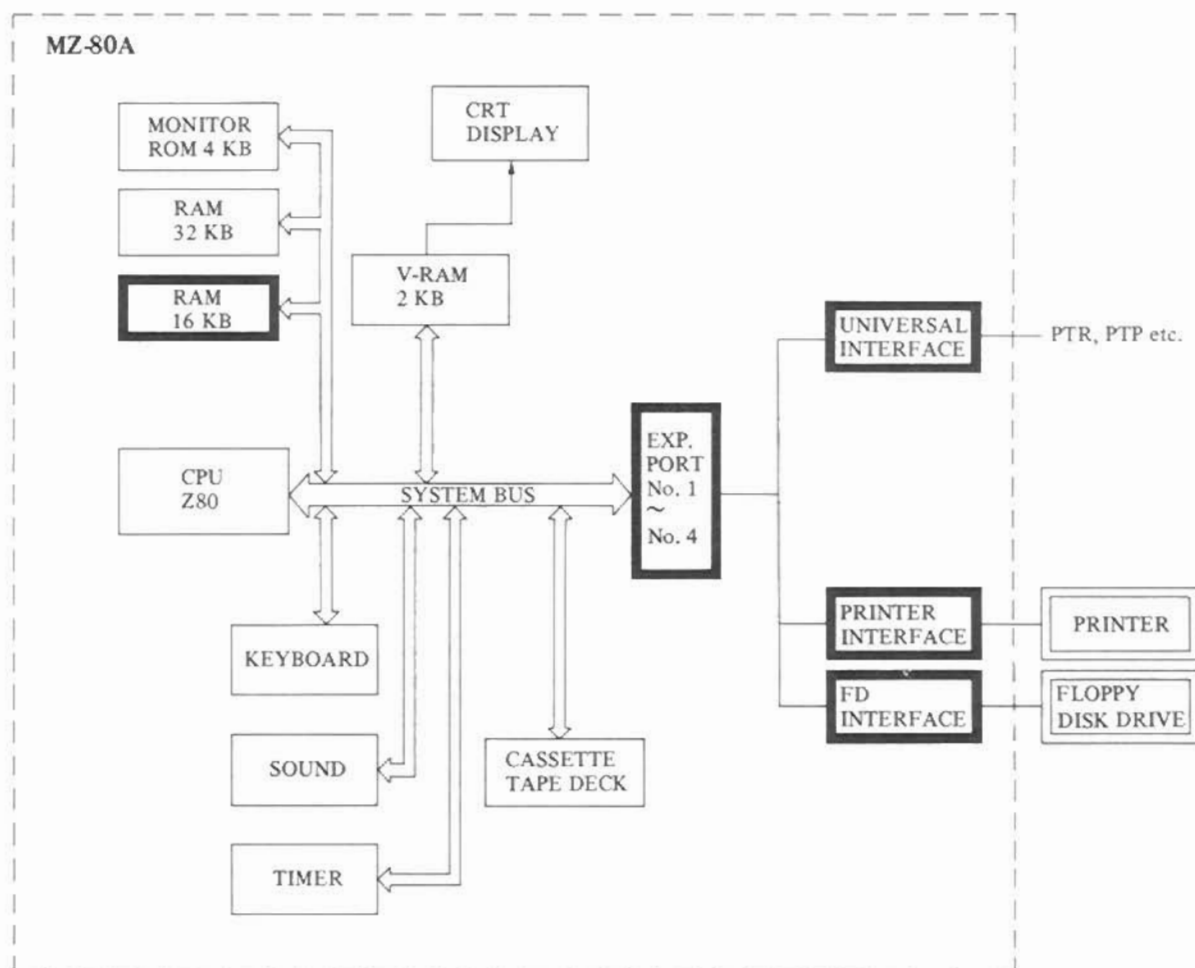


Figure 3.13 MZ-80A system expansion.

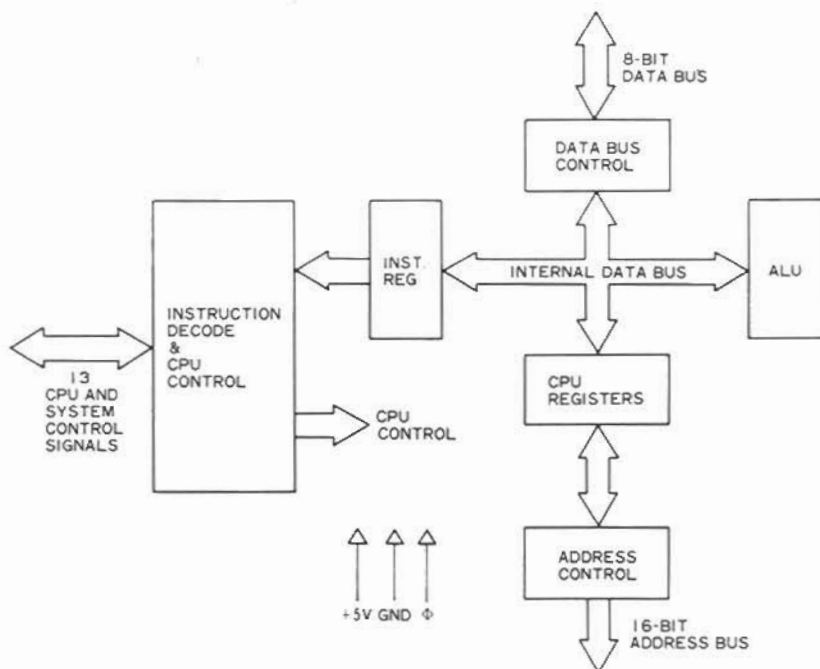
WARNING AND CAUTION

- Warning :** Dangerous voltage is inside of the main cabinet. Leave the installation of optional devices which can be connected in the main cabinet to your dealer.
- Caution :** If the power is turned on with the upper part of the main cabinet lifted, electrical parts may be damaged.
Metal articles remaining in the cabinet can cause serious trouble.
Ensure that no paper clips or other metallic articles fall into cabinet.

3.4 Technical data of Z-80 CPU

1.0 ARCHITECTURE

A block diagram of the internal architecture of the Z-80 CPU is shown in Figure 1.0-1. The diagram shows all of the major elements in the CPU and it should be referred to throughout the following description.



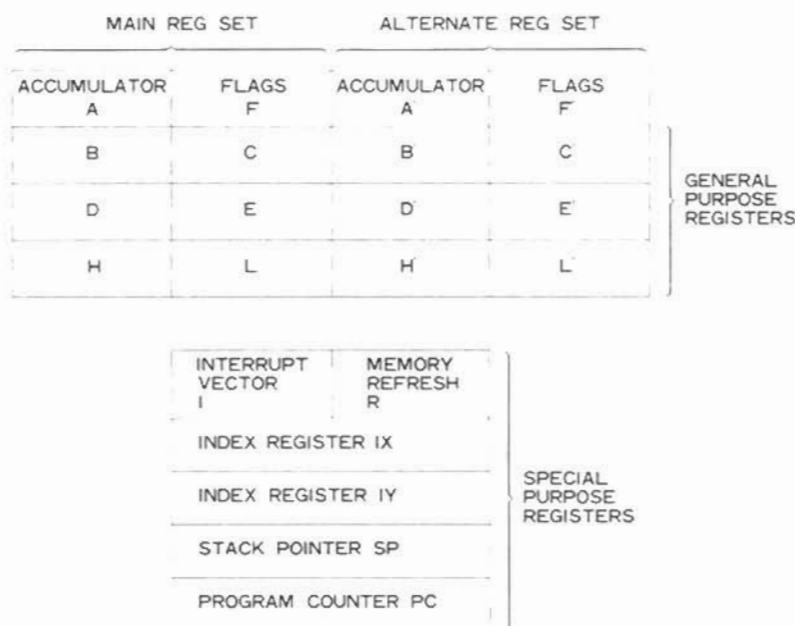
Z-80 CPU BLOCK DIAGRAM
FIGURE 1.0-1

1.1 CPU REGISTERS

The Z-80 CPU contains 208 bits of R/W memory that are accessible to the programmer. Figure 1.0-2 illustrates how this memory is configured into eighteen 8-bit registers and four 16-bit registers. All Z-80 registers are implemented using static RAM. The registers include two sets of six general purpose registers that may be used individually as 8-bit registers or in pairs as 16-bit registers. There are also two sets of accumulator and flag registers.

Special Purpose Registers

1. **Program counter (PC).** The program counter holds the 16-bit address of the current instruction being fetched from memory. The PC is automatically incremented after its contents have been transferred to the address lines. When a program jump occurs the new value is automatically placed in the PC, overriding the incrementer.
2. **Stack Pointer (SP).** The stack pointer holds the 16-bit address of the current top of a stack located anywhere in external system RAM memory. The external stack memory is organized as a last-in first-out (LIFO) file. Data can be pushed onto the stack from specific CPU registers or popped off of the stack into specific CPU registers through the execution of PUSH and POP instructions. The data popped from the stack is always the last data pushed onto it. The stack allows simple implementation of multiple level interrupts, unlimited subroutine nesting and simplification of many types of data manipulation.



Z-80 CPU REGISTER CONFIGURATION
FIGURE 1.0-2

- Two Index Registers (IX & IY).** The two independent index registers hold a 16-bit base address that is used in indexed addressing modes. In this mode, an index register is used as a base to point to a region in memory from which data is to be stored or retrieved. An additional byte is included in indexed instructions to specify a displacement from this base. This displacement is specified as a two's complement signed integer. This mode of addressing greatly simplifies many types of programs, especially where tables of data are used.
- Interrupt Page Address Register (I).** The Z-80 CPU can be operated in a mode where an indirect call to any memory location can be achieved in response to an interrupt. The I Register is used for this purpose to store the high order 8-bits of the indirect address while the interrupting device provides the lower 8-bits of the address. This feature allows interrupt routines to be dynamically located anywhere in memory with absolute minimal access time to the routine.
- Memory Refresh Register (R).** The Z-80 CPU contains a memory refresh counter to enable dynamic memories to be used with the same ease as static memories. Seven bits of this 8-bit register are automatically incremented after each instruction fetch. The eighth bit will remain as programmed as the result of an LD R, A instruction. The data in the refresh counter is sent out on the lower portion of the address bus along with a refresh control signal while the CPU is decoding and executing the fetched instruction. This mode of refresh is totally transparent to the programmer and does not slow down the CPU operation. The programmer can load the R register for testing purposes, but this register is normally not used by the programmer. During refresh, the contents of the I register are placed on the upper 8 bits of the address bus.

Accumulator and Flag Registers

The CPU includes two independent 8-bit accumulators and associated 8-bit flag registers. The accumulator holds the results of 8-bit arithmetic or logical operations while the flag register indicates specific conditions for 8 or 16-bit operations, such as indicating whether or not the result of an operation is equal to zero. The programmer selects the accumulator and flag pair that he wishes to work with a single exchange instruction so that he may easily work with either pair.

General Purpose Registers

There are two matched sets of general purpose registers, each set containing six 8-bit registers that may be used individually as 8-bit registers or as 16-bit register pairs by the programmer. One set is called BC, DE and HL while the complementary set is called BC', DE' and HL'. At any one time the programmer can select either set of registers to work with through a single exchange command for the entire set. In systems where fast interrupt response is required, one set of general purpose registers and an accumulator/flag register may be reserved for handling this very fast routine. Only a simple exchange commands need be executed to go between the routines. This greatly reduces interrupt service time by eliminating the requirement for saving and retrieving register contents in the external stack during interrupt or subroutine processing. These general purpose registers are used for a wide range of applications by the programmer. They also simplify programming, especially in ROM based systems where little external read/write memory is available.

1.2 ARITHMETIC AND LOGIC UNIT (ALU)

The 8-bit arithmetic and logical instructions of the CPU are executed in the ALU. Internally the ALU communicates with the registers and the external data bus on the internal data bus. The type of functions performed by the ALU include:

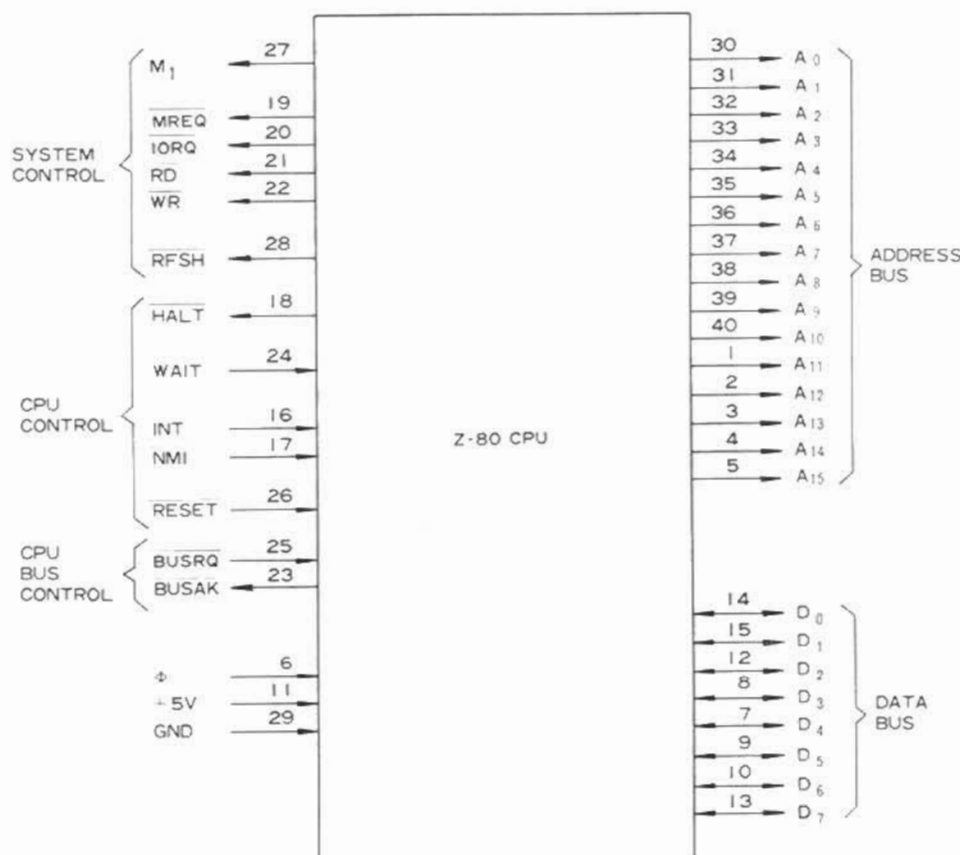
- Add
- Subtract
- Logical AND
- Logical OR
- Logical Exclusive OR
- Compare
- Left or right shifts or rotates (arithmetic and logical)
- Increment
- Decrement
- Set bit
- Reset bit
- Test bit

1.3 INSTRUCTION REGISTER AND CPU CONTROL

As each instruction is fetched from memory, it is placed in the instruction register and decoded. The control section performs this function and then generates and supplies all of the control signals necessary to read or write data from or to the registers, control the ALU and provide all required external control signals.

2.0 PIN DESCRIPTION

The Z-80 CPU is packaged in an industry standard 40 pin Dual In-Line Package. The I/O pins are shown in Figure 2.0-1 and the function of each is described below.



Z-80 PIN CONFIGURATION
FIGURE 2.0-1

A_0 - A_{15}
(Address Bus)

Tri-state output, active high. A_0 - A_{15} constitute a 16-bit address bus. The address bus provides the address for memory (up to 64K bytes) data exchanger and for I/O device data exchanges. I/O addressing uses the 8 lower address bits to allow the user to directly select up to 256 input or 256 output ports. A_0 is the least significant address bit. During refresh time, the lower 7 bits contain a valid refresh address.

D_0 - D_7
(Data Bus)

Tri-state input/output, active high. D_0 - D_7 constitute an 8-bit bidirectional data bus. The data bus is used for data exchanges with memory and I/O devices.

\overline{M}_1
(Machine Cycle one)

Output, active low. \overline{M}_1 indicates that the current machine cycle is the OP code fetch cycle of an instruction execution. Note that during execution of 2-byte op-codes, \overline{M}_1 is generated as each op code byte is fetched. These two byte op-codes always begin with CBH, DDH, EDH or FDH. \overline{M}_1 also occurs with \overline{IORQ} to indicate an interrupt acknowledge cycle.

\overline{MREQ}
(Memory Request)

Tri-state output, active low. The memory request signal indicates that the address bus holds a valid address for a memory read or memory write operation.

$\overline{\text{IORQ}}$ (Input/Output Request)	Tri-state output, active low. The $\overline{\text{IORQ}}$ signal indicates that the lower half of the address bus holds a valid I/O address for a I/O read or write operation. An $\overline{\text{IORQ}}$ signal is also generated with an $\overline{\text{M}_1}$ signal when an interrupt is being acknowledged to indicate that an interrupt response vector can be placed on the data bus. Interrupt Acknowledge operations occur during M_1 time while I/O operations never occur during M_1 time.
$\overline{\text{RD}}$ (Memory Read)	Tri-state output, active low. $\overline{\text{RD}}$ indicates that the CPU wants to read data from memory or an I/O device. The addressed I/O device or memory should use this signal to gate data onto the CPU data bus.
$\overline{\text{WR}}$ (Memory Write)	Tri-state output, active low. $\overline{\text{WR}}$ indicates that the CPU data bus holds valid data to be stored in the addressed memory or I/O device.
$\overline{\text{RFSH}}$ (Refresh)	Output, active low. $\overline{\text{RFSH}}$ indicates that the lower 7 bits of the address bus contain a refresh address for dynamic memories and the current $\overline{\text{MREQ}}$ signal should be used to do a refresh read to all dynamic memories.
$\overline{\text{HALT}}$ (Halt state)	Output, active low. $\overline{\text{HALT}}$ indicates that the CPU has executed a HALT software instruction and is awaiting either a non maskable or a maskable interrupt (with the mask enabled) before operation can resume. While halted, the CPU executes NOP's to maintain memory refresh activity.
$\overline{\text{WAIT}}$ (Wait)	Input, active low. $\overline{\text{WAIT}}$ indicates to the Z-80 CPU that the addressed memory or I/O devices are not ready for a data transfer. The CPU continues to enter wait states for as long as this signal is active. This signal allows memory or I/O devices of any speed to be synchronized to the CPU.
$\overline{\text{INT}}$ (Interrupt Request)	Input, active low. The Interrupt Request signal is generated by I/O devices. A request will be honored at the end of the current instruction if the internal software controlled interrupt enable flip-flop (IFF) is enabled and if the $\overline{\text{BUSRQ}}$ signal is not active. When the CPU accepts the interrupt, an acknowledge signal ($\overline{\text{IORQ}}$ during M_1 time) is sent out at the beginning of the next instruction cycle. The CPU can respond to an interrupt in three different modes.
$\overline{\text{NMI}}$ (Non Maskable Interrupt)	Input, negative edge triggered. The non maskable interrupt request line has a higher priority than $\overline{\text{INT}}$ and is always recognized at the end of the current instruction, independent of the status of the interrupt enable flip-flop. $\overline{\text{NMI}}$ automatically forces the Z-80 CPU to restart to location 0066_{H} . The program counter is automatically saved in the external stack so that the user can return to the program that was interrupted. Note that continuous $\overline{\text{WAIT}}$ cycles can prevent the current instruction from ending, and that a $\overline{\text{BUSRQ}}$ will override a $\overline{\text{NMI}}$.

$\overline{\text{RESET}}$	<p>Input, active low, $\overline{\text{RESET}}$ forces the program counter to zero and initializes the CPU. The CPU initialization includes:</p> <ol style="list-style-type: none">1) Disable the interrupt enable flip-flop2) Set Register I = 00_H3) Set Register R = 00_H4) Set Interrupt Mode 0 <p>During reset time, the address bus and data bus go to a high impedance state and all control output signals go to the inactive state.</p>
$\overline{\text{BUSRQ}}$ (Bus Request)	<p>Input, active low. The bus request signal is used to request the CPU address bus, data bus and tri-state output control signals to go to a high impedance state so that other devices can control these buses. When $\overline{\text{BUSRQ}}$ is activated, the CPU will set these buses to a high impedance state as soon as the current CPU machine cycle is terminated.</p>
$\overline{\text{BUSAK}}$ (Bus Acknowledge)	<p>Output, active low. Bus acknowledge is used to indicate to the requesting device that the CPU address bus, data bus and tri-state control bus signals have been set to their high impedance state and the external device can now control these signals.</p>
Φ	<p>Single phase TTL level clock which requires only a 330 ohm pull-up resistor to +5 volts to meet all clock requirements. (2 MHz)</p>

3.0 INSTRUCTION SET

The Z-80 CPU can execute 158 different instruction types including all 78 of the 8080A CPU. The instructions can be broken down into the following major groups:

- Load and Exchange
- Block Transfer and Search
- Arithmetic and Logical
- Rotate and Shift
- Bit Manipulation (set, reset, test)
- Jump, Call and Return
- Input/Output
- Basic CPU Control

3.1 INTRODUCTION TO INSTRUCTION TYPES

The load instructions move data internally between CPU registers or between CPU registers and external memory. All of these instructions must specify a source location from which the data is to be moved and a destination location. The source location is not altered by a load instruction. Examples of load group instructions include moves between any of the general purpose registers such as move the data to Register B from Register C. This group also includes load immediate to any CPU register or to any to Register B from Register C. This group also includes load immediate to any CPU register or to any external memory location. Other types of load instructions allow transfer between CPU registers and memory locations. The exchange instructions can trade the contents of two registers.

A unique set of block transfer instructions is provided in the Z-80. With a single instruction a block of memory of any size can be moved to any other location in memory. This set of block moves is extremely valuable when large strings of data must be processed. The Z-80 block search instructions are also valuable for this type of processing. With a single instruction, a block of external memory of any desired length can be searched for any 8-bit character. Once the character is found or the end of the block is reached, the instruction automatically terminates. Both the block transfer and the block search instructions can be interrupted during their execution so as to not occupy the CPU for long periods of time.

The arithmetic and logical instructions operate on data stored in the accumulator and other general purpose CPU registers or external memory locations. The results of the operations are placed in the accumulator and the appropriate flags are set according to the result of the operation. An example of an arithmetic operation is adding the accumulator to the contents of an external memory location. The results of the addition are placed in the accumulator. This group also includes 16-bit addition and subtraction between 16-bit CPU registers.

The rotate and shift group allows any register or any memory location to be rotated right or left with or without carry either arithmetic or logical. Also, a digit in the accumulator can be rotated right or left with two digits in any memory location.

The bit manipulation instructions allow any bit in the accumulator, any general purpose register or any external memory location to be set, reset or tested with a single instruction. For example, the most significant bit of register H can be reset. This group is especially useful in control applications and for controlling software flags in general purpose programming.

The jump, call and return instructions are used to transfer between various locations in the user's program. This group uses several different techniques for obtaining the new program counter address from specific external memory locations. A unique type of call is the restart instruction. This instruction actually contains the new address as a part of the 8-bit OP code. This is possible since only 8 separate addresses located in page zero of the external memory may be specified. Program jumps may also be achieved by loading register HL, IX or IY directly into the PC, thus allowing the jump address to be a complex function of the routine being executed.

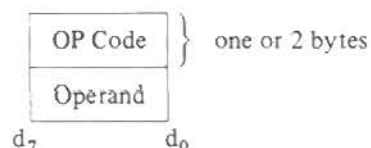
The input/output group of instructions in the Z-80 allow for a wide range of transfers between external memory locations or the general purpose CPU registers, and the external I/O devices. In each case, the port number is provided on the lower 8 bits of the address bus during any I/O transaction. One instruction allows this port number to be specified by the second byte of the instruction while other Z-80 instructions allow it to be specified as the content of the C register. One major advantage of using the C register as a pointer to the I/O device is that it allows different I/O ports to share common software driver routines. This is not possible when the address is part of the OP code if the routines are stored in ROM. Another feature of these input instructions is that they set the flag register automatically so that additional operations are not required to determine the state of the input data (for example its parity). The Z-80 CPU includes single instructions that can move blocks of data (up to 256 bytes) automatically to or from any I/O port directly to any memory location. In conjunction with the dual set of general purpose registers, these instructions provide for fast I/O block transfer rates. The value of this I/O instruction set is demonstrated by the fact that the Z-80 CPU can provide all required floppy disk formatting (i.e., the CPU provides the preamble, address, data and enables the CRC codes) on double density floppy disk drives on an interrupt driven basis.

Finally, the basic CPU control instructions allow various options and modes. This group includes instructions such as setting or resetting the interrupt enable flip flop or setting the mode of interrupt response.

3.2 ADDRESSING MODES

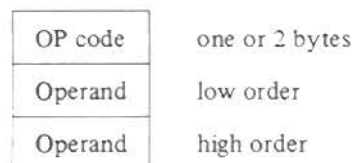
Most of the Z-80 instructions operate on data stored in internal CPU registers, external memory or in the I/O ports. Addressing refers to how the address of this data is generated in each instruction. This section gives a brief summary of the types of addressing used in the Z-80 while subsequent sections detail the type of addressing available for each instruction group.

Immediate. In this mode of addressing the byte following the OP code in memory contains the actual operand.



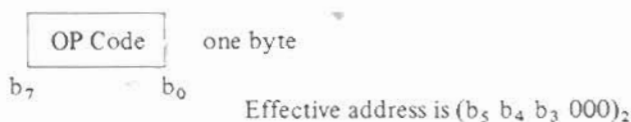
Examples of this type of instruction would be to load the accumulator with a constant, where the constant is the byte immediately following the OP code.

Immediate Extended. This mode is merely an extension of immediate addressing in that the two bytes following the OP codes are the operand.

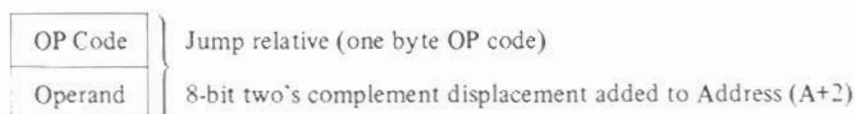


Examples of this type of instruction would be to load the HL register pair (16-bit register) with 16 bits (2 bytes) of data.

Modified Page Zero Addressing. The Z-80 has a special single byte CALL instruction to any of 8 locations in page zero of memory. This instruction (which is referred to as a restart) sets the PC to an effective address in page zero. The value of this instruction is that it allows a single byte to specify a complete 16-bit address where commonly called sub-routines are located, thus saving memory space.

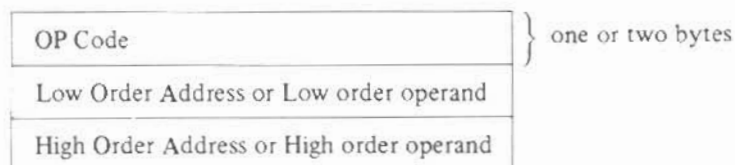


Relative Addressing. Relative addressing uses one byte of data following the OP code to specify a displacement from the existing program to which a program jump can occur. This displacement is a signed two's complement number that is added to the address of the OP code of the following instruction.



The value of relative addressing is that it allows jumps to nearby locations while only requiring two bytes of memory space. For most programs, relative jumps are by far the most prevalent type of jump due to the proximity of related program segments. Thus, these instructions can significantly reduce memory space requirements. The signal displacement can range between +127 and -128 from A+2. This allows for a total displacement of +129 to -126 from the jump relative OP code address. Another major advantage is that it allows for relocatable code.

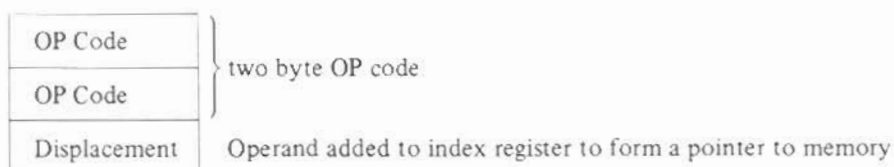
Extended Addressing. Extended Addressing provides for two bytes (16 bits) of address to be included in the instruction. This data can be an address to which a program can jump or it can be an address where an operand is located.



Extended addressing is required for a program to jump from any location in memory to any other location, or load and store data in any memory location.

When extended addressing is used to specify the source or destination address of an operand, the notation (nn) will be used to indicate the content of memory at nn, where nn is the 16-bit address specified in the instruction. This means *that the two bytes of address nn are used as a pointer to a memory location. The use of the parentheses always means that the value enclosed within them is used as a pointer to a memory location.* For example, (1200) refers to the contents of memory at location 1200.

Indexed Addressing. In this type of addressing, the byte of data following the OP code contains a displacement which is added to one of the two index registers (the OP code specifies which index register is used) to form a pointer to memory. The contents of the index register are not altered by this operation.



An example of an indexed instruction would be to load the contents of the memory location (Index Register + Displacement) into the accumulator. The displacement is a signed two's complement number. Indexed addressing greatly simplifies programs using tables of data since the index register can point to the start of any table. Two index registers are provided since very often operations require two or more tables. Indexed addressing also allows for relocatable code.

The two index registers in the Z-80 are referred to as IX and IY. To indicate indexed addressing the notation:

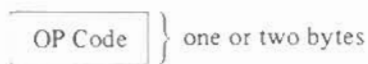
$$(IX + d) \text{ or } (IY + d)$$

is used. Here d is the displacement specified after the OP code. The parentheses indicate that this value is used as a pointer to external memory.

Register Addressing. Many of the Z-80 OP codes contain bits of information that specify which CPU register is to be used for an operation. An example of register addressing would be to load the data in register B into register C.

Implied Addressing. Implied addressing refers to operations where the OP code automatically implies one or more CPU registers as containing the operands. An example is the set of arithmetic operations where the accumulator is always implied to be the destination of the results.

Register Indirect Addressing. This type of addressing specifies a 16-bit CPU register pair (such as HL) to be used as a pointer to any location in memory. This type of instruction is very powerful and it is used in a wide range of applications.



An example of this type of instruction would be to load the accumulator with the data in the memory location pointed to by the HL register contents. Indexed addressing is actually a form of register indirect addressing except that a displacement is added with indexed addressing. Register indirect addressing allows for very powerful but simple to implement memory accesses. The block move and search commands in the Z-80 are extensions of this type of addressing where automatic register incrementing, decrementing and comparing has been added. The notation for indicating register indirect addressing is to put parentheses around the name of the register that is to be used as the pointer. For example, the symbol

(HL)

specifies that the contents of the HL register are to be used as a pointer to a memory location. Often register indirect addressing is used to specify 16-bit operands. In this case, the register contents point to the lower order portion of the operand while the register contents are automatically incremented to obtain the upper portion of the operand.

Bit Addressing. The Z-80 contains a large number of bit set, reset and test instructions. These instructions allow any memory location or CPU register to be specified for a bit operation through one of three previous addressing modes (register, register indirect and indexed) while three bits in the OP code specify which of the eight bits is to be manipulated.

ADDRESSING MODE COMBINATIONS

Many instructions include more than one operand (such as arithmetic instructions or loads). In these cases, two types of addressing may be employed. For example, load can use immediate addressing to specify the source and register indirect or indexed addressing to specify the destination.

3.3 INSTRUCTION OF OP CODES AND EXECUTION TIMES

The following section gives a summary of the Z-80 instructions set. The instructions are logically arranged into groups as shown on tables 3.3-1 through 3.3-11. Each table shows the assembly language mnemonic OP code, the actual OP code, the symbolic operation, the content of the flag register following the execution of each instruction, the number of bytes required for each instruction as well as the number of memory cycles and the total number of T states (external clock periods) required for the fetching and execution of each instruction.

All instruction OP codes are listed in binary notation. Single byte OP codes require two hex characters while double byte OP codes require four hex characters. The conversion from binary to hex is repeated here for convenience.

Binary	Hex	Decimal
0000	0	0
0001	1	1
0010	2	2
0011	3	3
0100	4	4
0101	5	5
0110	6	6
0111	7	7
1000	8	8
1001	9	9
1010	A	10
1011	B	11
1100	C	12
1101	D	13
1110	E	14
1111	F	15

Mnemonic	Symbolic Operation	Flags						OP-Code 76 543 210	No. of Bytes	No. of M Cycles	No. of T States	Comments	
		C	Z	P/V	S	N	H						
LD r, r'	r ← r'	●	●	●	●	●	●	01 r r'	1	1	4	r, r'	Reg.
LD r, n	r ← n	●	●	●	●	●	●	00 r 110 ← n →	2	2	7	000 001	B C
LD r, (HL)	r ← (HL)	●	●	●	●	●	●	01 r 110	1	2	7	010	D
LD r, (IX+d)	r ← (IX+d)	●	●	●	●	●	●	11 011 101 01 r 110 ← d →	3	5	19	011 100 101 111	E H L A
LD r, (IY+d)	r ← (IY+d)	●	●	●	●	●	●	11 111 101 01 r 110 ← d →	3	5	19		
LD (HL), r	(HL) ← r	●	●	●	●	●	●	01 110 r	1	2	7		
LD (IX+d), r	(IX+d) ← r	●	●	●	●	●	●	11 011 101 01 110 r ← d →	3	5	19		
LD (IY+d), r	(IY+d) ← r	●	●	●	●	●	●	11 111 101 01 110 r ← d →	3	5	19		
LD (HL), n	(HL) ← n	●	●	●	●	●	●	00 110 110 ← n →	2	3	10		
LD (IX+d), n	(IX+d) ← n	●	●	●	●	●	●	11 011 101 00 110 110 ← d → ← n →	4	5	19		
LD (IY+d), n	(IY+d) ← n	●	●	●	●	●	●	11 111 101 00 110 110 ← d → ← n →	4	5	19		
LD A, (BC)	A ← (BC)	●	●	●	●	●	●	00 001 010	1	2	7		
LD A, (DE)	A ← (DE)	●	●	●	●	●	●	00 011 010	1	2	7		
LD A, (nn)	A ← (nn)	●	●	●	●	●	●	00 111 010 ← n → ← n →	3	4	13		
LD (BC), A	(BC) ← A	●	●	●	●	●	●	00 000 010	1	2	7		
LD (DE), A	(DE) ← A	●	●	●	●	●	●	00 010 010	1	2	7		
LD (nn), A	(nn) ← A	●	●	●	●	●	●	00 110 010 ← n → ← n →	3	4	13		
LD A, I	A ← I	●	‡	IFF2	‡	0	0	11 101 101 01 010 111	2	2	9		
LD A, R	A ← R	●	‡	IFF2	‡	0	0	11 101 101 01 011 111	2	2	9		
LD I, A	I ← A	●	●	●	●	●	●	11 101 101 01 000 111	2	2	9		
LD R, A	R ← A	●	●	●	●	●	●	11 101 101 01 001 111	2	2	9		

Notes: r, r' means any of the registers A, B, C, D, E, H, L

IFF the content of the interrupt enable flip-flop (IFF) is copied into the P/V flag

Flag Notation: ● = flag not affected, 0 = flag reset, 1 = flag set, X = flag is unknown,
‡ = flag is affected according to the result of the operation.

8-BIT LOAD GROUP
TABLE 3.3-1

Mnemonic	Symbolic Operation	Flags						OP-Code 76 543 210	No. of Bytes	No. of M Cycles	No. of T States	Comments	
		C	Z	P V	S	N	H						
LD dd,nn	dd ← nn	•	•	•	•	•	•	00 dd0 001 ← n → ← n →	3	3	10	dd	Pair
												00	BC
												01	DE
												10	HL
LD IX,nn	IX ← nn	•	•	•	•	•	•	11 011 101 00 100 001 ← n → ← n →	4	4	14	11	SP
LD IY,nn	IY ← nn	•	•	•	•	•	•	11 111 101 00 100 001 ← n → ← n →	4	4	14		
LD HL,(nn)	H ← (nn + 1) L ← (nn)	•	•	•	•	•	•	00 101 010 ← n → ← n →	3	5	16		
LD dd,(nn)	dd _H ← (nn + 1) dd _L ← (nn)	•	•	•	•	•	•	11 101 101 01 dd1 011 ← n → ← n →	4	6	20		
LD IX,(nn)	IX _H ← (nn + 1) IX _L ← (nn)	•	•	•	•	•	•	11 011 101 00 101 010 ← n → ← n →	4	6	20		
LD IY,(nn)	IY _H ← (nn + 1) IY _L ← (nn)	•	•	•	•	•	•	11 111 101 00 101 010 ← n → ← n →	4	6	20		
LD (nn),HL	(nn + 1) ← H (nn) ← L	•	•	•	•	•	•	00 100 010 ← n → ← n →	3	5	16		
LD (nn),dd	(nn + 1) ← dd _H (nn) ← dd _L	•	•	•	•	•	•	11 101 101 01 dd0 011 ← n → ← n →	4	6	20		
LD (nn),IX	(nn + 1) ← IX _H (nn) ← IX _L	•	•	•	•	•	•	11 011 101 00 100 010 ← n → ← n →	4	6	20		
LD (nn),IY	(nn + 1) ← IY _H (nn) ← IY _L	•	•	•	•	•	•	11 111 101 00 100 010 ← n → ← n →	4	6	20		
LD SP,HL	SP ← HL	•	•	•	•	•	•	11 111 001	1	1	6		
LD SP,IX	SP ← IX	•	•	•	•	•	•	11 011 101 11 111 001	2	2	10		
LD SP,IY	SP ← IY	•	•	•	•	•	•	11 111 101 11 111 001	2	2	10		

Mnemonic	Symbolic Operation	Flags						OP-Code			No. of Bytes	No. of M Cycles	No. of T States	Comments	
		C	Z	P/V	S	N	H	76	543	210					
PUSH qq	$(SP-2) \leftarrow qq_L$	●	●	●	●	●	●	11	qq0	101	1	3	11	qq	Pair
	$(SP-1) \leftarrow qq_H$													00	BC
PUSH IX	$(SP-2) \leftarrow IX_L$	●	●	●	●	●	●	11	011	101	2	4	15	01	DE
	$(SP-1) \leftarrow IX_H$							11	100	101				10	HL
PUSH IY	$(SP-2) \leftarrow IY_L$	●	●	●	●	●	●	11	111	101	2	4	15	11	AF
	$(SP-1) \leftarrow IY_H$							11	100	101					
POP qq	$qq_H \leftarrow (SP+1)$	●	●	●	●	●	●	11	qq0	001	1	3	10		
	$qq_L \leftarrow (SP)$														
POP IX	$IX_H \leftarrow (SP+1)$	●	●	●	●	●	●	11	011	101	2	4	14		
	$IX_L \leftarrow (SP)$							11	100	001					
POP IY	$IY_H \leftarrow (SP+1)$	●	●	●	●	●	●	11	111	101	2	4	14		
	$IY_L \leftarrow (SP)$							11	100	001					

Notes: dd is any of the register pairs BC, DE, HL, SP

qq is any of the register pairs AF, BC, DE, HL

$(PAIR)_H$, $(PAIR)_L$ refer to high order and low order eight bits of the register pair respectively.

E.g. $BC_L = C$, $AF_H = A$

Flag Notation: ● = flag not affected, 0 = flag reset, 1 = flag set, X = flag is unknown,

‡ flag is affected according to the result of the operation.

16-BIT LOAD GROUP

TABLE 3.3-2

Mnemonic	Symbolic Operation	Flags						OP-Code			No. of Bytes	No. of M Cycles	No. of T States	Comments
		C	Z	P/V	S	N	H	76	543	210				
EX DE, HL	DE ↔ HL	•	•	•	•	•	•	11	101	011	1	1	4	Register bank and auxiliary register bank exchange
EX AF, AF'	AF ↔ AF'	•	•	•	•	•	•	00	001	000	1	1	4	
EXX	$\begin{pmatrix} BC \\ DE \\ HL \end{pmatrix} \leftrightarrow \begin{pmatrix} BC \\ DE \\ HL \end{pmatrix}$	•	•	•	•	•	•	11	011	001	1	1	4	
EX (SP), HL	H ↔ (SP+1) L ↔ (SP)	•	•	•	•	•	•	11	100	011	1	5	19	
EX (SP), IX	IX _H ↔ (SP+1) IX _L ↔ (SP)	•	•	•	•	•	•	11	011	101	2	6	23	
EX (SP), IY	IY _H ↔ (SP+1) IY _L ↔ (SP)	•	•	•	•	•	•	11	111	101	2	6	23	Load (HL) into (DE), increment the pointers and decrement the byte counter (BC)
LDI	(DE) ← (HL) DE ← DE + 1 HL ← HL + 1 BC ← BC - 1	•	•	•	•	0	0	11	101	101	2	4	16	
LDIR	(DE) ← (HL) DE ← DE + 1 HL ← HL + 1 BC ← BC - 1 Repeat until BC = 0	•	•	0	•	0	0	11	101	101	2	5	21	
								10	110	000	2	4	16	
LDD	(DE) ← (HL) DE ← DE - 1 HL ← HL - 1 BC ← BC - 1	•	•	•	•	0	0	11	101	101	2	4	16	If BC = 0 If BC = 0
LDDR	(DE) ← (HL) DE ← DE - 1 HL ← HL - 1 BC ← BC - 1 Repeat until BC = 0	•	•	0	•	0	0	11	101	101	2	5	21	
								10	111	000	2	4	16	
CPI	A - (HL) HL ← HL + 1 BC ← BC - 1	•	•	•	•	1	•	11	101	101	2	4	16	

Mnemonic	Symbolic Operation	Flags						OP-Code			No. of Bytes	No. of M Cycles	No. of T States	Comments
		C	Z	P/V	S	N	H	76	543	210				
CPIR	A ← (HL)	●	‡	‡	‡	1	‡	11	101	101	2	5	21	If BC ≠ 0 and A = (HL)
	HL ← HL + 1							10	110	001	2	4	16	If BC = 0 or A = (HL)
	BC ← BC - 1													
	Repeat until A = (HL) or BC = 0													
CPD	A ← (HL)	●	‡	‡	‡	1	‡	11	101	101	2	4	16	
	HL ← HL - 1							10	101	001				
	BC ← BC - 1													
CPDR	A ← (HL)	●	‡	‡	‡	1	‡	11	101	101	2	5	21	If BC ≠ 0 and A = (HL)
	HL ← HL - 1							10	111	001	2	4	16	If BC = 0 or A = (HL)
	BC ← BC - 1													
	Repeat until A = (HL) or BC = 0													

Notes: ① P/V flag is 0 if the result of BC-1 = 0, otherwise P/V = 1

② Z flag is 1 if A = (HL), otherwise Z = 0.

Flag Notation: ● = flag not affected, 0 = flag reset, 1 = flag set, X = flag is unknown,
‡ = flag is affected according to the result of the operation.

EXCHANGE GROUP AND BLOCK TRANSFER AND SEARCH GROUP
TABLE 3.3-3

Mnemonic	Symbolic Operation	Flags						OP-Code			No. of Bytes	No. of M Cycles	No. of T States	Comments	
		C	Z	P/V	S	N	H	76	543	210					
ADD A, r	$A \leftarrow A + r$	†	†	V	†	0	†	10	000	r	1	1	4	r	Reg.
ADD A, n	$A \leftarrow A + n$	†	†	V	†	0	†	11	000	110	2	2	7	000	B
									n					001	C
ADD A, (HL)	$A \leftarrow A + (HL)$	†	†	V	†	0	†	10	000	110	1	2	7	010	D
ADD A, (IX + d)	$A \leftarrow A + (IX + d)$	†	†	V	†	0	†	11	011	101	3	5	19	011	E
									000	110				100	H
									d					101	L
ADD A, (IY + d)	$A \leftarrow A + (IY + d)$	†	†	V	†	0	†	11	111	101	3	5	19	111	A
									000	110					
									d						
ADC A, s	$A \leftarrow A + s + CY$	†	†	V	†	0	†		001						
SUB s	$A \leftarrow A - s$	†	†	V	†	1	†		010						
SBC A, s	$A \leftarrow A - s - CY$	†	†	V	†	1	†		011						
AND s	$A \leftarrow A \wedge s$	0	†	P	†	0	1		100						
OR s	$A \leftarrow A \vee s$	0	†	P	†	0	0		110						
XOR s	$A \leftarrow A \oplus s$	0	†	P	†	0	0		101						
CP s	$A - s$	†	†	V	†	1	†		111						
INC r	$r \leftarrow r + 1$	●	†	V	†	0	†	00	r	100	1	1	4		
INC (HL)	$(HL) \leftarrow (HL) + 1$	●	†	V	†	0	†	00	110	100	1	3	11		
INC (IX + d)	$(IX + d) \leftarrow (IX + d) + 1$	●	†	V	†	0	†	11	011	101	3	6	23		
									00	110	100				
									d						
INC (IY + d)	$(IY + d) \leftarrow (IY + d) + 1$	●	†	V	†	0	†	11	111	101	3	6	23		
									00	110	100				
									d						
DEC m	$m \leftarrow m - 1$	●	†	V	†	1	†		101						

Notes: The V symbol in the P/V flag column indicates that the P/V flag contains the overflow of the result of the operation. Similarly the P symbol indicates parity. V = 1 means overflow, V = 0 means not overflow, P = 1 means parity of the result is even, P = 0 means parity of the result is odd.

Flag Notation: ● = flag not affected, 0 = flag reset, 1 = flag set, X = flag is unknown, † = flag is affected according to the result of the operation.

8-BIT ARITHMETIC AND LOGICAL GROUP
TABLE 3.3-4

Mnemonic	Symbolic Operation	Flags						OP-Code 76 543 210	No. of Bytes	No. of M Cycles	No. of T States	Comments
		C	Z	P/V	S	N	H					
DAA	Converts acc content into packed BCD following add or subtract with packed BCD operands	↑	↑	P	↑	●	↑	00 100 111	1	1	4	Decimal adjust accumulator
CPL	$A \leftarrow \bar{A}$	●	●	●	●	1	1	00 101 111	1	1	4	Complement accumulator (one's complement) N
NEG	$A \leftarrow \bar{A} + 1$	↑	↑	V	↑	1	↑	11 101 101 01 000 100	2	2	8	Negate acc. (two's complement)
CCF	$CY \leftarrow \bar{CY}$	↑	●	●	●	0	X	00 111 111	1	1	4	Complement carry flag
SCF	$CY \leftarrow 1$	1	●	●	●	0	0	00 110 111	1	1	4	Set carry flag
NOP	No operation	●	●	●	●	●	●	00 000 000	1	1	4	
	$PC \leftarrow PC + 1$											
HALT	CPU halted	●	●	●	●	●	●	01 110 110	1	1	4	
DI	$IFF \leftarrow 0$	●	●	●	●	●	●	11 110 011	1	1	4	
EI	$IFF \leftarrow 1$	●	●	●	●	●	●	11 111 011	1	1	4	
IM 0	Set interrupt mode 0	●	●	●	●	●	●	11 101 101 01 000 110	2	2	8	
IM 1	Set interrupt mode 1	●	●	●	●	●	●	11 101 101 01 010 110	2	2	8	
IM 2	Set interrupt mode 2	●	●	●	●	●	●	11 101 101 01 011 110	2	2	8	

Notes: IFF indicates the interrupt enable flip-flop
CY indicates the carry flip-flop.

Flag Notation: ● = flag not affected, 0 = flag reset, 1 = flag set, X = flag is unknown,
↑ = flag is affected according to the result of the operation.

GENERAL PURPOSE ARITHMETIC AND CPU CONTROL GROUPS
TABLE 3.3-5

Mnemonic	Symbolic Operation	Flags						OP-Code 76 543 210	No. of Bytes	No. of M Cycles	No. of T States	Comments	
		C	Z	P/V	S	N	H						
ADD HL,ss	HL ← HL + ss	†	●	●	●	0	X	00 ss1 001	1	3	11	ss	Reg.
ADC HL,ss	HL ← HL + ss + CY	†	†	V	†	0	X	11 101 101 01 ss1 010	2	4	15	00 01 10 11	BC DE HL SP
SBC HL,ss	HL ← HL - ss - CY	†	†	V	†	1	X	11 101 101 01 ss0 010	2	4	15	00 01 10 11	BC DE HL SP
ADD IX,pp	IX ← IX + pp	†	●	●	●	0	X	11 011 101 00 pp1 001	2	4	15	pp	Reg.
ADD IY,rr	IY ← IY + rr	†	●	●	●	0	X	11 111 101 00 rr1 001	2	4	15	rr	Reg.
INC ss	ss ← ss + 1	●	●	●	●	●	●	00 ss0 011	1	1	6		
INC IX	IX ← IX + 1	●	●	●	●	●	●	11 011 101 00 100 011	2	2	10		
INC IY	IY ← IY + 1	●	●	●	●	●	●	11 111 101 00 100 011	2	2	10		
DEC ss	ss ← ss - 1	●	●	●	●	●	●	00 ss1 011	1	1	6		
DEC IX	IX ← IX - 1	●	●	●	●	●	●	11 011 101 00 101 011	2	2	10		
DEC IY	IY ← IY - 1	●	●	●	●	●	●	11 111 101 00 101 011	2	2	10		

Notes: ss is any of the register pairs BC, DE, HL, SP
 pp is any of the register pairs BC, DE, IX, SP
 rr is any of the register pairs BC, DE, IY, SP.

Flag Notation: ● = flag not affected, 0 = flag reset, 1 = flag set, X = flag is unknown,
 † = flag is affected according to the result of the operation.

16-BIT ARITHMETIC GROUP
 TABLE 3.3-6

Mnemonic	Symbolic Operation	Flags						OP-Code 76 543 210	No. of Bytes	No. of M Cycles	No. of T States	Comments	
		C	Z	P/V	S	N	H						
RLC A		↓	●	●	●	0	0	00 000 111	1	1	4	Rotate left circular accumulator	
RL A		↓	●	●	●	0	0	00 010 111	1	1	4	Rotate left accumulator	
RRC A		↓	●	●	●	0	0	00 001 111	1	1	4	Rotate right circular accumulator	
RR A		↓	●	●	●	0	0	00 011 111	1	1	4	Rotate right accumulator	
RLC r		↓	↓	P	↓	0	0	11 001 011 00 000 r	2	2	8	Rotate left circular register r	
RLC (HL)		↓	↓	P	↓	0	0	11 001 011 00 000 110	2	4	15	r	Reg.
RLC (IX+d)		↓	↓	P	↓	0	0	11 011 101 11 001 011 ← d → 00 000 110	4	6	23	000	B
RLC (IY+d)		↓	↓	P	↓	0	0	11 111 101 11 001 011 ← d → 00 000 110	4	6	23	001	C
												010	D
												011	E
												100	H
												101	L
												111	A
RL s		↓	↓	P	↓	0	0	010				Instruction format and states are as shown for RLC, m. To form new OP-code replace 000 of RLC, m with shown code.	
RRC s		↓	↓	P	↓	0	0	001					
RR s		↓	↓	P	↓	0	0	011					
SLA s		↓	↓	P	↓	0	0	100					
SRA s		↓	↓	P	↓	0	0	101					
SRL s		↓	↓	P	↓	0	0	111				Rotate digit left and right between the accumulator and location (HL). The content of the upper half of the accumulator is unaffected.	
RLD		●	↓	P	↓	0	0	11 101 101 01 101 111	2	5	18		
RRD		●	↓	P	↓	0	0	11 101 101 01 100 111	2	5	18		

Flag Notation: ● = flag not affected, 0 = flag reset, 1 = flag set, X = flag is unknown,
 ↓ = flag is affected according to the result of the operation.

ROTATE AND SHIFT GROUP
TABLE 3.3-7

Mnemonic	Symbolic Operation	Flags						OP-Code 76 543 210	No. of Bytes	No. of M Cycles	No. of T States	Comments	
		C	Z	P/V	S	N	H						
BIT b,r	$Z \leftarrow \bar{r}_b$	●	‡	X	X	0	1	11 001 011 01 b r	2	2	8	r	Reg.
BIT b,(HL)	$Z \leftarrow \overline{(HL)}_b$	●	‡	X	X	0	1	11 001 011 01 b 110	2	3	12	000 001 010 011 100 101 111	B C D E H L A
BIT b,(IX+d)	$Z \leftarrow \overline{(IX+d)}_b$	●	‡	X	X	0	1	11 011 101 11 001 011 ← d → 01 b 110	4	5	20		
BIT b,(IY+d)	$Z \leftarrow \overline{(IY+d)}_b$	●	‡	X	X	0	1	11 111 101 11 001 011 ← d → 01 b 110	4	5	20	b	Bit Tested
SET b,r	$r_b \leftarrow 1$	●	●	●	●	●	●	11 001 011 11 b r	2	2	8	000 001 010 011 100 101 110 111	0 1 2 3 4 5 6 7
SET b,(HL)	$(HL)_b \leftarrow 1$	●	●	●	●	●	●	11 001 011 11 b 110	2	4	15		
SET b,(IX+d)	$(IX+d)_b \leftarrow 1$	●	●	●	●	●	●	11 011 101 11 001 011 ← d → 11 b 110	4	6	23		
SET b,(IY+d)	$(IY+d)_b \leftarrow 1$	●	●	●	●	●	●	11 111 101 11 001 011 ← d → 11 b 110	4	6	23		
RES b,s	$s_b \leftarrow 0$ $s \equiv r, (HL),$ $(IX+d),$ $(IY+d)$							10				To form new OP-code replace 11 of SET b,m with 10. Flags and time states for SET instruction.	

Notes: The notation s_b indicates bit b (0 to 7) or location s.

Flag Notation: ● = flag not affected, 0 = flag reset, 1 = flag set, X = flag is unknown,
‡ = flag is affected according to the result of the operation.

BIT SET, RESET AND TEST GROUP
TABLE 3.3-8

Mnemonic	Symbolic Operation	Flags						OP-Code	No. of Bytes	No. of M Cycles	No. of T States	Comments	
		C	Z	P/V	S	N	H	76 543 210					
JP nn	PC←nn	●	●	●	●	●	●	11 000 011 ← n → ← n →	3	3	10		
JP cc,nn	If condition cc is true PC←nn, otherwise continue	●	●	●	●	●	●	11 cc 010 ← n → ← n →	3	3	10	cc	Condition
												000	NZnon zero
												001	Z zero
												010	NCnon carry
												011	C carry
												100	PO parity odd
												101	PE parity even
												110	P sign positive
												111	M sign negative
JR e	PC←PC+e	●	●	●	●	●	●	00 011 000 ← e-2 →	2	3	12		
JR C,e	If C=0 continue	●	●	●	●	●	●	00 111 000 ← e-2 →	2	2	7	If condition not met	
	If C=1 PC←PC+e								2	3	12	If condition is met	
JR NC,e	If C=1 continue	●	●	●	●	●	●	00 110 000 ← e-2 →	2	2	7	If condition not met	
	If C=0 PC←PC+e								2	3	12	If condition is met	
JR Z,e	If Z=0 continue	●	●	●	●	●	●	00 101 000 ← e-2 →	2	2	7	If condition not met	
	If Z=1 PC←PC+e								2	3	12	If condition is met	
JR NZ,e	If Z=1 continue	●	●	●	●	●	●	00 100 000 ← e-2 →	2	2	7	If condition not met	
	If Z=0 PC←PC+e								2	3	12	If condition is met	
JP (HL)	PC←HL	●	●	●	●	●	●	11 101 001	1	1	4		
JP (IX)	PC←IX	●	●	●	●	●	●	11 011 101 11 101 001	2	2	8		
JP (IY)	PC←IY	●	●	●	●	●	●	11 111 101 11 101 001	2	2	8		
DJNZ,e	B←B-1 If B=0 continue	●	●	●	●	●	●	00 010 000 ← e-2 →	2	2	8	If B=0	
	If B≠0 PC←PC+e								2	3	13	If B≠0	

Notes: e represents the extension in the relative addressing mode.

e is a signed two's complement number in the range <-126, 129>

e-2 in the op-code provides an effective address of pc + e as PC is incremented by 2 prior to the addition of e.

Flag Notation: ● = flag not affected, 0 = flag reset, 1 = flag set, X = flag is unknown,
‡ = flag is affected according to the result of the operation.

JUMP GROUP
TABLE 3.3-9

Mnemonic	Symbolic Operation	Flags						OP-Code			No. of Bytes	No. of M Cycles	No. of T States	Comments	
		C	Z	P/V	S	N	H	76	543	210					
CALL nn	$(SP-1) \leftarrow PC_H$ $(SP-2) \leftarrow PC_L$ $PC \leftarrow nn$	●	●	●	●	●	●	11	001	101	3	5	17		
CALL cc,nn	If condition cc is false continue, otherwise same as CALL nn	●	●	●	●	●	●	11	cc	100	3	3	10	If cc is false	
								←	n	→	3	5	17	If cc is true	
RET	$PC_L \leftarrow (SP)$ $PC_H \leftarrow (SP+1)$	●	●	●	●	●	●	11	001	001	1	3	10		
RET cc	If condition cc is false continue, otherwise same as RET	●	●	●	●	●	●	11	cc	000	1	1	5	If cc is false	
											1	3	11	If cc is true	
RETI	Return from interrupt	●	●	●	●	●	●	11	101	101	2	4	14	cc	Condition
								01	001	101				000	NZ non zero
RETN	Return from non maskable interrupt	●	●	●	●	●	●	11	101	101	2	4	14	001	Z zero
								01	000	101				010	NC non carry
														011	C carry
														100	PO parity odd
														101	PE parity even
														110	P sign positive
														111	M sign negative
RST p	$(SP-1) \leftarrow PC_H$ $(SP-2) \leftarrow PC_L$ $PC_H \leftarrow 0$ $PC_L \leftarrow P$	●	●	●	●	●	●	11	t	111	1	3	11	t	P
														000	00H
														001	08H
														010	10H
														011	18H
														100	20H
														101	28H
														110	30H
														111	38H

Flag Notation: ● = flag not affected, 0 = flag reset, 1 = flag set, X = flag is unknown
‡ = flag is affected according to the result of the operation.

CALL AND RETURN GROUP
TABLE 3.3-10

Mnemonic	Symbolic Operation	Flags						OP-Code 76 543 210	No. of Bytes	No. of M Cycles	No. of T States	Comments
		C	Z	P/V	S	N	H					
IN A,(n)	A ← (n)	●	●	●	●	●	●	11 011 011 ← n →	2	3	11	n to A ₀ ~A ₇ Acc to A ₈ ~A ₁₅
IN r,(C)	r ← (C) If r=110 only the flags will be affected	●	‡	P	‡	0	0	11 101 101 01 r 000	2	3	12	C to A ₀ ~A ₇ B to A ₈ ~A ₁₅
INI	(HL) ← (C) B ← B-1 HL ← HL+1	●	‡	X	X	1	X	11 101 101 10 100 010	2	4	16	C to A ₀ ~A ₇ B to A ₈ ~A ₁₅
INIR	(HL) ← (C) B ← B-1 HL ← HL+1 Repeat until B=0	●	1	X	X	1	X	11 101 101 10 110 010	2	5 4 4	21 16	C to A ₀ ~A ₇ B to A ₈ ~A ₁₅
IND	(HL) ← (C) B ← B-1 HL ← HL-1	●	‡	X	X	1	X	11 101 101 10 101 010	2	4	16	C to A ₀ ~A ₇ B to A ₈ ~A ₁₅
INDR	(HL) ← (C) B ← B-1 HL ← HL-1 Repeat until B=0	●	1	X	X	1	X	11 101 101 10 111 010	2	5 4 4	21 16	C to A ₀ ~A ₇ B to A ₈ ~A ₁₅
OUT (n),A	(n) ← A	●	●	●	●	●	●	11 010 011	2	3	11	n to A ₀ ~A ₇ Acc to A ₈ ~A ₁₅
OUT (C),r	(C) ← r	●	●	●	●	●	●	11 101 101 01 r 001	2	3	12	C to A ₀ ~A ₇ B to A ₈ ~A ₁₅
OUTI	(C) ← (HL) B ← B-1 HL ← HL+1	●	‡	X	X	1	X	11 101 101 10 100 011	2	4	16	C to A ₀ ~A ₇ B to A ₈ ~A ₁₅
OTIR	(C) ← (HL) B ← B-1 HL ← HL+1 Repeat until B=0	●	1	X	X	1	X	11 101 101 10 110 011	2	5 4 4	21 16	C to A ₀ ~A ₇ B to A ₈ ~A ₁₅
OUTD	(C) ← (HL) B ← B-1 HL ← HL-1	●	‡	X	X	1	X	11 101 101 10 101 011	2	4	16	C to A ₀ ~A ₇ B to A ₈ ~A ₁₅
OTDR	(C) ← (HL) B ← B-1 HL ← HL-1 Repeat until B=0	●	1	X	X	1	X	11 101 101 10 111 011	2	5 4 4	21 16	C to A ₀ ~A ₇ B to A ₈ ~A ₁₅

Notes: ① If the result of B-1 is zero the Z flag is set, otherwise it is reset.

Flag Notation: ● = flag not affected, 0 = flag reset, 1 = flag set, X = flag is unknown,
‡ = flag is affected according to the result of the operation.

INPUT AND OUTPUT GROUP
TABLE 3.3-11

3.4 FLAGS

Each of the two Z-80 CPU Flag registers contains six bits of information which are set or reset by various CPU operations. Four of these bits are testable; that is, they are used as conditions for jump, call or return instructions. For example a jump may be desired only if a specific bit in the flag register is set. The four testable flag bits are:

- 1) **Carry Flag (C)** – This flag is the carry from the highest order bit of the accumulator. For example, the carry flag will be set during an add instruction where a carry from the highest bit of the accumulator is generated. This flag is also set if a borrow is generated during a subtraction instruction. The shift and rotate instructions also affect this bit.
- 2) **Zero Flag (Z)** – This flag is set if the result of the operation loaded a zero into the accumulator. Otherwise it is reset.
- 3) **Sign Flag (S)** – This flag is intended to be used with signed numbers and it is set if the result of the operation was negative. Since bit 7 (MSB) represents the sign of the number (A negative number has a 1 in bit 7), this flag stores the state of bit 7 in the accumulator.
- 4) **Parity/Overflow Flag (P/V)** – This dual purpose flag indicates the parity of the result in the accumulator when logical operations are performed (such as AND A, B) and it represents overflow when signed two's complement arithmetic operations are performed. The Z-80 overflow flag indicates that the two's complement number in the accumulator is in error since it has exceeded the maximum possible (+127) or is less than the minimum possible (–128) number than can be represented in two's complement notation. For example consider adding:

$$\begin{array}{rcl}
 +120 & = & 0111\ 1000 \\
 +105 & = & 0110\ 1001 \\
 \hline
 C & = & 0\ 1110\ 0001 = -31 \text{ (wrong) Overflow has occurred}
 \end{array}$$

Here the result is incorrect. Overflow has occurred and yet there is no carry to indicate an error. For this case the overflow flag would be set. Also consider the addition of two negative numbers:

$$\begin{array}{rcl}
 -5 & = & 1111\ 1011 \\
 -16 & = & 1111\ 0000 \\
 \hline
 C & = & 1\ 1110\ 1011 = -21 \text{ correct}
 \end{array}$$

Notice that the answer is correct but the carry is set so that this flag can not be used as an overflow indicator. In this case the overflow would not be set.

For logical operations (AND, OR, XOR) this flag is set if the parity of the result is even and it is reset if it is odd.

There are also two non-testable bits in the flag register. Both of these are used for BCD arithmetic. They are:

- 1) **Half carry (H)** – This is the BCD carry or borrow result from the least significant four bits of operation. When using the DAA (Decimal Adjust Instruction) this flag is used to correct the result of a previous packed decimal add or subtract.
- 2) **Add/Subtract Flag (N)** – Since the algorithm for correcting BCD operations is different for addition or subtraction, this flag is used to specify what type of instruction was executed last so that the DAA operation will be correct for either addition or subtraction.

The Flag register can be accessed by the programmer and its format is as follows:

S	Z	X	H	X	P/V	N	C
---	---	---	---	---	-----	---	---

X means flag is indeterminate.

Table 3.4-1 lists how each flag bit is affected by various CPU instructions. In this table a '●' indicates that the instruction does not change the flag, an 'X' means that the flag goes to an indeterminate state, a '0' means that it is reset, a '1' means that it is set and the symbol '↑' indicates that it is set or reset according to the previous discussion. Note that any instruction not appearing in this table does not affect any of the flags.

Table 3.4-1 includes a few special cases that must be described for clarity. Notice that the block search instruction sets the Z flag if the last compare operation indicated a match between the source and the accumulator data. Also, the parity flag is set if the byte counter (register pair BC) is not equal to zero. This same use of the parity flag is made with the block move instructions. Another special case is during block input or output instructions, here the Z flag is used to indicate the state of register B which is used as a byte counter. Notice that when the I/O block transfer is complete, the zero flag will be reset to a zero (i.e. B = 0) while in the case of a block move command the parity flag is reset when the operation is complete. A final case is when the refresh or I register is loaded into the accumulator, the interrupt enable flip flop is loaded into the parity flag so that the complete state of the CPU can be saved at any time.

Instruction	C	Z	P/V	S	N	H	Comments
ADD A, s; ADC A, s	↑	↑	V	↑	0	↑	8-bit add or add with carry
SUB s; SBC A, s; CP s; NEG	↑	↑	V	↑	1	↑	8-bit subtract, subtract with carry, compare and negate accumulator
AND s	0	↑	P	↑	0	1	Logical operations And set's different flags
OR s; XOR s	0	↑	P	↑	0	0	
INC s	•	↑	V	↑	0	↑	8-bit increment
DEC m	•	↑	V	↑	1	↑	8-bit decrement
ADD DD, ss	↑	•	•	•	0	X	16-bit add
ADC HL, ss	↑	↑	V	↑	0	X	16-bit add with carry
SBC HL, ss	↑	↑	V	↑	1	X	16-bit subtract with carry
RLA; RLCA; RRA; RRCA;	↑	•	•	•	0	0	Rotate accumulator
RL m; RLC m; RR m; RRC m	↑	↑	P	↑	0	0	Rotate and shift location s
SLA m; SRA m; SRL m							
RLD; RRD	•	↑	P	↑	0	0	Rotate digit left and right
DAA	↑	↑	P	↑	•	↑	Decimal adjust accumulator
CPL	•	•	•	•	1	1	Complement accumulator
SCF	1	•	•	•	0	0	Set carry
CCF	↑	•	•	•	0	X	Complement carry
IN r, (C)	•	↑	P	↑	0	0	Input register indirect
INI; IND; OUTI; OUTD	•	↑	X	X	1	X	Block input and output Z = 0 if B ≠ 0 otherwise Z = 1
INIR; INDR; OTIR; OTDR	•	1	X	X	1	X	
LDI; LDD	•	X	↑	X	0	0	Block transfer instructions P/V = 1 if BC ≠ 0, otherwise P/V = 0
LDIR; LDDR	•	X	0	X	0	0	
CPI; CPIR; CPD; CPDR	•	↑	↑	X	1	X	Block search instructions Z = 1 if A = (HL), otherwise Z = 0 P/V = 1 if BC ≠ 0, otherwise P/V = 0
LD A, I; LD A, R	•	↑	IFF	↑	0	0	The content of the interrupt enable flip-flop (IFF) is copied into the P/V flag
BIT b, s	•	↑	X	X	0	1	The state of bit b of location s is copied into the Z flag
NEG	↑	↑	V	↑	1	↑	Negative accumulator

The following notation is used in this table:

Symbol	Operation
C	Carry/link flag. C = 1 if the operation produced a carry from the MSB of the operand or result.
Z	Zero flag. Z = 1 if the result of the operation is zero.
S	Sign flag. S = 1 if the MSB of the result is one.
P/V	Parity or overflow flag. Parity (P) and overflow (V) share the same flag. Logical operations affect this flag with the parity of the result while arithmetic operations affect this flag with the overflow of the result. If P/V holds parity, P/V = 1 if the result of the operation is even, P/V = 0 if result is odd. If P/V holds overflow, P/V = 1 if the result of the operation produced an overflow.
H	Half-carry flag. H = 1 if the add or subtract operation produced a carry into or borrow from into bit 4 of the accumulator.
N	Add/Subtract flag. N = 1 if the previous operation was a subtract.
	H and N flags are used in conjunction with the decimal adjust instruction (DAA) to properly correct the result into packed BCD format following addition or subtraction using operands with packed BCD format.
↑	The flag is affected according to the result of the operation.
•	The flag is unchanged by the operation.
0	The flag is reset by the operation.
1	The flag is set by the operation.
X	The flag is a "don't care."
V	P/V flag affected according to the overflow result of the operation.
P	P/V flag affected according to the parity result of the operation.
r	Any one of the CPU registers A, B, C, D, E, H, L.
s	Any 8-bit location for all the addressing modes allowed for the particular instruction.
ss	Any 16-bit location for all the addressing modes allowed for that instruction.
ii	Any one of the two index registers IX or IY.
R	Refresh counter.
n	8-bit value in range <0, 255>
nn	16-bit value in range <0, 65535>
m	Any 8-bit location for all the addressing modes allowed for the particular instruction.

SUMMARY OF FLAG OPERATION
TABLE 3.4-1

4.0 INTERRUPT RESPONSE

The purpose of an interrupt is to allow peripheral devices to suspend CPU operation in an orderly manner and force the CPU to start a peripheral service routine. Usually this service routine is involved with the exchange of data, or status and control information, between the CPU and the peripheral. Once the service routine is completed, the CPU returns to the operation from which it was interrupted.

INTERRUPT ENABLE – DISABLE

The Z-80 CPU has two interrupt inputs, a software maskable interrupt and a non maskable interrupt. The non maskable interrupt (NMI) can *not* be disabled by the programmer and it will be accepted whenever a peripheral device requests it. This interrupt is generally reserved for very important functions that must be serviced whenever they occur, such as an impending power failure. The maskable interrupt (INT) can be selectively enable or disabled by the programmer. This allows the programmer to disable the interrupt during periods where his program has timing constraints that do not allow it to be interrupted. In the Z-80 CPU there is an enable flip flop (called IFF) that is set or reset by the programmer using the Enable Interrupt (EI) and Disable Interrupt (DI) instructions. When the IFF is reset, an interrupt can not be accepted by the CPU.

Actually, for purposes that will be subsequently explained, there are two enable flip flops, called IFF₁ and IFF₂.



The state of IFF₁ is used to actually inhibit interrupts while IFF₂ is used as a temporary storage location for IFF₁. The purpose of storing the IFF₁ will be subsequently explained.

A reset to the CPU will force both IFF₁ and IFF₂ to the reset state so that interrupts are disabled. They can then be enabled by an EI instruction at any time by the programmer. When an EI instruction is executed, any pending interrupt request will not be accepted until after the instruction following EI has been executed. This single instruction delay is necessary for cases when the following instruction is a return instruction and interrupts must not be allowed until the return has been completed. The EI instruction sets both IFF₁ and IFF₂ to the enable state. When an interrupt is accepted by the CPU, both IFF₁ and IFF₂ are automatically reset, inhibiting further interrupts until the programmer wishes to issue a new EI instruction. Note that for all of the previous cases, IFF₁ and IFF₂ are always equal.

The purpose of IFF₂ is to save the status of IFF₁ when a non maskable interrupt occurs. When a non maskable interrupt is accepted, IFF₁ is reset to prevent further interrupts until reenabled by the programmer. Thus, after a non maskable interrupt has been accepted, maskable interrupts are disabled but the previous state of IFF₁ has been saved so that the complete state of the CPU just prior to the non maskable interrupt can be restored at any time. When a Load Register A with Register I (LD A, I) instruction or a Load Register A with Register R (LD A, R) instruction is executed, the state of IFF₂ is copied into the parity flag where it can be tested or stored.

A second method of restoring the status of IFF₁ is thru the execution of a Return From Non Maskable Interrupt (RETN) instruction. Since this instruction indicates that the non maskable interrupt service routine is complete, the contents of IFF₂ are now copied back into IFF₁, so that the status of IFF₁ just prior to the acceptance of the non maskable interrupt will be restored automatically.

Figure 4.0-1 is a summary of the effect of different instructions on the two enable flip flops.

Action	IFF ₁	IFF ₂	
CPU Reset	0	0	
DI	0	0	
EI	1	1	
LD A, I	•	•	IFF ₂ → Parity flag
LD A, R	•	•	IFF ₂ → Parity flag
Accept NMI	0	•	
RETN	IFF ₂	•	IFF ₂ → IFF ₁

“•” indicates no change

FIGURE 4.0-1
INTERRUPT ENABLE/DISABLE FLIP FLOPS

CPU RESPONSE

Non Maskable

A nonmaskable interrupt will be accepted at all times by the CPU. When this occurs, the CPU ignores the next instruction that it fetches and instead does a restart to location 0066H. Thus, it behaves exactly as if it had received a restart instruction but, it is to a location that is not one of the 8 software restart locations. A restart is merely a call to a specific address in page 0 of memory.

Maskable

The CPU can be programmed to respond to the maskable interrupt in any one of three possible modes.

Mode 0

This mode is identical to the 8080A interrupt response mode. With this mode, the interrupting device can place *any instruction on the data bus and the CPU will execute it*. Thus, the interrupting device provides the next instruction to be executed instead of the memory. Often this will be a restart instruction since the interrupting device only need supply a single byte instruction. Alternatively, any other instruction such as a 3 byte call to any location in memory could be executed.

The number of clock cycles necessary to execute this instruction is 2 more than the normal number for the instruction. This occurs since the CPU automatically adds 2 wait states to an interrupt response cycle to allow sufficient time to implement an external daisy chain for priority control.

After the application of RESET the CPU will automatically enter interrupt Mode 0.

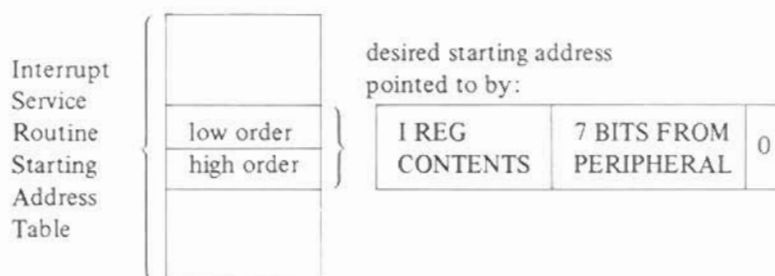
Mode 1

When this mode has been selected by the programmer, the CPU will respond to an interrupt by executing a restart to location 0038H. Thus the response is identical to that for a non maskable interrupt except that the call location is 0038H instead of 0066H. Another difference is that the number of cycles required to complete the restart instruction is 2 more than normal due to the two added wait states.

Mode 2

This mode is the most powerful interrupt response mode. With a single 8 bit byte from the user an indirect call can be made to any memory location.

With this mode the programmer maintains a table of 16 bit starting addresses for every interrupt service routine. This table may be located anywhere in memory. When an interrupt is accepted, a 16 bit pointer must be formed to obtain the desired interrupt service routine starting address from the table. The upper 8 bits of this pointer is formed from the contents of the I register. The I register must have been previously loaded with the desired value by the programmer, i.e. LDI, A. Note that a CPU reset clears the I register so that it is initialized to zero. The lower eight bits of the pointer must be supplied by the interrupting device. Actually, only 7 bits are required from the interrupting device as the least significant bit must be a zero. This is required since the pointer is used to get two adjacent bytes to form a complete 16 bit service routine starting address and the addresses must always start in even locations.



The first byte in the table is the least significant (low order) portion of the address. The programmer must obviously fill this table in with the desired addresses before any interrupts are to be accepted.

Note that this table can be changed at any time by the programmer (if it is stored in Read/Write Memory) to allow different peripherals to be serviced by different service routines.

Once the interrupting device supplies the lower portion of the pointer, the CPU automatically pushes the program counter onto the stack, obtains the starting address from the table and does a jump to this address. This mode of response requires 19 clock periods to complete (7 to fetch the lower 8 bits from the interrupting device, 6 to save the program counter, and 6 to obtain the jump address.)

Appendix

A.1 ASCII Code Table

The following are the ASCII codes for characters:

MSD \ LSD		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
		0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0	0000			SP	O	@	P	☼	☐	}	—	q	n		⌋	—	□
1	0001	↓	!	!	A	Q	H	☐	☐	☐	a	/	☐	☐	♠	●	
2	0010	↑	"	2	B	R	I	☐	☐	e	z	Ü	☐	☐	☐	☐	☐
3	0011	→	#	3	C	S	♠	☐	☐	☐	w	m	☐	☐	☐	☐	♥
4	0100	←	\$	4	D	T	♠	☐	☐	~	s	☐	☐	☐	☐	☐	☐
5	0101	H	%	5	E	U	♠	☐	☐	☐	u	☐	☐	☐	☐	☐	☐
6	0110	©	&	6	F	V	☐	☐	☐	t	i	☐	☐	☐	☐	☐	☐
7	0111		'	7	G	W	☐	☐	☐	g	=	o	☐	☐	☐	☐	☐
8	1000		(8	H	X	☐	☐	☐	h	ö	l	☐	☐	☐	☐	☐
9	1001)	9	I	Y	☐	☐	☐	k	Ä	☐	☐	☐	☐	☐	☐
A	1010		*	:	J	Z	☐	☐	☐	b	f	ö	☐	☐	☐	☐	☐
B	1011		+	;	K	☐	☐	☐	☐	x	v	ä	☐	☐	☐	☐	£
C	1100		,	<	L	☐	☐	☐	☐	d	☐	☐	☐	☐	☐	☐	☐
D	1101	CR	—	=	M	☐	☐	☐	☐	r	ü	y	☐	☐	☐	☐	☐
E	1110		.	>	N	↑	☐	☐	☐	p	β	☐	☐	☐	☐	☐	☐
F	1111		/	?	O	←	☐	☐	☐	c	j	☐	☐	☐	☐	☐	π

A.2 Display Code Table

The following are the display codes of the MZ-80A.

MSD LSD		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
		0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0	0000	SP	P	0	□	}	↑	π	□		p	□	□	↓	□	□	□
1	0001	A	Q	I	□	♠	<	!	□	a	q	□	□	↓	□	□	□
2	0010	B	R	2	□	□	□	"	□	b	r	□	□	↑	□	□	□
3	0011	C	S	3	□	♥	♥	#	□	c	s	□	□	→	□	□	□
4	0100	D	T	4	□	♦	□	\$	□	d	t	□	□	←	□	□	□
5	0101	E	U	5	□	←	@	%	□	e	u	□	□	H	□	□	□
6	0110	F	V	6	□	♣	□	&	□	f	v	□	□	□	□	□	□
7	0111	G	W	7	□	●	>	'	□	g	w	□	□	□	□	□	□
8	1000	H	X	8	□	○	↓	(□	h	x	□	□	H	□	□	□
9	1001	I	Y	9	□	?	□)	□	i	y	□	□	I	□	□	□
A	1010	J	Z	□	□	□	→	+	□	j	z	□	□	□	□	□	□
B	1011	K	£	=	□	□	□	*	□	k	ä	ü	□	□	□	□	□
C	1100	L	□	;	□	□	□	□	□	l	□	ö	□	□	□	□	□
D	1101	M	□	□	□	□	□	□	□	m	□	ü	□	□	□	□	□
E	1110	N	□	□	□	□	□	□	□	n	□	Ä	□	□	□	□	□
F	1111	O	□	□	□	□	□	□	□	o	□	Ö	□	□	□	□	□

A. 3 Mnemonic Codes and Corresponding Object Codes

(Mnemonic codes are arranged in alphabetic order.)

Note

nn, n, d and e in the operands of each mnemonic code represent constant data. The example values set forth below are used for these constants in this table.

nn = 584H

n = 20H

d = 5

e = 30H

Data codes represented by example values are shown in italic and underlined.

OP-Code	Mnemonic
8E	ADC A, (HL)
<u>DD8E05</u>	ADC A, (IX + d)
<u>FD8E05</u>	ADC A, (IY + d)
8F	ADC A, A
88	ADC A, B
89	ADC A, C
8A	ADC A, D
8B	ADC A, E
8C	ADC A, H
8D	ADC A, L
<u>CE20</u>	ADC A, n
ED4A	ADC HL, BC
ED5A	ADC HL, DE
ED6A	ADC HL, HL
ED7A	ADC HL, SP
86	ADD A, (HL)
<u>DD8605</u>	ADD A, (IX + d)
<u>FD8605</u>	ADD A, (IY + d)
87	ADD A, A
80	ADD A, B
81	ADD A, C
82	ADD A, D
83	ADD A, E
84	ADD A, H
85	ADD A, L
<u>C620</u>	ADD A, n
09	ADD HL, BC
19	ADD HL, DE
29	ADD HL, HL
39	ADD HL, SP
DD09	ADD IX, BC
DD19	ADD IX, DE
DD29	ADD IX, IX
DD39	ADD IX, SP
FD09	ADD IY, BC
FD19	ADD IY, DE
FD29	ADD IY, IY
FD39	ADD IY, SP

OP-Code	Mnemonic	OP-Code	Mnemonic
A6	AND (HL)	CB54	BIT 2, H
DDA605	AND (IX + d)	CB55	BIT 2, L
FDA605	AND (IY + d)	CB5E	BIT 3, (HL)
A7	AND A	DDCB05 5E	BIT 3, (IX + d)
A0	AND B	FDCB055E	BIT 3, (IY + d)
A1	AND C	CB5F	BIT 3, A
A2	AND D	CB58	BIT 3, B
A3	AND E	CB59	BIT 3, C
A4	AND H	CB5A	BIT 3, D
A5	AND L	CB5B	BIT 3, E
E620	AND n	CB5C	BIT 3, H
		CB5D	BIT 3, L
CB46	BIT 0, (HL)	CB66	BIT 4, (HL)
DDCB0546	BIT 0, (IX + d)	DDCB05 66	BIT 4, (IX + d)
FDCB0546	BIT 0, (IY + d)	FDCB05 66	BIT 4, (IY + d)
CB47	BIT 0, A	CB67	BIT 4, A
CB40	BIT 0, B	CB60	BIT 4, B
CB41	BIT 0, C	CB61	BIT 4, C
CB42	BIT 0, D	CB62	BIT 4, D
CB43	BIT 0, E	CB63	BIT 4, E
CB44	BIT 0, H	CB64	BIT 4, H
CB45	BIT 0, L	CB65	BIT 4, L
CB4E	BIT 1, (HL)	CB6E	BIT 5, (HL)
DDCB054E	BIT 1, (IX + d)	DDCB05 6E	BIT 5, (IX + d)
FDCB054E	BIT 1, (IY + d)	FDCB05 6E	BIT 5, (IY + d)
CB4F	BIT 1, A	CB6F	BIT 5, A
CB48	BIT 1, B	CB68	BIT 5, B
CB49	BIT 1, C	CB69	BIT 5, C
CB4A	BIT 1, D	CB6A	BIT 5, D
CB4B	BIT 1, E	CB6B	BIT 5, E
CB4C	BIT 1, H	CB6C	BIT 5, H
CB4D	BIT 1, L	CB6D	BIT 5, L
CB56	BIT 2, (HL)	CB76	BIT 6, (HL)
DDCB05 56	BIT 2, (IX + d)	DDCB05 76	BIT 6, (IX + d)
FDCB05 56	BIT 2, (IY + d)	FDCB05 76	BIT 6, (IY + d)
CB57	BIT 2, A	CB77	BIT 6, A
CB50	BIT 2, B	CB70	BIT 6, B
CB51	BIT 2, C	CB71	BIT 6, C
CB52	BIT 2, D	CB72	BIT 6, D
CB53	BIT 2, E	CB73	BIT 6, E

OP-Code	Mnemonic	OP-Code	Mnemonic
CB74	BIT 6,H	EDB1	CPIR
CB75	BIT 6,L		
CB7E	BIT 7,(HL)	2F	CPL
DDCB057E	BIT 7,(IX+d)		
FDCB057E	BIT 7,(IY+d)	27	DAA
CB7F	BIT 7,A		
CB78	BIT 7,B	35	DEC (HL)
CB79	BIT 7,C	DD3505	DEC (IX+d)
CB7A	BIT 7,D	FD3505	DEC (IY+d)
CB7B	BIT 7,E	3D	DEC A
CB7C	BIT 7,H	05	DEC B
CB7D	BIT 7,L	0B	DEC BC
		0D	DEC C
DC8405	CALL C,nn	15	DEC D
FC8405	CALL M,nn	1B	DEC DE
D48405	CALL NC,nn	1D	DEC E
CD8405	CALL nn	25	DEC H
C48405	CALL NZ,nn	2B	DEC HL
F48405	CALL P,nn	DD2B	DEC IX
EC8405	CALL PE,nn	FD2B	DEC IY
E48405	CALL PO,nn	2D	DEC L
CC8405	CALL Z,nn	3B	DEC SP
3F	CCF	F3	DI
BE	CP (HL)	102E	DJNZ e
DDBE05	CP (IX+d)		
FDBE05	CP (IY+d)	FB	EI
BF	CP A		
B8	CP B	E3	EX (SP),HL
B9	CP C	DDE3	EX (SP),IX
BA	CP D	FDE3	EX (SP),IY
BB	CP E	08	EX AF,AF'
BC	CP H	EB	EX DE,HL
BD	CP L	D9	EXX
FE20	CP n		
		76	HALT
EDA9	CPD		
EDB9	CPDR	ED46	IM 0
EDA1	CPI	ED56	IM 1

OP-Code	Mnemonic	OP-Code	Mnemonic
ED5E	IM 2	<u>C28405</u>	JP NZ,nn
		<u>F28405</u>	JP P,nn
ED78	IN A,(C)	<u>EA8405</u>	JP PE,nn
<u>DB20</u>	IN A,(n)	<u>E28405</u>	JP PO,nn
ED40	IN B,(C)	<u>CA8405</u>	JP Z,nn
ED48	IN C,(C)		
ED50	IN D,(C)	<u>382E</u>	JR C,e
ED58	IN E,(C)	<u>182E</u>	JR e
ED60	IN H,(C)	<u>302E</u>	JR NC,e
ED68	IN L,(C)	<u>202E</u>	JR NZ,e
		<u>282E</u>	JR Z,e
34	INC (HL)	02	LD (BC), A
<u>DD3405</u>	INC (IX + d)	12	LD (DE), A
<u>FD3405</u>	INC (IY + d)	77	LD (HL), A
3C	INC A	70	LD (HL), B
04	INC B	71	LD (HL), C
03	INC BC	72	LD (HL), D
0C	INC C	73	LD (HL), E
14	INC D	74	LD (HL), H
13	INC DE	75	LD (HL), L
1C	INC E	<u>3620</u>	LD (HL), n
24	INC H	<u>DD7705</u>	LD (IX + d), A
23	INC HL	<u>DD7005</u>	LD (IX + d), B
<u>DD23</u>	INC IX	<u>DD7105</u>	LD (IX + d), C
<u>FD23</u>	INC IY	<u>DD7205</u>	LD (IX + d), D
2C	INC L	<u>DD7305</u>	LD (IX + d), E
33	INC SP	<u>DD7405</u>	LD (IX + d), H
		<u>DD7505</u>	LD (IX + d), L
EDAA	IND	<u>DD360520</u>	LD (IX + d), n
EDBA	INDR	<u>FD7705</u>	LD (IY + d), A
EDA2	INI	<u>FD7005</u>	LD (IY + d), B
EDB2	INIR	<u>FD7105</u>	LD (IY + d), C
		<u>FD7205</u>	LD (IY + d), D
E9	JP (HL)	<u>FD7305</u>	LD (IY + d), E
DDE9	JP (IX)	<u>FD7405</u>	LD (IY + d), H
FDE9	JP (IY)	<u>FD7505</u>	LD (IY + d), L
<u>DA8405</u>	JP C,nn	<u>FD360520</u>	LD (IY + d), n
<u>FA8405</u>	JP M,nn	<u>328405</u>	LD (nn), A
<u>D28405</u>	JP NC,nn	<u>ED438405</u>	LD (nn), BC
<u>C38405</u>	JP nn		

OP-Code	Mnemonic	OP-Code	Mnemonic
<u>ED538405</u>	LD (nn), DE	4B	LD C, E
<u>228405</u>	LD (nn), HL	4C	LD C, H
<u>DD228405</u>	LD (nn), IX	4D	LD C, L
<u>FD228405</u>	LD (nn), IY	<u>0E20</u>	LD C, n
<u>ED738405</u>	LD (nn), SP	56	LD D, (HL)
0A	LD A, (BC)	<u>DD5605</u>	LD D, (IX + d)
1A	LD A, (DE)	<u>FD5605</u>	LD D, (IY + d)
7E	LD A, (HL)	57	LD D, A
<u>DD7E05</u>	LD A, (IX + d)	50	LD D, B
<u>FD7E05</u>	LD A, (IY + d)	51	LD D, C
<u>3A 8405</u>	LD A, (nn)	52	LD D, D
7F	LD A, A	53	LD D, E
78	LD A, B	54	LD D, H
79	LD A, C	55	LD D, L
7A	LD A, D	<u>1620</u>	LD D, n
7B	LD A, E	<u>ED5B8405</u>	LD DE, (nn)
7C	LD A, H	<u>118405</u>	LD DE, nn
ED57	LD A, I	5E	LD E, (HL)
7D	LD A, L	<u>DD5E05</u>	LD E, (IX + d)
<u>3E20</u>	LD A, n	<u>FD5E05</u>	LD E, (IY + d)
46	LD B, (HL)	5F	LD E, A
<u>DD4605</u>	LD B, (IX + d)	58	LD E, B
<u>FD4605</u>	LD B, (IY + d)	59	LD E, C
47	LD B, A	5A	LD E, D
40	LD B, B	5B	LD E, E
41	LD B, C	5C	LD E, H
42	LD B, D	5D	LD E, L
43	LD B, E	<u>1E20</u>	LD E, n
44	LD B, H	66	LD H, (HL)
45	LD B, L	<u>DD6605</u>	LD H, (IX + d)
<u>0620</u>	LD B, n	<u>FD6605</u>	LD H, (IY + d)
<u>ED4B8405</u>	LD BC, (nn)	67	LD H, A
<u>018405</u>	LD BC, nn	60	LD H, B
4E	LD C, (HL)	61	LD H, C
<u>DD4E05</u>	LD C, (IX + d)	62	LD H, D
<u>FD4E05</u>	LD C, (IY + d)	63	LD H, E
4F	LD C, A	64	LD H, H
48	LD C, B	65	LD H, L
49	LD C, C	<u>2620</u>	LD H, n
4A	LD C, D	<u>2A8405</u>	LD H, (nn)

OP-Code	Mnemonic	OP-Code	Mnemonic
<u>218405</u>	LD HL,nn	B4	OR H
ED47	LD I,A	B5	OR L
<u>DD2A8405</u>	LD IX,(nn)	<u>F620</u>	OR n
<u>DD218405</u>	LD IX,nn	EDBB	OTDR
<u>FD2A8405</u>	LD IY,(nn)	EDB3	OTIR
<u>FD218405</u>	LD IY,nn	ED79	OUT (C),A
6E	LD L,(HL)	ED41	OUT (C),B
<u>DD6E05</u>	LD L,(IX+d)	ED49	OUT (C),C
<u>FD6E05</u>	LD L,(IY+d)	ED51	OUT (C),D
6F	LD L,A	ED59	OUT (C),E
68	LD L,B	ED61	OUT (C),H
69	LD L,C	ED69	OUT (C),L
6A	LD L,D	<u>D320</u>	OUT (n),A
6B	LD L,E	EDAB	OUTD
6C	LD L,H	EDA3	OUTI
6D	LD L,L		
<u>2E20</u>	LD L,n	F1	POP AF
<u>ED7B8405</u>	LD SP,(nn)	C1	POP BC
F9	LD SP,HL	D1	POP DE
DDF9	LD SP,IX	E1	POP HL
FDF9	LD SP,IY	DDE1	POP IX
<u>318405</u>	LD SP,nn	FDE1	POP IY
EDA8	LDD		
EDB8	LDDR	F5	PUSH AF
EDA0	LDI	C5	PUSH BC
EDB0	LDIR	D5	PUSH DE
		E5	PUSH HL
ED44	NEG	DDE5	PUSH IX
		FDE5	PUSH IY
00	NOP		
B6	OR (HL)	CB86	RES 0,(HL)
<u>DDB605</u>	OR (IX+d)	<u>DDCB0586</u>	RES 0,(IX+d)
<u>FDB605</u>	OR (IY+d)	<u>FDCB0586</u>	RES 0,(IY+d)
B7	OR A	CB87	RES 0,A
B0	OR B	CB80	RES 0,B
B1	OR C	CB81	RES 0,C
B2	OR D	CB82	RES 0,D
B3	OR E	CB83	RES 0,E
		CB84	RES 0,H

OP-Code	Mnemonic	OP-Code	Mnemonic
CB85	RES 0, L	CBA5	RES 4, L
CB8E	RES 1, (HL)	CBAE	RES 5, (HL)
DDCB058E	RES 1, (IX + d)	DDCB05 AE	RES 5, (IX + d)
FDCB058E	RES 1, (IY + d)	FDCB05 AE	RES 5, (IY + d)
CB8F	RES 1, A	CBAF	RES 5, A
CB88	RES 1, B	CBA8	RES 5, B
CB89	RES 1, C	CBA9	RES 5, C
CB8A	RES 1, D	CBA A	RES 5, D
CB8B	RES 1, E	CBAB	RES 5, E
CB8C	RES 1, H	CBAC	RES 5, H
CB8D	RES 1, L	CBAD	RES 5, L
CB96	RES 2, (HL)	CBB6	RES 6, (HL)
DDCB05 96	RES 2, (IX + d)	DDCB05 B6	RES 6, (IX + d)
FDCB05 96	RES 2, (IY + d)	FDCB05 B6	RES 6, (IY + d)
CB97	RES 2, A	CBB7	RES 6, A
CB90	RES 2, B	CBB0	RES 6, B
CB91	RES 2, C	CBB1	RES 6, C
CB92	RES 2, D	CBB2	RES 6, D
CB93	RES 2, E	CBB3	RES 6, E
CB94	RES 2, H	CBB4	RES 6, H
CB95	RES 2, L	CBB5	RES 6, L
CB9E	RES 3, (HL)	CBBE	RES 7, (HL)
DDCB05 9E	RES 3, (IX + d)	DDCB05 BE	RES 7, (IX + d)
FDCB05 9E	RES 3, (IY + d)	FDCB05 BE	RES 7, (IY + d)
CB9F	RES 3, A	CBBF	RES 7, A
CB98	RES 3, B	CBB8	RES 7, B
CB99	RES 3, C	CBB9	RES 7, C
CB9A	RES 3, D	CBBA	RES 7, D
CB9B	RES 3, E	CBBB	RES 7, E
CB9C	RES 3, H	CBBC	RES 7, H
CB9D	RES 3, L	CBB D	RES 7, L
CBA6	RES 4, (HL)		
DDCB05 A6	RES 4, (IX + d)	C9	RET
FDCB05 A6	RES 4, (IY + d)	D8	RET C
CBA7	RES 4, A	F8	RET M
CBA0	RES 4, B	D0	RET NC
CBA1	RES 4, C	C0	RET NZ
CBA2	RES 4, D	F0	RET P
CBA3	RES 4, E	E8	RET PE
CBA4	RES 4, H	E0	RET PO

OP-Code	Mnemonic	OP-Code	Mnemonic
C8	RET Z	CB0E	RRC (HL)
ED4D	RETI	DDCB050E	RRC (IX + d)
ED45	RETN	FDCB050E	RRC (IY + d)
CB16	RL (HL)	CB0F	RRC A
DDCB0516	RL (IX + d)	CB08	RRC B
FDCB0516	RL (IY + d)	CB09	RRC C
CB17	RL A	CB0A	RRC D
CB10	RL B	CB0B	RRC E
CB11	RL C	CB0C	RRC H
CB12	RL D	CB0D	RRC L
CB13	RL E	0F	RRCA
CB14	RL H	ED67	RRD
CB15	RL L	C7	RST 00H
17	RLA	CF	RST 08H
CB06	RLC (HL)	D7	RST 10H
DDCB0506	RLC (IX + d)	DF	RST 18H
FDCB0506	RLC (IY + d)	E7	RST 20H
CB07	RLC A	EF	RST 28H
CB00	RLC B	F7	RST 30H
CB01	RLC C	FF	RST 38H
CB02	RLC D	9E	SBC A, (HL)
CB03	RLC E	DD9E05	SBC A, (IX + d)
CB04	RLC H	FD9E05	SBC A, (IY + d)
CB05	RLC L	9F	SBC A, A
07	RLCA	98	SBC A, B
ED6F	RLD	99	SBC A, C
CB1E	RR (HL)	9A	SBC A, D
DDCB051E	RR (IX + d)	9B	SBC A, E
FDCB051E	RR (IY + d)	9C	SBC A, H
CB1F	RR A	9D	SBC A, L
CB18	RR B	DE20	SBC A, n
CB19	RR C	ED42	SBC HL, BC
CB1A	RR D	ED52	SBC HL, DE
CB1B	RR E	ED62	SBC HL, HL
CB1C	RR H	ED72	SBC HL, SP
CB1D	RR L	37	SCF
1F	RRA		

OP-Code	Mnemonic	OP-Code	Mnemonic
CBC6	SET 0, (HL)	CBE6	SET 4, (HL)
DDCB05 C6	SET 0, (IX + d)	DDCB05 E6	SET 4, (IX + d)
FDCB05 C6	SET 0, (IY + d)	FDCB05 E6	SET 4, (IY + d)
CBC7	SET 0, A	CBE7	SET 4, A
CBC0	SET 0, B	CBE0	SET 4, B
CBC1	SET 0, C	CBE1	SET 4, C
CBC2	SET 0, D	CBE2	SET 4, D
CBC3	SET 0, E	CBE3	SET 4, E
CBC4	SET 0, H	CBE4	SET 4, H
CBC5	SET 0, L	CBE5	SET 4, L
CBCE	SET 1, (HL)	CBEE	SET 5, (HL)
DDCB05 CE	SET 1, (IX + d)	DDCB05 EE	SET 5, (IX + d)
FDCB05 CE	SET 1, (IY + d)	FDCB05 EE	SET 5, (IY + d)
CBCF	SET 1, A	CBEF	SET 5, A
CBC8	SET 1, B	CBE8	SET 5, B
CBC9	SET 1, C	CBE9	SET 5, C
CBCA	SET 1, D	CBEA	SET 5, D
CBCB	SET 1, E	CBEB	SET 5, E
CBCC	SET 1, H	CBEC	SET 5, H
CBCD	SET 1, L	CBED	SET 5, L
CBD6	SET 2, (HL)	CBF6	SET 6, (HL)
DDCB05 D6	SET 2, (IX + d)	DDCB05 F6	SET 6, (IX + d)
FDCB05 D6	SET 2, (IY + d)	FDCB05 F6	SET 6, (IY + d)
CBD7	SET 2, A	CBF7	SET 6, A
CBD0	SET 2, B	CBF0	SET 6, B
CBD1	SET 2, C	CBF1	SET 6, C
CBD2	SET 2, D	CBF2	SET 6, D
CBD3	SET 2, E	CBF3	SET 6, E
CBD4	SET 2, H	CBF4	SET 6, H
CBD5	SET 2, L	CBF5	SET 6, L
CBD8	SET 3, B	CBFE	SET 7, (HL)
CBDE	SET 3, (HL)	DDCB05 FE	SET 7, (IX + d)
DDCB05 DE	SET 3, (IX + d)	FDCB05 FE	SET 7, (IY + d)
FDCB05 DE	SET 3, (IY + d)	CBFF	SET 7, A
CBDF	SET 3, A	CBF8	SET 7, B
CBD9	SET 3, C	CBF9	SET 7, C
CBDA	SET 3, D	CBFA	SET 7, D
CBDB	SET 3, E	CBFB	SET 7, E
CBDC	SET 3, H	CBFC	SET 7, H
CBDD	SET 3, L	CBFD	SET 7, L

OP-Code	Mnemonic	OP-Code	Mnemonic
CB26	SLA (HL)	93	SUB E
DDCB05 26	SLA (IX+d)	94	SUB H
FDCB05 26	SLA (IY+d)	95	SUB L
CB27	SLA A	D620	SUB n
CB20	SLA B		
CB21	SLA C	AE	XOR (HL)
CB22	SLA D	DDAE05	XOR (IX+d)
CB23	SLA E	FDAE05	XOR (IY+d)
CB24	SLA H	AF	XOR A
CB25	SLA L	A8	XOR B
		A9	XOR C
CB2E	SRA (HL)	AA	XOR D
DDCB05 2E	SRA (IX+d)	AB	XOR E
FDCB05 2E	SRA (IY+d)	AC	XOR H
CB2F	SRA A	AD	XOR L
CB28	SRA B	EE20	XOR n
CB29	SRA C		
CB2A	SRA D		
CB2B	SRA E		
CB2C	SRA H		
CB2D	SRA L		
CB3E	SRL (HL)		
DDCB05 3E	SRL (IX+d)		
FDCB05 3E	SRL (IY+d)		
CB3F	SRL A		
CB38	SRL B		
CB39	SRL C		
CB3A	SRL D		
CB3B	SRL E		
CB3C	SRL H		
CB3D	SRL L		
96	SUB (HL)		
DD9605	SUB (IX+d)		
FD9605	SUB (IY+d)		
97	SUB A		
90	SUB B		
91	SUB C		
92	SUB D		

A. 4 Specifications

1. MZ-80A GENERAL SPECIFICATIONS

CPU	SHARP LH0080 (Z80-CPU)	Key layout	73 keys ASCII standard main keyboard Numeric pad
Clock	2 MHz		
Memory	ROM 4K bytes (monitor program) ROM 2K bytes (character generator) RAM 32K bytes (dynamic RAM) Can be expanded to 48K bytes. (option; 16K bytes)	Editing function	Cursor control; up, down, left, right, home, clear Deletion, insertion
		Clock function	Built-in
		Power supply	Local supply rating voltage
Display	9" CRT (green display) Character display 8×8 dot matrix Characters; 1000 (40 characters × 25 lines) Pseudo-graphic display 80 × 50 dots	Temperature	Operating temp; 0° to 35°C Storage temp; -15° to 60°C
		Humidity	Lower than 80%
		Weight	Approx. 10 kg
		Dimensions	Width; 440 mm Depth; 480 mm Height; 260 mm
Cassette	Standard audio cassette tape Data transfer speed; 1200 bits/sec. Data transfer system; SHARP PWM		
Sound output	Max. 400 mW (440 Hz)		


2. CPU BOARD SECTION SPECIFICATIONS

CPU	SHARP LH0080 (Z80-CPU) 1 pc.		
ROM	Monitor ROM (4K bytes) 1 pc.	Programmable peripheral interface	8255 1 pc.
	Character generator ROM (2K bytes) 1 pc.		
RAM	Standard; 16K bits dynamic RAM 16 pcs.	Programmable counter	8253 1 pc.
	Optional; 16K bits dynamic RAM 8 pcs.		
	Video RAM (2K bytes) 1 pc.		

3. POWER SUPPLY SECTION SPECIFICATIONS

INPUT	Use a power source with the voltage shown on rating name plate.	OUTPUT	5V, -5V, 12V (stabilizing), 12V (non-stabilizing)
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4. DISPLAY SECTION SPECIFICATIONS

Size	9"	Resolution	Horizontal  *The pattern of the left in the center of the picture must be clear.
Vertical horizontal frequency	60Hz (vertical), 15.75kHz (horizontal)	Non-linearity distortion	Horizontal; $\pm 8\%$ ($\pm 14\%$ max.) Vertical; $\pm 8\%$ ($\pm 12\%$ max.)
Power source	DC 12V, 1.1A $\pm 10\%$	Geometrical distortion	Pincushion dist.; 1% (2% max.) Barrel dist.; 1% (2% max.) Trapezoidal dist.; 1% (2% max.) Parallelogram dist.; 1° (2.5° max.)
Picture tube	E2728B3; 9"90° deflection explosion proof type Heater; 12V, 75mA	High voltage	Zero beam; 11.0kV (10.0kV, min., 12.0kV, max.)
ICs	2 pcs.	Power supply	DC 12.0V, 1.05A (1.2A max.)
Transistors	7 pcs.	Working range	12V $\pm 10\%$
Diodes	13 pcs.	Scan size	Horizontal; 10% (15% max.) Vertical; 10% (15% max.)
Sound output	400mW max. (440 Hz) Speaker 8cm, round dynamic type (32 Ω)	Horizontal lock-in range	$\pm 300\text{Hz}$ ($\pm 100\text{Hz}$ limit)
Control knobs	Volume, V-Hold, Contrast, H-Hold, Brightness, Focus	Vertical lock-in range	-12Hz (-6Hz limit)
Working temperature	-10°C to 50°C	Audio frequency characteristic	440Hz (0dB) -10dB $\pm 4\text{dB}$ at 100Hz -12dB $\pm 4\text{dB}$ at 10kHz
Video output	40Vp-p standard (35Vp-p limit)		

5. CASSETTE TAPE DECK SECTION SPECIFICATIONS

System	PWM recording	Erasing	DC system
Power source	5V $\pm 0.25\text{V}$ (rated)	Playback sensitivity	1m sec. to 500 μ sec. (standard)
Rated amperage	Wait; 2mA Record; 70mA (TEAC test tape) Playback; 7mA (TEAC test tape)	Input level	Below 0.4V ("L") Over 2.0V ("H")
Semiconductors	4 transistors 1 IC 4 diodes	Input impedance	Over 10k Ω (record jack)
Applied tape	From C30 to C60	Output level	Below 0.4V ("L") Over 2.0V ("H")
Tape speed	4.75 cm/sec.	Working temperature	-10°C to 50°C
Track	2-track monaural type	Storage temperature	-25°C to 70°C
Motor	Electronic governor motor (12V)		
Biasing	DC system		

Specifications and design subject to change without prior notice for product improvement. In such cases, items mentioned may be partially different from the product.

A. 5 Caring for the system

- **Power cable**
Don't place heavy objects such as desks or chairs on the power cable and do not damage the covering of the power cable or a severe accident may occur. Be sure to pull the plug (not the cable) when disconnecting the unit from the AC outlet.
- **Line voltage**
The correct line voltage is shown on rating plate. Extremely high or low line voltages may cause trouble or result in incorrect operation. Contact your dealer if such trouble occurs.
- **Ventilation**
Ventilation holes are provided in the cabinet. Never place the unit on a carpet or the like because the holes on the bottom plate of the cabinet will be covered. Place the set in a well ventilated location.
- **Moisture and dust**
Place the unit in a location which is free from moisture and dust.
- **Temperature**
Do not expose the unit to direct sunlight and do not place it near heaters to prevent its temperature from rising.
- **Water and other foreign substances**
Operating the unit when it is wet or contains foreign articles such as clips, pins or other metallic items is dangerous. If water or other liquid enters the unit, immediately pull the power plug and contact your dealer.
- **Shock**
If the unit is subjected to shock the sensitive electronic parts may be damaged.
- **Trouble**
If any trouble occurs, stop operating the unit immediately and contact your dealer.
- **Long periods of disuse**
When the unit is not operated for a long time, be sure to pull the power plug from the AC outlet.
- **Connection of peripheral devices**
When connecting peripheral devices, use only parts and devices designated by the Sharp Corporation. Use of parts and devices other than those designated (or modification of the set) may result in trouble.
- **Stains**
Remove stains from the cabinet with a soft cloth moistened with water or detergent. Never use solvents such as benzine, or discoloration will result.
- **Noise**
When the unit is used in locations where there are high electrical noise levels induced in the AC line, use a line filter to remove the noise. Keep the signal cables away from power cables and other electric equipment.
- **Use and storage**
Do not use or store the unit with the upper cabinet open, or trouble may occur.
- **Radio wave interference**
When a radio or TV set is used near the MZ-80A, noise may interfere with broadcast reception. Equipment causing a strong magnetic field may interfere with operation of the MZ-80A.
Keep such equipment at least 2 to 3 meters away from the MZ-80A.

- **Power switch operation**

Once the power switch is turned off, wait at least 10 seconds before turning it on again. This ensures correct operation of the microprocessor. Never insert the power plug into an AC outlet with the power switch set to ON.

- **Cassette deck maintenance**

Dirty cassette deck recording and reproducing heads may result in incorrect data recording or reproduction. Be sure to clean the heads every month. Commercially available cleaning tape is convenient.

- **Discoloration of CRT screen**

If a certain spot of the CRT screen is lit an extended period of time the spot may become discolored. (If it is necessary for certain spot to be lit for an extended time, turn down the brightness control on the display control unit.)

MODEL: MZ8AM01E

TINSE0038PAZZ
080311-250182

MZ-80A
E2

SHARP CORPORATION